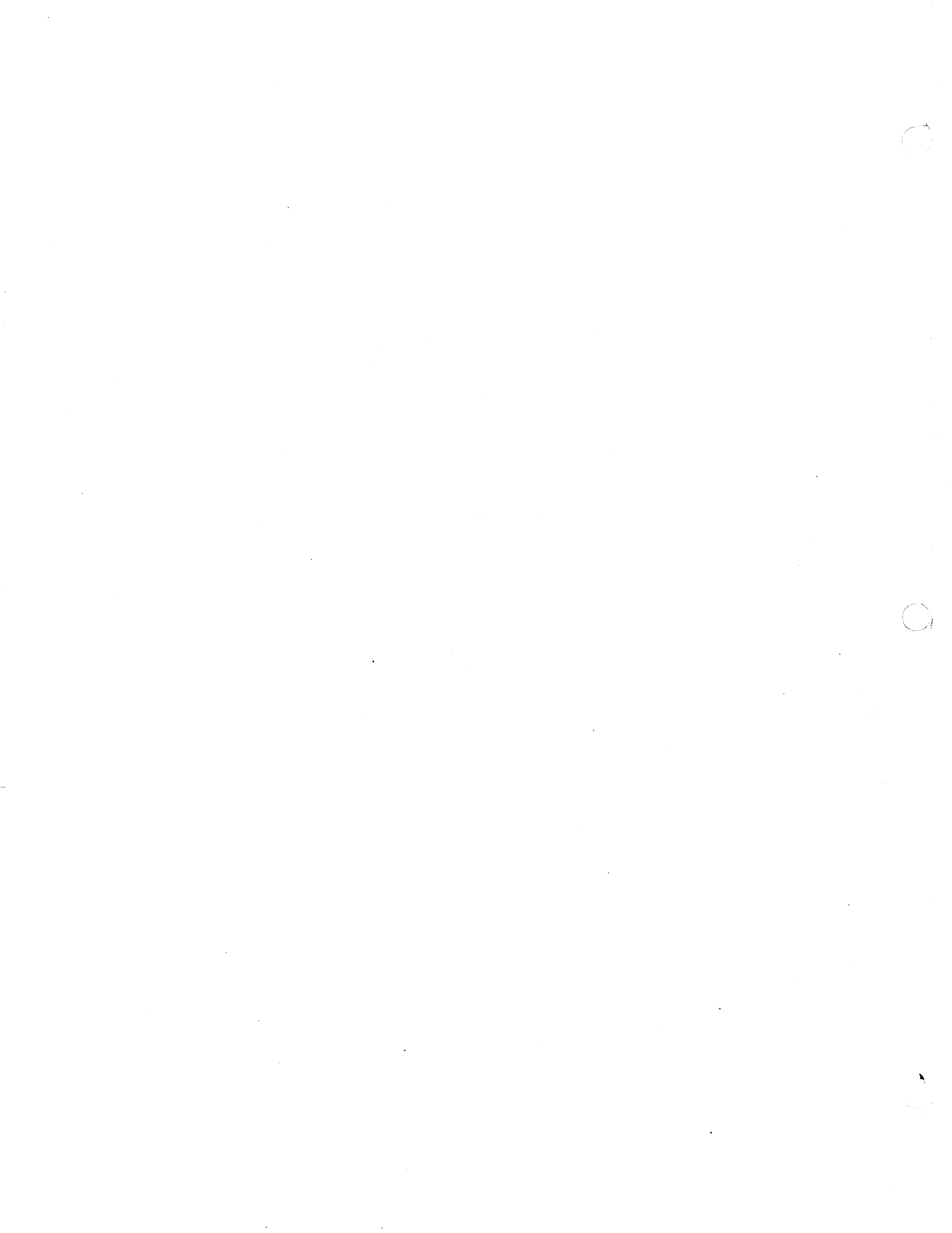


SYSTEM ROM LISTING

Version 1.23

CONTENTS

<u>SECTION</u>	<u>ITEM</u>
A.	MEMORY MAP
B.	LINK MAP
C.	EQUATE FILE INDEX (alphabetical order)
D.	MODULE LISTING INDEX (alphabetical order)
E.	POWER UP SELF-TEST (by function) Entry module (puptst) Memory test Interrupt controller test Timers accuracy test Disks controller test Disk drivers tests CRT tests Option Rom & Ram tests
F.	DEVICE SERVICES ROUTINES (dsr) Beeper dsr Day time clock dsr CRT dsr Disk io dsr Key board dsr Printer dsr Timer dsr Screen display dsr
G.	ROM DATA AREA
H.	BOOT STRAP VECTOR (reset)
I.	EXAMPLES



SECTION A.

SYSTEM ROM ORGANIZATION

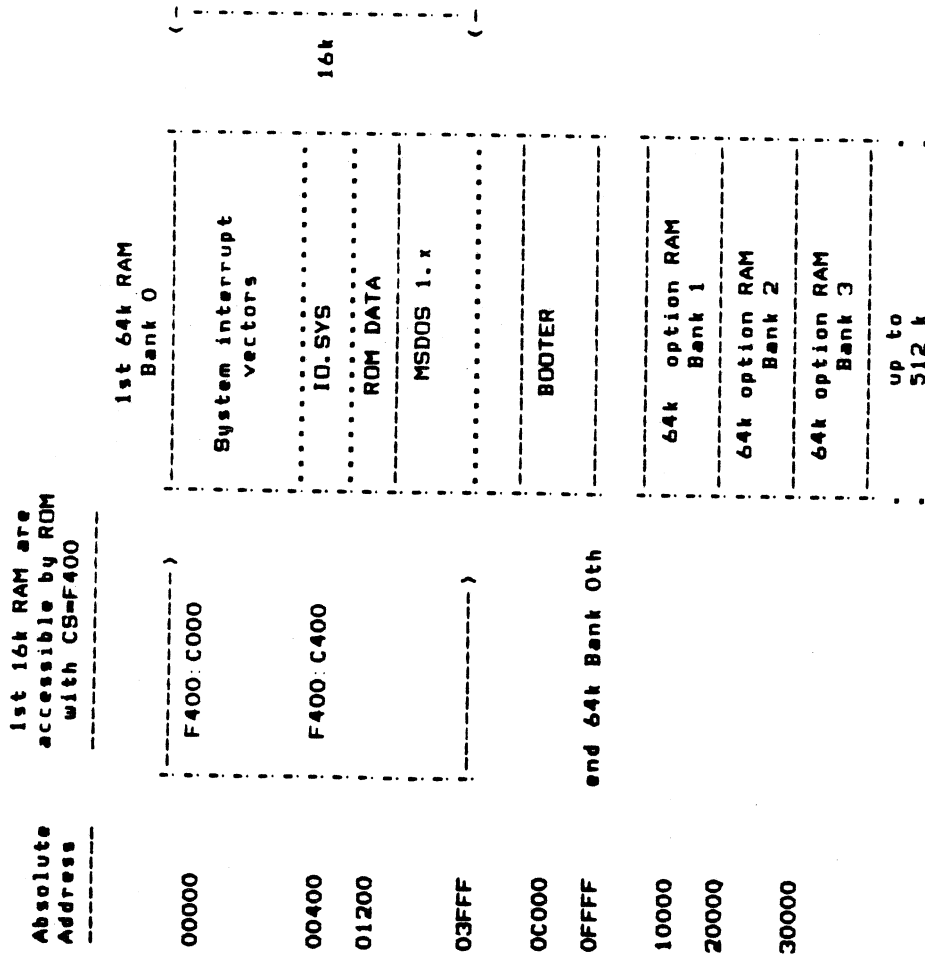
Absolute Address	Relative to CS= F400	OPTION ROMS
F40000	F400:0000	1st 8k option ROM future use
F6000	F400:2000	2nd 8k option ROM
F8000	F400:4000	3rd 8k option ROM
FA000	F400:6000	4th 8k option ROM
FC000	F400:8000	5th 8k option ROM future use
FDFFF		

CURRENT SYSTEM ROM PARTITION

FE000	F400:A000	System Rom HEADER Directory of this Rom is in 'HEADER'
FE03F	F400:A03F	PUPTST Rom code entry point for power up tests..
		ROM BIOS
		RESET
	F400:BFF0	system boot strap vector = 'PUPTST'

The TIPC System memory Organization is defined in the 'SYSORC' module, which must be the first module of the input link control file. This module does not allocate memory but assigns the Rom and Ram symbols.

SYSTEM RAM ORGANIZATION



Note that the location of ROMDAT is initially 40:0000. During the boot process ROMDAT is relocated in memory immediately after the location of RAM BIOS.

To determine the specific location of the ROMDAT (after boot), read the contents of the first word in software interrupt 60 (segment address) (absolute location 180H). The size of ROMDAT is indicated by the second word of software interrupt 60 (absolute location 182H). The contents of this software interrupt is set by the RAM BIOS loader (IO.SYS).

SECTION D.

MODULE LISTING INDEX (alphabetical order)

Module Name	Address CS-F400	Module Description	PAGE
BELDSR	F400:AAFE	Beeper dsr	-
CLKDSR	F400:AB9B	Day time clock dsr	-
CONFIG	F400:BA84	Return system configuration	-
CRTDSR	F400:AC1A	CRT dsr	-
CRTIBL	F400:BF83	CRT initialization tables	-
CRTTST	F400:A636	CRT tests	-
DKBOOT	F400:AA17	Disk booter loader	
DRVTST	F400:A583	Disk drivers tests	
DSKID	F400:80D9	Disk io dsr	
DSKISR	F400:B325	Disk interrupt handler	
DSKTST	F400:A4A4	Floppy disks controller tests	
FLPDIR	F400:B475	Generic disk operation	
HEADER	F400:A000	Rom header information	
INTTST	F400:A190	Interrupt controller tests	
INTXIT	F400:BD42	Common interrupts exit	
KBTABL	F400:BD8E	Key board encoding tables	
KEYDSR	F400:B719	Key board dsr	
KEYTST	F400:ABA1	Key board initialization	
MSCTST	F400:A7DD	Miscellaneous initialization	
OUTPUT	F400:BB73	Screen display service	
PRTDSR	F400:B9F6	printer dsr	
PUPT91	F400:A915	Option Rom & Ram tests	
PUPTBT	F400:A03F	Power up test entry point	
RAMINI	F400:A143	Setup Rom's data in Ram	
RESET	F400:BFF0	Boot strap vector	
ROMDAT	0000:0400	Rom data is Ram resident	
ROMERR	no memory	Error equ module	
ROMTIM	F400:BA9D	Rom timing services	
ROMUTL	F400:BD66	System Rom Utilities	
SYSORQ	no memory	System ROM organization	
TIMISR	F400:BAC1	System timer intr. serv.	
TIMSUB	F400:BB1D	Restart timer service	
TIMTST	F400:A333	Timers accuracy tests	
TSTERR	no memory	Error equ module	
VECINI	F400:A454	Intrpt. vectors initialization	

```
1 ;*****  
2 ; The BIOS routines should be called through software interrupts. *  
3 ; Each module includes a header describing the correct calling *  
4 ; sequence. Applications should not access the BIOS through *  
5 ; absolute addresses. Incompatibility with future TI releases can *  
6 ; result from incorrect use of the BIOS. *  
7 ;*****  
8  
9
```

Missing END statement

10

No errors detected

 * OUTPUT MAP FILE *

Section	Type	Status	Base	Length	Range
.REL.	Rel	Float	0	0	FFFF
ABSO	Rel	Fixed	0	C200	FFFF
ROMDAT	Rel	Fixed	400	140	FFFF
CRMDAT	Rel	Fixed	DE000	1820	FFFF
ROMCOD	Rel	Fixed	F4000	BFDA	FFFF
REBET	Rel	Fixed	FFFF0	10	FFFF

Global	Value	Global	Value	Global	Value
ALTMON	FFFC3	ARCHM	FFC9C	ATMERR	1
ATTERR	2	ATTBAV	439	BEEP	FEB84
BEEP10	FEAFE	BEEPVF	FEB6E	BELEVT	420
BOOTLO	C000	BOOTMV	C000	BOOTSZ	200
CFG#10	FFA6D	CINTER	20	CLK#10	FEB98
CLKBRV	FE85	CRAMER	8	CRT#10	FEC1A
CRTINT	430	CRTRRR	437	CRTHLD	438
CRTINI	FE7BF	CRTTOUT	FEC32	CRTRET	FEC80
CRTTBT	FE636	CURERR	4	CURPOS	42C
CURTYP	432	D#DBGN	462	D\$DEND	4A5
D\$DIT	462	D\$EKNT	482	D\$NCMD	4B3
D\$NSTA	484	D\$P09	485	D\$REG	4BD
D\$SOFT	49E	D\$STAT	49F	D\$WAIT	4A0
D\$WGBP	4A1	DATE	53E	DBCERR	38
DCALL	FF286	DCRERR	32	DDMERR	39
DECLD	FFD71	DELAY	FFD66	DFINIS	FF293
DFMERR	37	DI8BE0	428	DISEND	42A
DKBOOT	FEA47	DKSPSV	4AC	DKSSSV	4AA
DNERR	30	DNRERR	31	DNERR	36
DRVTST	FE983	DSK\$10	FF0D9	DSKC\$M	400
DSKERR	33	DSKEVT	40B	DSKINI	FF299
DSKISP	516	DSKISR	FF325	DSK\$M	402
DSKTST	FE4A4	DSNERR	34	DSPDIE	FFB73
DSPERR	FFC04	DSPERZ	FFBF8	DSPKER	FFBFC
DUNERR	35	E\$CMD	8	E\$EVEN	1
E\$ODD	2	E\$REG	4	END0	462
EPRDM	FC000	FATAL	FFC9A	FATERR	50
FILVEC	FE476	FLIEVT	410	FL2EVT	418
FLTERR	8	FLPDIR	FF475	FLU9H	FF77F
FRCINT	FF49F	OC0NF1	FFA84	FFA7E	FFA7E
HOURS	53D	HUNS	53A	GCONF0	0
INICRT	FE5ED	INILST	FE780	ICNERR	0
INTTBT	FE190	INTXIT	FFD42	INIRET	FFD65
IVIERR	1	IXSPSV	484	INVAL1	FFFF
KB\$ALT	FFEC6	KBCTRL	FFE5E	IXSSSV	4B2
KBMODE	43F	KBNUMK	FFFA9	KBFUNC	FFF2E
				KBSHFT	FFDF6

```

, Utilities
,
, Input OUTPUT ; FF873 Screen display servive
, Input INTXIT ; FFD42 Common interrupts exit
, Input ROMUTL ; FFD66 System Rom Utilities
,
, Tables
, Input KBTABL ; FFD8E Key board encoding tables
, Input CRTTBL ; FFFB3 CRT initialization tables
, Input ROMDAT ; 00400 Rom data is Ram resident
, Input REBET ; FFFF0 Boot strap vector

```

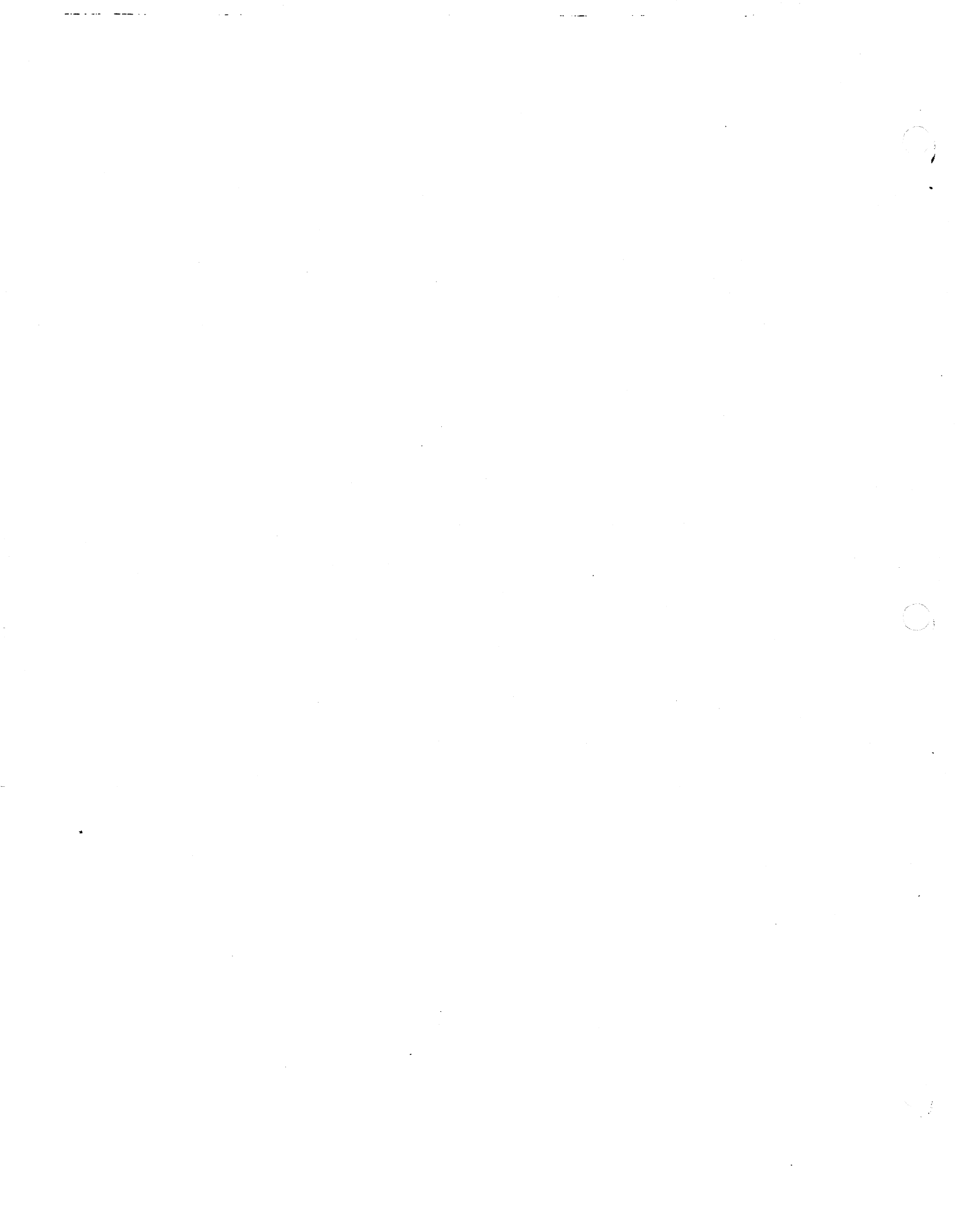
Define the sections

```

,
, BABE AB80-00000
, BABE ROMDAT-400
, BABE CRTDAT-DE000
, BABE ROMCOD-F4000
, BABE REBET-FFFF0

```

KBSPV	4AB	KBSSV	4A6	KBUNSH	FFD8E
KEY\$10	FF719	KEYERR	10	KEYIN	FF741
KEYINI	FF810	KEYISP	4E6	KEYISR	FF857
KEYOUT	FF79C	KEYRST	FF82B	KEYTST	FE8A1
KNIERR	10	KNRERR	11	KPAUSE	FF83A
KRAERR	12	KRCERR	15	KRDERR	13
KUNERR	14	LBECP	FEB7C	LED000	7
LED001	6	LED010	9	LED011	4
LED100	3	LED101	2	LED110	1
LED111	0	MINS	53C	MOTOFF	FF515
MSCT9T	FE7DD	MSG	FFCD4	NUMIERR	2
NMIGO	FFC45	NUMCNT	441	NUMDRV	8
NUMVAL	440	OPRERR	20	PRCOSH	401
PRINT	FFA34	PROMPT	FEA0E	PRT\$10	FF9F6
PRTINI	FFA13	PRTRST	FE28D	PUPNMI	FFC81
PUPTS1	FE915	PUPST	FE03F	PURMPE	9
PUXNME	8	PWRUP	FFFF0	QDEPTH	43E
QFRONT	43A	GREAR	43C	QUEUE	442
RAMINI	FE143	RCONFQ	FFABC	RETFLQ	436
RMDT80	400	RMDTSZ	140	RMPERR	41
ROMERX	28	ROMEVT	406	ROMF4	F4000
ROMF6	F6000	ROMFB	F8000	ROMFA	FA000
ROMS1Z	2000	ROMTIM	FFA9D	ROMTST	FE0FD
ROMVER	FE007	SBEEP	FE880	SECS	53B
SETDAT	FE8B8	SETTIM	FEBBC	SOFINT	FF2EA
STAMON	FFFB3	STATLN	434	STATUS	FF79A
SYSCON	403	SYSDRM	FE000	TOERR	20
TIERR	40	T2ERR	80	THSLIN	42E
TIKCTR	405	TIMCAN	FFB18	TIMERR	4
TIMIS9	53A	TIMISR	FFAC1	TIMOUT	FF2E5
TIMRST	FFB1D	TIMT9T	FE333	TMSPSV	480
TMSSSV	4AE	T8TPAT	FFFD3	VBLERR	40
VECINI	FE454	VECT80	FE3A0	VIDERR	10
VTOLEN	A	WILD**	FFC96	WLDERR	42
XNMERR	40				



ACCBUF	0000	Reserved	ACCFC	0080	Reserved	ACK	0006	Reserved	ACTINT	0054	Reserved	AH	Reserved
AL	Reserved	ATLAT	1800	Reserved	AX	0007	BEL	BELINT	004B	Reserved	DI	Reserved	
BH	Reserved	BL	Reserved	Reserved	BLNLEN	005C	BLOCK	BLOCRC	01FE	Reserved	DI	Reserved	
BOOTID	01FC	BP	Reserved	Reserved	BRKINT	0003	BS	BUFFDC	00C0	Reserved	DI	Reserved	
BX	Reserved	BYTE	Reserved	Reserved	CAN	001B	CANINT	CDSPEN	0040	Reserved	DI	Reserved	
CH	Reserved	CINTEN	0080	Reserved	CINTIM	1500	CL	CLKINT	004E	Reserved	DI	Reserved	
CMPINT	0057	COMPLT	0055	Reserved	CININT	004F	CR	CRCRR	000B	Reserved	DI	Reserved	
CRTACB	0010	CRTADR	1810	Reserved	CRTGCR	0014	CRTCTX	CRTGDB	0017	Reserved	DI	Reserved	
CRTINT	0049	CRTLPP	0004	Reserved	CRTMOD	0000	CRTOFF	CRTPTS	0018	Reserved	DI	Reserved	
CRTRAC	0008	CRTRCP	0003	Reserved	CRTRPX	000D	CRTRVB	CRTSAT	0016	Reserved	DI	Reserved	
CRTSCA	0012	CRTSCP	0002	Reserved	CRTSTY	0001	CRTSDN	CRTSDP	0005	Reserved	DI	Reserved	
CRTSGP	0008	CRTSTA	1811	Reserved	CRTSTY	0015	CRTSBL	CRTVBL	0020	Reserved	DI	Reserved	
CRTWAC	0009	CRTWCB	0011	Reserved	CRTWCH	000A	CRTWXP	CRTWTY	000E	Reserved	DI	Reserved	
CB	Reserved	CSWRAP	C000	Reserved	CUREND	000B	CURPSH	CURPSL	000F	Reserved	DI	Reserved	
CURSTR	000A	CX	Reserved	Reserved	DATERR	0004	DC1	DC2	0012	Reserved	DI	Reserved	
DC3	0013	DC4	0014	Reserved	DEL	007F	DH	DI	0008	Reserved	DI	Reserved	
DISSTH	000C	DISSTL	000D	Reserved	DITCYL	0007	DITDIR	DITDSK	000B	Reserved	DI	Reserved	
DITERR	0009	DITINV	0000	Reserved	DITLEN	000B	DITPRC	DITBEC	0004	Reserved	DI	Reserved	
DITTRK	0006	DKABRT	000D	Reserved	DKABTD	0011	DKBADC	DKBADD	000F	Reserved	DI	Reserved	
DKBOUN	0009	DKCMDE	0001	Reserved	DKCRCE	0010	DKDMAE	DKFAIL	0020	Reserved	DI	Reserved	
DKFME	0002	DKFSET	000B	Reserved	DKFBTA	000E	DKMMOT	DKNDRM	0000	Reserved	DI	Reserved	
DKNRDY	0080	DKRDIT	000A	Reserved	DKREAD	0002	DKRNFE	DKRSET	0000	Reserved	DI	Reserved	
DKSEEK	0040	DKSETC	000C	Reserved	DKSSTA	0007	DKSTAT	DKVERF	0004	Reserved	DI	Reserved	
DKVFYE	0005	DKVRFY	0006	Reserved	DKWPRT	0003	DKWRIT	DKXSET	0009	Reserved	DI	Reserved	
DL	Reserved	DLE	0010	Reserved	DRSELO	000E	DRSEL1	DRSEL2	000B	Reserved	DI	Reserved	
DRSEL3	0007	DRVBYT	019B	Reserved	DRVMSK	000F	DRVSSD	DRV48T	0002	Reserved	DI	Reserved	
DROMSK	0001	DR1MSK	0002	Reserved	DR2MSK	0004	DR3MSK	DS	0190	Reserved	DI	Reserved	
DSADDR	0180	DSADDO	0184	Reserved	DSADD2	018B	DSADD4	DSADD6	0190	Reserved	DI	Reserved	
DSADD8	0194	DSKCS\$P	0000	Reserved	DSKINT	004D	DSK\$P	DSSIZR	0182	Reserved	DI	Reserved	
D\$ADD0	0186	D\$91Z2	018A	Reserved	D\$91Z6	018E	D\$91Z6	DS81Z8	0196	Reserved	DI	Reserved	
DWORD	Reserved	DX	Reserved	Reserved	EM	0019	ENAB0	ENAB1	00FD	Reserved	DI	Reserved	
ENAB2	00FB	ENAB3	00F7	Reserved	ENAB4	00EF	ENAB5	ENAB6	00BF	Reserved	DI	Reserved	
ENAB7	007F	ENG	0005	Reserved	EDI	0020	EOT	EB	0006	Reserved	DI	Reserved	
ESC\$	0018	ETB	0017	Reserved	ETX	0003	EVTACK	EVTEXP	0006	Reserved	DI	Reserved	
EVTSTA	0007	EVTYP	0004	Reserved	E\$ABTD	0011	E\$BADC	E\$BCYL	0004	Reserved	DI	Reserved	
E\$BOUN	0009	E\$BSEC	0004	Reserved	E\$BTRK	0004	E\$CRC	E\$NDRM	0000	Reserved	DI	Reserved	
E\$NRDY	0080	E\$RNF	0004	Reserved	E\$SEEK	0040	E\$TIME	E\$VRFY	0005	Reserved	DI	Reserved	
E\$WRPT	0003	E12MSK	0010	Reserved	E34MSK	0020	E56MSK	FAR	0023	Reserved	DI	Reserved	
FATINT	0050	FDC	0020	Reserved	FDCBUF	0040	FDCCMD	FDCDAT	0023	Reserved	DI	Reserved	
FDCSEC	0022	FDCSTA	0020	Reserved	FDCTRK	0021	FF	FMTERR	0020	Reserved	DI	Reserved	
FORCED	0000	FB	001C	Reserved	GAMINT	004C	GAMMSK	GRAMSK	1000	Reserved	DI	Reserved	
GRASEQ	C000	GRBMSK	2000	Reserved	GRBSEG	C800	GRCHMSK	GRCSEQ	D000	Reserved	DI	Reserved	
GRPMSK	7000	GR\$BLU	1010	Reserved	GR\$GRN	1020	GR\$RED	GS	001D	Reserved	DI	Reserved	
HIGHAT	000F	HT	0009	Reserved	HZDCHR	0001	HZSP08	HZTCHR	0000	Reserved	DI	Reserved	
ICW1	0010	ICM4	0000	Reserved	IC\$E01	0002	IC\$IW4	IC\$LTM	000B	Reserved	DI	Reserved	
IC\$HB6	0001	IC\$5N0	0002	Reserved	IEVCTR	0004	IEVFL0	IEVLNK	0000	Reserved	DI	Reserved	
IEVCTR	0006	IEVTID	0002	Reserved	IMMINT	000B	INTA00	INTA01	0019	Reserved	DI	Reserved	
INBTR	019A	IOB251	0010	Reserved	IOB253	0014	IR4INT	IR0INT	0040	Reserved	DI	Reserved	
IR1INT	0041	IR2INT	0042	Reserved	IR3INT	0043	KEYINT	IR5INT	0045	Reserved	DI	Reserved	
IR6INT	0046	IR7INT	0047	Reserved	I87MSK	0001	LED10F	LED10F	0001	Reserved	DI	Reserved	
LED20F	0002	LED30F	0004	Reserved	LF	000A	LINLEN	LPENHI	0010	Reserved	DI	Reserved	
LPENLO	0011	MAPINT	003B	Reserved	MEMBEG	0000	MEMEND	MEMSIZ	019B	Reserved	DI	Reserved	

MMASK	FF3F	MODCTL	0008	MONTYP	0004	MOTR\$0	0010	MOTR\$1	0020
MOTR\$2	0040	MOTR\$3	0080	MBCINP	1000	MSCOUT	1820	M103P1	1000
M103P2	2000	M103P3	4000	M103P4	8000	M212P1	0100	M212P2	0200
M212P3	0400	M212P4	0800	NAK	0015	NEAR	Reserved	NM11INT	0002
NRVERR	0080	NUL	0000	OFFSET	Reserved	OP\$ASP	0001	OP\$B0N	0009
OP\$EP	0002	OP\$FI	0000	OP\$IR	0003	OP\$IS	0004	OP\$IW	0005
OP\$NUL	00FF	OP\$R1B	0008	OP\$TER	000A	OP\$VFC	0006	OP\$VFD	0007
DVFINT	0004	PAR1EN	0008	PARINT	0008	PAUJNT	005C	PBKINT	005D
PHNBEG	0800	PHNBND	0FF	PRAUTO	0010	PRBUSY	0010	PRCI\$P	0001
PRCD\$P	0003	PRDA\$P	0002	PRECMP	0010	PRFAUL	0080	PRINEN	0080
PRINIT	0040	PRPAFO	0020	PRSLCT	0040	PRSTRB	0020	PRTINT	004B
PBCINT	005E	PRPBO0	0001	PBPB01	0002	PBPB02	0004	PBPB03	000B
PBPB04	0010	PBPB05	0020	PBPB06	0040	PBPB07	0080	PTR	Reserved
QUEINT	005F	RDCHD	0080	RD\$REG	1813	READIR	000A	READIB	0008
RQ\$MAX	0004	RQ\$REA	0001	RQ\$VFC	0003	RQ\$VFD	0004	RQ\$WRI	0002
RB	001E	RBTBUF	0022	RBTCHD	000C	RBTINT	0051	SCN1NB	0009
SCRLN	4000	SC1MSK	0100	SC2MSK	0200	SC3MSK	0400	SC4MSK	0800
SECBUF	0020	SEQ	Reserved	SEKCHD	001C	SEKERR	0002	SHORT	Reserved
SI	Reserved	SIDES0	0020	SINGLE	0008	SI\$	000F	S1CINT	005B
SNFERR	0010	SOH	0001	SO\$	000E	SP	Reserved	SPACE	0020
SPAREN	0001	SB	Reserved	STPCMD	005C	STPINT	0001	STPSPD	0000
BTX	0002	SUB\$	001A	SVCINT	0053	BYN	0016	BYNWD	0003
BYBCEL	01F0	BYSC01	019C	BYSC02	019E	B\$DEP	00FF	B\$DONE	0003
B\$IDLE	0000	B\$16EE	0002	B\$MAX	0003	B\$WAIT	0001	TC\$BCD	0001
TC\$LAT	0000	TC\$LSB	0010	TC\$MDO	0000	TC\$MD1	0002	TC\$MD2	0004
TC\$MD3	0006	TC\$MD4	0008	TC\$MD5	000A	TC\$MSB	0020	TC\$SCO	0000
TC\$BC1	0040	TC\$BC2	0080	TC\$HRD	0030	TIMCHD	0017	TIMERO	0014
TIMER1	0015	TIMER2	0016	TIMINT	005A	TMR1EN	0002	TMR2EN	0004
UB	001F	VFDERR	0001	VRADJL	0005	VRDROW	0006	VRBPOB	0007
VRTROW	0004	VT	0008	WINBK	0080	WINRBT	0031	WORD	Reserved
WRPERR	0040	WRBTCH	0010	WRTCMD	00A0	WRTREG	1812	XITVEC	0059
ZDVINT	0000								

No errors detected

SECTION B

 * LINK LOAD THE MODULES *

; SYBORG must be loaded first

```

; Input SYBORG
;
; System equates
;           ; no memory allocation
;           ; for these modules
;
; Input ROMERR
; Input TBERR
;

```

```

; Input      Output
; Powerup test      Modules entries reference.
; Modules

; absolute      relative to      module
; address       CS= F400         description

input HEADER      ; FE000      F400: A000      Rom header information
input PUP1ST      ; FE03F      F400: A03F      Power up test entry point
input RAMINI      ; FE143      F400: A143      Setup Rom's data in Ram
input INT1ST      ; FE190      F400: A190      Interrupt controller tests
input TIM1ST      ; FE333      F400: A333      Timers accuracy tests
input VECINI      ; FE454      F400: A454      Intrpt. vectors initialization
input DSK1ST      ; FE4A4      F400: A4A4      Floppy disks controller tests
input DRV1ST      ; FE583      F400: A583      Disk drivers tests
input CRT1ST      ; FE736      F400: A636      CRT tests
input MSC1ST      ; FE7DD      F400: A7DD      Miscellaneous initialization
input KEY1ST      ; FE8A1      F400: A8A1      Key board initialization
input PUP1SI      ; FE915      F400: A915      Option Rom & Ram tests
input DKBOOT      ; FEA47      F400: AA47      Disk booter loader

```

```

; Device drivers

input BELDSR      ; FEAFE      F400: AAFE      Beeper dsr
input CLKDSR      ; FEB98      F400: AB98      Day time clock dsr
input CRTDSR      ; FEC1A      F400: AC1A      CRT dsr
input DSK_IO      ; FF0D9      F400: B0D9      Disk io dsr
input DSK1SR      ; FF325      F400: B325      Disk dsr
input FLPD1R      ; FF475      F400: B475      Disk interrupt service
input KEYDSR      ; FF719      F400: B719      Key board dsr
input PR1DSR      ; FF9F6      F400: B9F6      Printer dsr
input CONFIO      ; FFAB4      F400: BAB4      Return system configuration
input ROMTIM      ; FFA9D      F400: BA9D      Rom timing services
input TIM1SR      ; FFAC1      F400: BAC1      System timer intr. serv.
input TIMSUB      ; FFB1D      F400: BB1D      Restart timer service

```


**SECTION C.
EQUATE FILES**

File name

PEQ: ASCII.EQU
PEQ: CRT.EQU
PEQ: CRTOP.EQU
PEQ: DSMDIT.EQU
PEQ: DSKERR.EQU
PEQ: DSKOP.EQU
PEQ: DSKOPS.EQU
PEQ: DSKREQ.EQU
PEQ: DSKSTA.EQU
PEQ: INTCTLR.EQU
PEQ: LATCHES.EQU
PEQ: PORTADDR.EQU
PEQ: FLOPPY.EQU
PEQ: SYSCELL.EQU
PEQ: BYSCON.EQU
PEQ: TIMERS.EQU
PEQ: TIMEVT.EQU
PEQ: VECTOR.EQU
PEQ: WINCHSTR.EQU


```
-0000
-0001
-0002
-0003
-0004
-0005
-0006
-0007
-0008
-0009
-000A
-000B
-000C
-000D
-000E
-000F
-0010
-0011
-0012
-0013
-0014
-0015
-0016
-0017
-0018
-0019
-001A
-001B
-001C
-001D
-001E
-001F
-0020
-007F

1 INCLUDE PEG:ASCII.EQU
2 ,
3 , --- ASCII CONTROL CHARACTER CODES ---
4 ,
5 NUL 00H EQU
6 SOH 01H EQU
7 STX 02H EQU
8 ETX 03H EQU
9 EOT 04H EQU
10 ENG 05H EQU
11 ACK 06H EQU
12 BEL 07H EQU
13 BS 08H EQU
14 HT 09H EQU
15 LF 0AH EQU
16 VT 0BH EQU
17 FF 0CH EQU
18 CR 0DH EQU
19 SO_ 0EH EQU
20 SI_ 0FH EQU
21 DLE 10H EQU
22 DC1 11H EQU
23 DC2 12H EQU
24 DC3 13H EQU
25 DC4 14H EQU
26 NAK 15H EQU
27 SYN 16H EQU
28 ETB 17H EQU
29 CAN 18H EQU
30 EM 19H EQU
31 SUB_ 1AH EQU
32 ESC_ 1BH EQU
33 FS_ 1CH EQU
34 OS 1DH EQU
35 RS 1EH EQU
36 US 1FH EQU
37 SPACE 20H EQU
38 DEL 7FH EQU

Trailing ', ' is to prevent keyword clash
Trailing ', ' is to prevent keyword clash

Trailing ', ' is to prevent keyword clash
Trailing ', ' is to prevent keyword clash
```

```

40          INCLUDE PEQ: CRT.EQU
41          ,
42          , CRT DEVICE EQUATES
43          ,
44          MEMBEO EQU 0000H          ; BEGINNING OF CRT MEMORY
45          MEMEND EQU 07FFH         ; END OF CRT MEMORY
46          PHINBEO EQU 0800H        ; BEGINNING OF PHANTOM MEMORY
47          PHINEND EQU 0FFFH       ; END OF PHANTOM MEMORY
48          MSCINP EQU 1000H        ; MISC. INPUT BUFFER
49          QR_BLU EQU 1010H        ; GRAPHICS BLUE PALETTE
50          QR_GRN EQU 1020H        ; GRAPHICS GREEN PALETTE
51          QR_RED EQU 1030H        ; GRAPHICS RED PALETTE
52          ATTLAT EQU 1800H        ; ATTRIBUTE LATCH
53          CRTADR EQU 1810H        ; CRT ADDRESS REGISTER
54          CRTSTA EQU 1811H        ; CRT STATUS REGISTER
55          WRTREQ EQU 1812H        ; CRT WRITE REGISTER
56          RD_REQ EQU 1813H        ; CRT READ REGISTER
57          MSCOUT EQU 1820H        ; MISC. OUTPUT REGISTER
58          GRASEQ EQU 0C000H       ; GRAPHIC BANK A ADDRESS
59          GRBSEQ EQU 0C800H       ; GRAPHIC BANK B ADDRESS
60          QRCBEQ EQU 0D000H       ; GRAPHIC BANK C ADDRESS
61          SCRLEN EQU 04000H       ; LENGTH OF GRAPHICS MEMORY IN WORDS
62          LINLEN EQU 45           ; GRAPHICS LINE LENGTH IN WORDS
63          BLNLEN EQU 92          ; GRAPHICS LINE LENGTH IN BYTES
64          ,
65          , CRT REGISTER EQUATES
66          ,
67          HZTCHR EQU 00H          ; HORIZONTAL TOTAL CHARB.
68          HZDCHR EQU 01H         ; HORIZONTAL DISPLAYED CHARB.
69          HZSPOS EQU 02H         ; HORIZONTAL SYNC POSITION
70          SYNWDI EQU 03H         ; VSYNC WIDTH, HSYNC WIDTH
71          VRTROW EQU 04H         ; VERTICAL TOTAL ROWS
72          VRADJL EQU 05H         ; VERTICAL ADJUST LINES
73          VRDROW EQU 06H         ; VERTICAL DISPLAYED ROWS
74          VRSPBS EQU 07H         ; VERTICAL SYNC POSITION
75          MODCTL EQU 08H         ; MODE CONTROL
76          SCNLNS EQU 09H         ; SCAN LINES PER ROW
77          CURSTR EQU 0AH         ; CURSOR START SCAN LINE
78          CUREND EQU 0BH         ; CURSOR END SCAN LINE
79          DISSTH EQU 0CH         ; DISPLAY START ADDRESS HIGH
80          DISSTL EQU 0DH         ; DISPLAY START ADDRESS LOW
81          CURPSH EQU 0EH         ; CURSOR POSITION ADDRESS HIGH
82          CURPSL EQU 0FH         ; CURSOR POSITION ADDRESS LOW
83          LPENHI EQU 10H         ; LIGHT PEN ADDRESS HIGH
84          LPENLO EQU 11H         ; LIGHT PEN ADDRESS LO
85          ,
86          , CRT BIT EQUATES
87          ,
88          CDBPEN EQU 01000000B   ; CRT DISPLAY ENABLE (ALSO DISABLES INTERRUPT)
89          CINTEN EQU 10000000B   ; CRT INTERRUPT ENABLE
90          CINTIM EQU 1500H       ; CRT INTERRUPT WAIT LOOP COUNT
91          CRTVBL EQU 00100000B   ; CRT IN VERTICAL BLANK PERIOD

```

```

-0000
-07FF
-0800
-0FFF
-1000
-1010
-1020
-1030
-1800
-1810
-1811
-1812
-1813
-1820
-8000
-C800
-D000
-F000
-002D
-005C

```

```

-0000
-0001
-0002
-0003
-0004
-0005
-0006
-0007
-0008
-0009
-000A
-000B
-000C
-000D
-000E
-000F
-0010
-0011

```

```

-0040
-0080
-1500
-0020

```

-0020	92	CRTOFF	EQV	20H	;	CRT DISABLE
-000F	93	HIGHAT	EQV	OFH	;	HIGH BRILLIANCE ATTRIBUTES
	94	;	CHARACTER	EQUATES		
	95	;				
	96	;				
-00DB	97	BLOCK	EQV	ODBH	;	BLOCK CHARACTER



```
-0000  
-0001  
-0002  
-0003  
-0004  
-0005  
-0006  
-0007  
-0008  
-0009  
-000A  
-000C  
-000D  
-000E  
-000F  
-0010  
-0011  
-0012  
-0013  
-0014  
-0015  
-0016  
-0017  
-0018  
99  
100  
101  
102  
103  
104  
105  
106  
107  
108  
109  
110  
111  
112  
113  
114  
115  
116  
117  
118  
119  
120  
121  
122  
123  
124  
125  
126  
127  
INCLUDE PEG: CRTOP.EQU  
,  
, --- CRT DSR 'opcodes' (INT 49H)  
,  
CRTMOD EQU 00H ; Set CRT mode function  
CRTSCP EQU 01H ; Set cursor type function  
CRTSCP EQU 02H ; Set cursor position function  
CRTSCP EQU 03H ; Read cursor position function  
CRTLPP EQU 04H ; Read light pen position function  
CRTSDP EQU 05H ; Set active display page  
CRTSDN EQU 06H ; Scroll text up function  
CRTSDN EQU 07H ; Scroll text down function  
CRTTRAC EQU 08H ; Read attribute and character function  
CRTTRAC EQU 09H ; Write attribute and character function  
CRTTRAC EQU 0AH ; Write character only function  
CRTSGP EQU 0BH ; Set graphics palette function  
CRTWPX EQU 0CH ; Write graphics pixel function  
CRTRPX EQU 0DH ; Read graphics pixel function  
CRTWTY EQU 0EH ; Write TTY screen output function  
CRTRVS EQU 0FH ; Read video state function  
CRTACB EQU 10H ; Write attribute and character block function  
CRTWCB EQU 11H ; Write character block function  
CRTSCA EQU 12H ; Change screen attributes function  
CRTCTX EQU 13H ; Clear text screen function  
CRTGCR EQU 14H ; Clear graphics screen function  
CRTSTY EQU 15H ; Set TTY status line function  
CRTSAT EQU 16H ; Set attribute latch function  
CRTGDB EQU 17H ; Get physical display begin pointer function  
CRTPTS EQU 18H ; Print TTY string function
```

```
-0000  
-0004  
-0006  
-0007  
-0008  
-0009  
-000A  
-000B  
-0000  
  
129  
130  
131  
132  
133  
134  
135  
136  
137  
138  
139  
140  
  
INCLUDE PEQ: DBKDIT.EQU  
DITDIR EQU 0  
DITBEC EQU 4  
DITTRK EQU 6  
DITCYL EQU 7  
DITDSK EQU 8  
DITERR EQU 9  
DITPRC EQU 10  
DITLEN EQU 11  
DITINV EQU 0000h  
  
; Floppy disk DIT equates  
; Disk Interface Routine vector (dword)  
; Sector size in bytes (word)  
; Track size in sectors (byte)  
; Cylinder size in tracks (byte)  
; Disk size in cylinders (word)  
; Maximum number of error retries  
; Write pre-comp threshold cylinder  
; Length of DIT  
; Used to signal that a DIT is invalid
```



```
-0000  
-0080  
-0040  
-0020  
-0010  
-0008  
-0004  
-0002  
-0001  
-0003  
-0005  
-0009  
-0011  
  
142  
143  
144  
145  
146  
147  
148  
149  
150  
151  
152  
153  
154  
155  
156  
157  
158  
159  
160  
161  
162  
163  
164  
165  
166  
167  
168  
169  
170  
171  
172  
173  
174  
175  
176  
  
DKNORM EQU 00000000B  
DKNRDY EQU 10000000B  
DKSEEK EQU 01000000B  
DKFAIL EQU 00100000B  
DKCRCE EQU 00010000B  
DKDMAE EQU 00001000B  
DKRNFE EQU 00000100B  
DKFMTE EQU 00000010B  
DKCMDE EQU 00000001B  
DKWPRT EQU 00000011B  
DKVFYE EQU 00000101B  
DKBDUN EQU 00001001B  
DKABTD EQU 00010001B  
  
E_NORM EQU DKNORM  
E_ABDT EQU DKABTD  
E_BADC EQU DKABTD  
E_BCYL EQU DKRNFE  
E_BTRK EQU DKRNFE  
E_BOUN EQU DKBDUN  
E_BSEC EQU DKRNFE  
E_CRC EQU DKCRCE  
E_NRDY EQU DKNRDY  
E_RNF EQU DKRNFE  
E_BEEK EQU DKSEEK  
E_TIME EQU DKNRDY  
E_VRFY EQU DKVFYE  
E_MRPT EQU DKWPRT  
  
INCLUDE PEG: DSKERR.EQU  
  
DISK DSR ERROR CODES (IBM COMPATIBLE)  
; No error  
; Controller timeout - disk not ready  
; Seek failed - track not found  
; Controller hardware failure  
; CRC error on read  
; Data request error - controller failure  
; Sector not found error  
; Timeout - no data error - bad disk format  
; Command error - bad command passed  
; Write protect error  
; Data verification error  
; I/O transfer crosses 64k boundary  
; I/O operation aborted by request  
  
; E_XXXX = ) Operation status' (error codes)  
; These values are used in the disk driver and  
; are translated via equate to the above set of  
; error codes for compatibility with the values  
; used elsewhere in the system.  
; No error  
; Disk I/O aborted by request  
; Bad command  
; Non-existent cylinder requested  
; Non-existent track requested  
; I/O transfer crosses 64K boundary  
; Non-existent sector requested  
; Bad CRC found  
; Device is not ready  
; Could not find requested sector  
; Could not find requested cylinder  
; Operation did not complete  
; Write data did not verify  
; Media is write protected
```

```
-00FF  
-0000  
-0001  
-0002  
-0003  
-0004  
-0005  
-0006  
-0007  
-0008  
-0009  
-000A  
  
178  
179  
180  
181  
182  
183  
184  
185  
186  
187  
188  
189  
190  
191  
192  
193  
194  
  
OP_NUL EQU 235  
OP_FI EQU 0  
OP_ABP EQU 1  
OP_EP EQU 2  
OP_IR EQU 3  
OP_IB EQU 4  
OP_IW EQU 5  
OP_VFC EQU 6  
OP_VFD EQU 7  
OP_RIB EQU 8  
OP_BON EQU 9  
OP_TER EQU 10  
  
INCLUDE PEO:DSKOP.EQU  
  
; OP_### =) Opcodes for generic disk operations  
; These values are displacements in the Device  
; Interface Table to the branch vector for each  
; corresponding routine.  
; Null operation  
; Interrupt  
; Auxiliary State Processor  
; Error Recovery Processor  
; Initiate Read  
; Initiate Seek  
; Write  
; Verify CRC  
; Verify data  
; Read and Interpret Status  
; Begin request  
; Process request termination
```

```
-0000  
-0001  
-0002  
-0003  
-0004  
  
-0006  
-0007  
-0008  
-0009  
-000A  
-000B  
-000C  
-000D  
-000E  
-000F
```

196
197
198
199
200
201
202
203
204
205
206
207
208
209
210
211
212
213
214

DKRSET EQU 0
DKSTAT EQU 1
DKREAD EQU 2
DKWRIT EQU 3
DKVERF EQU 4
DKFORM EQU 5
DKVRFY EQU 6
DKSSTA EQU 7
DKFBET EQU 8
DKRDIT EQU 9
DKKMOT EQU 10
DKBADC EQU 11
DKSETC EQU 12
DKABRT EQU 13
DKFSTA EQU 14
DKBADD EQU 15

INCLUDE PEQ: DSKOPS.EQU

DISK DSR OPERATION CODES
; Reset disk system, drive parms must be preset
; Get disk status in (al)
; Read sectors into memory
; Write memory to disk sectors
; Verify crc on disk sectors
; Format track (not implemented)
; Verify memory against disk sectors
; Get disk status for preretry (if any)
; Set UNIT & standard DIT for a drive
; Set UNIT & DIT address for a drive
; Return DIT address for drive
; Turn off Floppy Disk Motors
; Old)= this is a bad command
; Set concurrent mode
; Abort pending operation
; Return last full status
; New)= this is a bad command

```

-0001
-0002
-0003
-0004
-0004

216
217
218
219
220
221
222
223

INCLUDE PEO: DSKREQ.EQU

;RO_SEEK EQU 0
;RO_READ EQU 1
;RO_WRIT EQU 2
;RO_VFC EQU 3
;RO_VFD EQU 4
;RO_MAX EQU 4

; DSKIBR request function codes
; Seek ***** OBSELETE *****
; Read
; Write
; Verify CRC
; Verify data
; Maximum request number
```

```

-00FF
-0000
-0001
-0002
-0003
-0003

225          INCLUDE PEQ: DSKSTA.EQU
226          ,
227          S_DEP EQU 255
228          S_IDLE EQU 0
229          S_WAIT EQU 1
230          S_ISEEK EQU 2
231          S_DONE EQU 3
232          S_MAX EQU 3
233          ,

          S_XXXX =) Driver states
          Device-dependent code must set state
          Waiting for a request
          Waiting to retry a request
          Implicit seek pending
          Operation completed
          The highest "standard" state number
          Other values are device-dependent
```

```
235 INCLUDE PEO: INTCTLR.EQU
236
237 ;
238 ;
239 ;
240 ;
241 ;
242 ;
243 ;
244 ;
245 ;
246 ;
247 ;
248 ;
249 ;
250 ;
251 ;
252 ;
253 ;
254 ;
255 ;
256 ;
257 ;
258 ;
259 ;
260 ;
261 ;
262 ;

-0018 INTA00 EQU 1BH
-0019 INTA01 EQU 19H

-0010 ICH1 EQU 10H
-0001 IC_IW4 EQU 01H
-0002 IC_SNG EQU 02H
-0008 IC_LTM EQU 08H

-0000 ICH4 EQU 00H
-0001 IC_MB6 EQU 01H
-0002 IC_E01 EQU 02H

ENAB0 EQU 11111110B
ENAB1 EQU 11111101B
ENAB2 EQU 11111011B
ENAB3 EQU 11110111B
ENAB4 EQU 11101111B
ENAB5 EQU 11011111B
ENAB6 EQU 10111111B
ENAB7 EQU 01111111B

E01 EQU 20H
READIR EQU 0AH
READI8 EQU 0BH

; Interrupt controller ports
; ICH1, ICH2, ICH3 ; Read IRR, IBR
; ICH1, ICH2, ICH3, ICH4 ; Read IMR
; ICH1 base (written to INTA00)
; ICH4 needed
; Single B259 mode (not cascaded)
; Level-triggered mode
; ICH4 base (written to INTA01)
; Microprocessor mode = 8086/88
; Auto End-Of-Interrupt
; Interrupt Masks (written to INTA01)

; Commands (written to INTA00)
; Non-specific End-Of-Interrupt command
; Read Interrupt Request Register next
; Read In-Service Register next
```

```
264 INCLUDE PEO:LATCHES.EQU
265
266 ; Floppy control and timer interrupt latch - 'DSKS_P'
267
268 ; Speaker enable (timer0 gate input)
269 ; Timer 1 interrupt enable
270 ; Timer 2 interrupt enable
271 ; Single density (FM) (vs. Double - MFM)
272 ; Write precomp enable (inner tracks only)
273 ; Head 0 select
274 ; Sector buffer control bits M1, M0 :
275 ; Access RAM or reset RAM counter
276 ; Access Floppy controller chip
277 ; Select FDC --) BUFFER mode (disk read)
278 ; Select BUFFER --) FDC mode (disk write)
279 ; Mask for setting sector buffer mode
280
281 ; Floppy select and motor control latch - 'DSKS_P'
282
283 ; Floppy drive selects (Active LOW)
284
285 ; Motor On for drive 0 (Note: MOTR_0 & MOTR_1
286 ; Motor On for drive 1 are OR'd in hardware)
287 ; Motor On for drive 2
288 ; Motor On for drive 3
289
290 ; Parallel port control input port - 'PRCI_P'
291
292 ; Drives are single-sided (vs. double-sided)
293 ; Drives are 48tpi (vs. 96tpi)
294 ; Monitor type {(60HZ system (vs. 50HZ))}
295 ; Parity interrupt pending
296 ; Parallel port BUSY signal
297 ; Parallel port PAPER OUT signal
298 ; Parallel port SELECTED signal
299 ; Parallel port FAULT- signal
300
301 ; Parallel port control output latch - 'PRCO_P'
302
303 ; LED 1 off \
304 ; LED 2 off ) Active LOW
305 ; LED 3 off /
306 ; Parity interrupt enable
307 ; Parallel port AUTO FEED- signal
308 ; Parallel port STROBE- signal
309 ; Parallel port INIT- signal
310 ; Parallel port interrupt enable
311
312
```

```

-0010
-0014
-0018
-0020
-0000
-0001
-0002
-0003
-0004

314 INCLUDE PEG:PORTADDR.EQU
315 , --- I/O port addresses ---
316 10B251 EQU 10H ; Base addr of UBART ; (U44)
317 10B253 EQU 14H ; Base addr of Timer Chip ; (U45)
318 10B259 EQU 18H ; Base addr of Interrupt Controller ; (U46)
319 FDC EQU 20H ; Base addr of Floppy Controller ; (U13)
320 ,
321 DBKC_P EQU 00H ; Floppy control and timer intr. latch ; (U47)
322 PRCI_P EQU 01H ; Parallel port control input port ; (U48)
323 PRDA_P EQU 02H ; Parallel port data output latch ; (U49)
324 PRCD_P EQU 03H ; Parallel port control output latch ; (U50)
325 DBKB_P EQU 04H ; Floppy select and motor control latch ; (U51)

```



```
327 INCLUDE PEO:FLOPPY.EQU
328 ;
329 ;   DISK CONTROLLER EQUATES
330 ;
331 FDCCMD EQU FDC+0 ; Command port
332 FDCBTA EQU FDC+0 ; Status register
333 FDCTRK EQU FDC+1 ; Track register
334 FDCBEC EQU FDC+2 ; Sector register
335 FDCDAT EQU FDC+3 ; Data register
336 SECBUF EQU FDC+0 ; Sector buffer read/write port
337 RSTBUF EQU FDC+2 ; Reset sector buffer port
338 ;
339 STPSPD EQU 00H ; Track-to-track step speed: 00 --) 6 msec
340 ;
341 ;
342 ;   FDC Commands:
343 ;
344 RDCMD EQU 80H ; Read sector
345 WRTCMD EQU 0A0H ; Write sector
346 RSTCMD EQU 0CH + STPSPD ; Restore (home)
347 SEKCMD EQU 1CH + STPSPD ; Seek w/verify
348 STPCMD EQU 5CH + STPSPD ; Step in w/verify & update
349 FORCED EQU 0D0H ; Forced interrupt (gets type I status)
350 IMMINT EQU 0DBH ; Immediate interrupt
351 ;
352 ;   FDC and Disk DSR internal error codes:
353 ;
354 NRVERR EQU 80H ; Not ready error
355 WRPERR EQU 40H ; Write protect error
356 FMTERR EQU 20H ; Disk format error (timeout w/disk in drive)
357 BNFERR EQU 10H ; Sector not found error
358 CRCERR EQU 0BH ; CRC error
359 DATERR EQU 04H ; Lost data error
360 SEKERR EQU 02H ; Seek error (contrived - not an FDC error)
361 VFDERR EQU 01H ; Data verification error
```

```
-0020
-0020
-0021
-0022
-0023
-0020
-0022
-0000
-0080
-00A0
-000C
-001C
-005C
-00D0
-00DB
-0080
-0040
-0020
-0010
-000B
-0004
-0002
-0001
```

```

363 INCLUDE PEO:BYBCELL.EQU
364 ; ---- SYSTEM CELL OFFSETS ----
365 ;
366 ; The 'System Cell' is an area of the boot sector reserved for system
367 ; configuration and identification information. These locations are
368 ; defined as offsets from BOOTLO.
369 ;
370 BYBCEL EQU 1FOH ; Beginning of system cell area
371 BOOTID EQU 1FCH ; Offset of boot id word
372 BOOTCRC EQU 1FEH ; Offset of boot sector CRC

```

```

-01FO
-01FC
-01FE

```

```
374 INCLUDE PEQ: BYSCON.EQU
375 ; --- Primary System Configuration word (BYSCON, accessed with INT 4FH)
376 ; NOTE: Several routines take advantage of the knowledge of which byte
377 ; the bits are in and that the DRx, SCx, and QRx bits are adjacent.
378 DRVMSK EQU 0000000000001111B
379 DROMSK EQU 0000000000000001B
380 DR1MSK EQU 000000000000010B
381 DR2MSK EQU 000000000000100B
382 DR3MSK EQU 000000000001000B
383 E12MSK EQU 000000000010000B
384 E34MSK EQU 000000000100000B
385 E56MSK EQU 000000000100000B
386 WINMSK EQU 0000000010000000B
387 ; (high byte):
388 SC1MSK EQU 0000000100000000B
389 SC2MSK EQU 0000001000000000B
390 SC3MSK EQU 0000010000000000B
391 SC4MSK EQU 0000100000000000B
392 GRPMSK EQU 0111000000000000B
393 GRAMS1 EQU 0001000000000000B
394 GRAMS2 EQU 0010000000000000B
395 GRAMS3 EQU 0100000000000000B
396 GRAMS4 EQU 1000000000000000B
397 ; (high byte of EXTWD1): comm boards
398 M103P1 EQU 0001000000000000B
399 M103P2 EQU 0010000000000000B
400 M103P3 EQU 0100000000000000B
401 M103P4 EQU 1000000000000000B
402 M212P1 EQU 0000000100000000B
403 M212P2 EQU 0000001000000000B
404 M212P3 EQU 0000010000000000B
405 M212P4 EQU 0000100000000000B
406 ; (low byte of EXTWD1): speech boards
407 PSPB00 EQU 0000000000000001B
408 PSPB01 EQU 000000000000010B
409 PSPB02 EQU 0000000000000100B
410 PSPB03 EQU 00000000000001000B
411 PSPB04 EQU 0000000000010000B
412 PSPB05 EQU 0000000000100000B
413 PSPB06 EQU 0000000001000000B
414 PSPB07 EQU 0000000010000000B
415 ;
416 ; Secondary System Configuration words (accessed with INT 4BH)
417 ;
418 ; BYSCD1 (in Interrupt Vector area):
419 1B7MSK EQU 0000000000000001B
420 ;
421 ; SYBCD2 (in Interrupt Vector area):
422 ; DRVBYT (in Interrupt Vector area):
423 ;
424 ;
425 ;
426 ;
427 ;
428 ;
429 ;
430 ;
431 ;
432 ;
433 ;
434 ;
435 ;
436 ;
437 ;
438 ;
439 ;
440 ;
441 ;
442 ;
443 ;
444 ;
445 ;
446 ;
447 ;
448 ;
449 ;
450 ;
451 ;
452 ;
453 ;
454 ;
455 ;
456 ;
457 ;
458 ;
459 ;
460 ;
461 ;
462 ;
463 ;
464 ;
465 ;
466 ;
467 ;
468 ;
469 ;
470 ;
471 ;
472 ;
473 ;
474 ;
475 ;
476 ;
477 ;
478 ;
479 ;
480 ;
481 ;
482 ;
483 ;
484 ;
485 ;
486 ;
487 ;
488 ;
489 ;
490 ;
491 ;
492 ;
493 ;
494 ;
495 ;
496 ;
497 ;
498 ;
499 ;
500 ;
501 ;
502 ;
503 ;
504 ;
505 ;
506 ;
507 ;
508 ;
509 ;
510 ;
511 ;
512 ;
513 ;
514 ;
515 ;
516 ;
517 ;
518 ;
519 ;
520 ;
521 ;
522 ;
523 ;
524 ;
525 ;
526 ;
527 ;
528 ;
529 ;
530 ;
531 ;
532 ;
533 ;
534 ;
535 ;
536 ;
537 ;
538 ;
539 ;
540 ;
541 ;
542 ;
543 ;
544 ;
545 ;
546 ;
547 ;
548 ;
549 ;
550 ;
551 ;
552 ;
553 ;
554 ;
555 ;
556 ;
557 ;
558 ;
559 ;
560 ;
561 ;
562 ;
563 ;
564 ;
565 ;
566 ;
567 ;
568 ;
569 ;
570 ;
571 ;
572 ;
573 ;
574 ;
575 ;
576 ;
577 ;
578 ;
579 ;
580 ;
581 ;
582 ;
583 ;
584 ;
585 ;
586 ;
587 ;
588 ;
589 ;
590 ;
591 ;
592 ;
593 ;
594 ;
595 ;
596 ;
597 ;
598 ;
599 ;
600 ;
601 ;
602 ;
603 ;
604 ;
605 ;
606 ;
607 ;
608 ;
609 ;
610 ;
611 ;
612 ;
613 ;
614 ;
615 ;
616 ;
617 ;
618 ;
619 ;
620 ;
621 ;
622 ;
623 ;
624 ;
625 ;
626 ;
627 ;
628 ;
629 ;
630 ;
631 ;
632 ;
633 ;
634 ;
635 ;
636 ;
637 ;
638 ;
639 ;
640 ;
641 ;
642 ;
643 ;
644 ;
645 ;
646 ;
647 ;
648 ;
649 ;
650 ;
651 ;
652 ;
653 ;
654 ;
655 ;
656 ;
657 ;
658 ;
659 ;
660 ;
661 ;
662 ;
663 ;
664 ;
665 ;
666 ;
667 ;
668 ;
669 ;
670 ;
671 ;
672 ;
673 ;
674 ;
675 ;
676 ;
677 ;
678 ;
679 ;
680 ;
681 ;
682 ;
683 ;
684 ;
685 ;
686 ;
687 ;
688 ;
689 ;
690 ;
691 ;
692 ;
693 ;
694 ;
695 ;
696 ;
697 ;
698 ;
699 ;
700 ;
701 ;
702 ;
703 ;
704 ;
705 ;
706 ;
707 ;
708 ;
709 ;
710 ;
711 ;
712 ;
713 ;
714 ;
715 ;
716 ;
717 ;
718 ;
719 ;
720 ;
721 ;
722 ;
723 ;
724 ;
725 ;
726 ;
727 ;
728 ;
729 ;
730 ;
731 ;
732 ;
733 ;
734 ;
735 ;
736 ;
737 ;
738 ;
739 ;
740 ;
741 ;
742 ;
743 ;
744 ;
745 ;
746 ;
747 ;
748 ;
749 ;
750 ;
751 ;
752 ;
753 ;
754 ;
755 ;
756 ;
757 ;
758 ;
759 ;
760 ;
761 ;
762 ;
763 ;
764 ;
765 ;
766 ;
767 ;
768 ;
769 ;
770 ;
771 ;
772 ;
773 ;
774 ;
775 ;
776 ;
777 ;
778 ;
779 ;
780 ;
781 ;
782 ;
783 ;
784 ;
785 ;
786 ;
787 ;
788 ;
789 ;
790 ;
791 ;
792 ;
793 ;
794 ;
795 ;
796 ;
797 ;
798 ;
799 ;
800 ;
801 ;
802 ;
803 ;
804 ;
805 ;
806 ;
807 ;
808 ;
809 ;
810 ;
811 ;
812 ;
813 ;
814 ;
815 ;
816 ;
817 ;
818 ;
819 ;
820 ;
821 ;
822 ;
823 ;
824 ;
825 ;
826 ;
827 ;
828 ;
829 ;
830 ;
831 ;
832 ;
833 ;
834 ;
835 ;
836 ;
837 ;
838 ;
839 ;
840 ;
841 ;
842 ;
843 ;
844 ;
845 ;
846 ;
847 ;
848 ;
849 ;
850 ;
851 ;
852 ;
853 ;
854 ;
855 ;
856 ;
857 ;
858 ;
859 ;
860 ;
861 ;
862 ;
863 ;
864 ;
865 ;
866 ;
867 ;
868 ;
869 ;
870 ;
871 ;
872 ;
873 ;
874 ;
875 ;
876 ;
877 ;
878 ;
879 ;
880 ;
881 ;
882 ;
883 ;
884 ;
885 ;
886 ;
887 ;
888 ;
889 ;
890 ;
891 ;
892 ;
893 ;
894 ;
895 ;
896 ;
897 ;
898 ;
899 ;
900 ;
901 ;
902 ;
903 ;
904 ;
905 ;
906 ;
907 ;
908 ;
909 ;
910 ;
911 ;
912 ;
913 ;
914 ;
915 ;
916 ;
917 ;
918 ;
919 ;
920 ;
921 ;
922 ;
923 ;
924 ;
925 ;
926 ;
927 ;
928 ;
929 ;
930 ;
931 ;
932 ;
933 ;
934 ;
935 ;
936 ;
937 ;
938 ;
939 ;
940 ;
941 ;
942 ;
943 ;
944 ;
945 ;
946 ;
947 ;
948 ;
949 ;
950 ;
951 ;
952 ;
953 ;
954 ;
955 ;
956 ;
957 ;
958 ;
959 ;
960 ;
961 ;
962 ;
963 ;
964 ;
965 ;
966 ;
967 ;
968 ;
969 ;
970 ;
971 ;
972 ;
973 ;
974 ;
975 ;
976 ;
977 ;
978 ;
979 ;
980 ;
981 ;
982 ;
983 ;
984 ;
985 ;
986 ;
987 ;
988 ;
989 ;
990 ;
991 ;
992 ;
993 ;
994 ;
995 ;
996 ;
997 ;
998 ;
999 ;
1000 ;
```

```
426 INCLUDE PEQ: TIMERS.EQU  
427  
428 ;  
429 ; TIMER CHIP EQUATES  
430  
431 ; TIMER0 EQU 108253 ; Timer 0 read/load addr (Speaker beep)  
432 ; TIMER1 EQU 108253+1 ; Timer 1 read/load addr (System timer)  
433 ; TIMER2 EQU 108253+2 ; Timer 2 read/load addr  
434 ; TIMCMD EQU 108253+3 ; Command Register  
435 ;  
436 ; Control word format  
437 ;  
438 TC_BCO EQU 00H ; Select counter 0  
439 TC_BC1 EQU 40H ; Select counter 1  
440 TC_BC2 EQU 80H ; Select counter 2  
441 ;  
442 TC_LAT EQU 00H ; Counter latching operation  
443 TC_MSB EQU 20H ; Read/load most significant byte only  
444 TC_LBB EQU 10H ; Read/load least significant byte only  
445 TC_WRD EQU 30H ; Read/load LBB first, then MBB  
446 ;  
447 TC_MD0 EQU 00H ; Select mode 0  
448 TC_MD1 EQU 02H ; Select mode 1  
449 TC_MD2 EQU 04H ; Select mode 2  
450 TC_MD3 EQU 06H ; Select mode 3  
451 TC_MD4 EQU 08H ; Select mode 4  
452 TC_MD5 EQU 0AH ; Select mode 5  
453 ;  
454 TC_BCD EQU 01H ; BCD counter mode
```

```
455 INCLUDE PEQ: TIMEVT.EQU
456
457 ; TIMING EVENT OFFSETS
458 ;
459 IEVLNK EQU 00H ; Link pointer
460 IEVTID EQU 02H ; Task id
461 IEVFLO EQU 03H ; Flags
462 IEVCTR EQU 04H ; Counter
463 IEVSUB EQU 06H ; Subroutine address
464 ;
465 ; Event flags
466 ;
467 EVTTYP EQU 4 ; Event type
468 EVTACK EQU 5 ; Event acknowledged
469 EVTEXP EQU 6 ; Event expired
470 EVTBITA EQU 7 ; Event status
471
```

-0000
-0002
-0003
-0004
-0006

-0004
-0005
-0006
-0007

0/1 = Interrupt/Task
0/1 = Yes/No
0/1 = No/Yes
0/1 = Inactive/Active

```
473 INCLUDE PEO:VECTOR.EQU
474 *****
475 *** ANY CHANGES MADE TO THIS EQUATE FILE MUST ALSO BE MADE TO ***
476 *** THE VECTOR INITIALIZATION TABLES IN THE MODULE 'VECTINI'. ***
477 *** ALSO NOTE THAT CSMRAP CHANGES AFFECT THE MODULE 'BYBORG'. ***
478 *****
479 ----- Pegasus interrupt vector types -----
480
481 ; 8088-specific (hardware) interrupts:
482
483 ZDVINT EQU 00H ; Divide-by-zero trap
484 BTPIINT EQU 01H ; Single-step trap
485 NMIINT EQU 02H ; Non-maskable interrupt
486 BRKINT EQU 03H ; Break (single-byte) software interrupt
487 OVFINTEQU 04H ; Overflow trap
488
489 ; (types 05H-1FH reserved by INTEL)
490 ; (types 20H-3FH reserved for MSDOS)
491
492 ; Pegasus hardware interrupts:
493
494 IROINT EQU 40H ; 8259 interrupt 0 (unused)
495 IRIINT EQU 41H ; 8259 interrupt 1 (unused)
496 IR2INT EQU 42H ; 8259 interrupt 2 (unused)
497 IR3INT EQU 43H ; 8259 interrupt 3 (Timer 1)
498 IR4INT EQU 44H ; 8259 interrupt 4 (unused)
499 IR5INT EQU 45H ; 8259 interrupt 5 (Parallel port ACK)
500 IR6INT EQU 46H ; 8259 interrupt 6 (Disk controller)
501 IR7INT EQU 47H ; 8259 interrupt 7 (Keyboard UBART)
502
503 ; ROM BIOS interface vectors:
504
505 BELINT EQU 48H ; System beeper I/O and general ROM interface
506 CRTINT EQU 49H ; Screen I/O
507 KEYINT EQU 4AH ; Keyboard I/O
508 PRINT EQU 4BH ; Parallel port I/O
509 QAMINT EQU 4CH ; Analog Input/Clock I/O
510 DSKINT EQU 4DH ; Floppy disk I/O
511 CLKINT EQU 4EH ; Time-of-day clock I/O
512 CONINT EQU 4FH ; System configuration
513
514 FATINT EQU 50H ; Fatal error trap
515 RBTINT EQU 51H ; Restart timing event (Timing services)
516 CANINT EQU 52H ; Cancel timing event (Timing services)
517 SVCINT EQU 53H ; SVC Interface subroutine
518 ACTINT EQU 54H ; Activate Task subroutine
519 COMPLT EQU 55H ; Addr for disk I/O completion (concurrent env)
520
521 ; System interface vectors (soft interrupts):
522
523 CMPINT EQU 57H ; CRT character mapping vector
524 BLCINT EQU 58H ; Time slicing, if needed (every 25 msec)
```

```
-0059 XITVEC EQU 59H ; Address of common interrupt exit routine
-005A TIMINT EQU 5AH ; Dynamic timing services, if needed (100 msec)
-005B MAPINT EQU 5BH ; Keyboard mapping vector
-005C PAUINT EQU 5CH ; Keyboard program pause key vector
-005D PBKINT EQU 5DH ; Keyboard program break key vector
-005E PBCINT EQU 5EH ; Keyboard print screen vector
-005F QUEINT EQU 5FH ; Keyboard queuing vector

;
; (type OEH reserved for CP/M)
;
; FIXED ROM DATA AREA - (absolute offsets from absolute 0)
; These equates define the ROM communications area, containing data
; that must be accessed by both the ROM and user/application programs.
; This data is accessed from the 'user' program by setting DB = 0. The
; ROM uses a 'trick' to get to this data by taking advantage of the fact
; that the hardware 'wraps' memory addresses (i.e. the address following
; 0FFFFFFH is 0). By accessing relative to CB, and adding the magic number
; CBWRAP (defined below), the desired data (DBADDR, MEMSIZ, etc.) may
; be accessed without having to set up a separate DB first.
;
-0180 DBADDR EQU 4*60H ; (WORD) pointer to DB for System ROM (ROMDAT)
-0182 DBSIZR EQU 4*60H+2 ; (WORD) size of DB for System ROM (ROMDAT)
-0184 DBADD0 EQU 4*61H ; (WORD) pointer to DB for ROM at ROMCOD:0000
-0186 DBSIZ0 EQU 4*61H+2 ; (WORD) size of DB for ROM at ROMCOD:0000
-0188 DBADD2 EQU 4*62H ; (WORD) pointer to DB for ROM at ROMCOD:2000
-018A DBSIZ2 EQU 4*62H+2 ; (WORD) size of DB for ROM at ROMCOD:2000
-018C DBADD4 EQU 4*63H ; (WORD) pointer to DB for ROM at ROMCOD:4000
-018E DBSIZ4 EQU 4*63H+2 ; (WORD) size of DB for ROM at ROMCOD:4000
-0190 DBADD6 EQU 4*64H ; (WORD) pointer to DB for ROM at ROMCOD:6000
-0192 DBSIZ6 EQU 4*64H+2 ; (WORD) size of DB for ROM at ROMCOD:6000
-0194 DBADD8 EQU 4*65H ; (WORD) pointer to DB for ROM at ROMCOD:8000
-0196 DSSIZ8 EQU 4*65H+2 ; (WORD) size of DB for ROM at ROMCOD:8000
-0198 MEMSIZ EQU 4*66H ; (WORD) memory size (number of 16-byte blocks)
-019A INTCTR EQU 4*66H+2 ; (BYTE) outstanding-interrupt counter (for DS)
-019B DRVBYT EQU 4*66H+3 ; (BYTE) Drive configuration byte
-019C SYBC01 EQU 4*67H ; (WORD) Additional system configuration info
-019E SYBC02 EQU 4*67H+2 ; (WORD) Additional system configuration info
;
-C000 CBWRAP EQU 0C000H ; Magic number which causes CB to wrap to 00000
```


565		INCLUDE PEG:WINCHSTR.EQU
566	,	WINCHESTER PORT ADDRESSES AND EQUATES
567	WINRBT EQU	31H ,PORT FOR WINCH RESET
568	WRBTCH EQU	00010000B ,RESET WINCHESTER
569		END

-0031
-0010


```

1 ; *****
2 ; TITLE - BELDSR - System beeper device service routine *****
3 ; ABSTRACT - This module contains the routines that handle the speaker I/O. *
4 ; It includes a user interface as well as routines used internally to *
5 ; the ROM. *
6 ; *
7 ; COMPUTER - BOBB assembly language (BSD) *
8 ; * The DSR entry was converted to be table driven from the opcodes in order *
9 ; to allow additional calls to be added. The new opcodes are listed below. *
10 ; *****
11
12 NAME BELDSR - System beeper device service routine
13 ;
14 ; PUBLIC DEFINITIONS
15 ;
16 PUBLIC BEEP
17 PUBLIC BEEPIO
18 PUBLIC BEEPOF
19 PUBLIC LBEEP
20 PUBLIC SBEEP
21 ;
22 ; EXTERNAL REFERENCES
23 ;
24 ; SECT. ROMDAT
25
26 EXTRN BELEVT:WORD ; System Bell timing event block (ROMDAT)
27 EXTRN DSKC_M:BYTE ; RAM copy of disk control port (ROMDAT)
28 SECT ROMCOD
29 EXTRN ROMTST:NEAR ; CRC calculation routine (PUPTST)
30 EXTRN DELAY:NEAR ; 1 Millisecond delay subroutine (ROMUTL)
31 EXTRN DSPERZ:NEAR ; Print error routine (OUTPUT)
32 EXTRN MSG:NEAR ; Print string routine (OUTPUT)
33 EXTRN GCONF1:NEAR ; Get pointer to system configuration (CONFIG)
34 EXTRN GCONF2:NEAR ; Get pointer to system configuration (CONFIG)
35 EXTRN RCONF1:NEAR ; Return Extra Configuration status (CONFIG)
36 ;
37 ; LOCAL CONSTANTS
38 ;
39 BELTMR EQU 1563 ; 1563 ==) 800 Hz for system beeper
40 BEPLEN EQU 10 ; normal system beep length = 250 msec
41 LBPLEN EQU 40 ; Long beep = 1 sec
42 SBPLEN EQU 3 ; short beep = 75 msec
43 ;
44 ; EXTERNAL EQU FILES
45 ;
46 INCLUDE PEG:LATCHES.EQU
47 INCLUDE PEG:PORTADDR.EQU
48 INCLUDE PEG:TIMERS.EQU
49 INCLUDE PEG:TIMEVT.EQU
50 INCLUDE PEG:VECTOR.EQU
    
```

-0000

-0000

-061B
 -000A
 -002B
 -0003

```

251 *****
252 *****
253 *****
254 *****
255 *****
256 *****
257 *****
258 *****
259 *****
260 *****
261 *****
262 *****
263 *****
264 *****
265 *****
266 *****
267 *****
268 *****
269 *****
270 *****
271 *****
272 *****
273 *****
274 *****
275 *****
276 *****
277 *****
278 *****
279 *****
280 *****
281 *****
282 *****
283 *****
284 *****
285 *****
286 *****
287 *****
288 *****
289 *****
290 *****
291 *****
292 *****
293 *****
294 *****
295 *****
296 *****
297 *****
298 *****
299 *****
300 *****
301 *****
302 *****

-0000

SECT ROMCOD
ASSUME CB:ROMCOD

*****
CODE SEGMENT DEFINITION
*****

MODULE ENTRY POINT
*****
BELL (or BEEPER) DSR - Entry point for ROM user calls - INT 4BH

INPUT: AH - Function:
        0 - Sound the beeper for specified time (@ current frequency)
          AL - Beep time in 25 millisecond increments
        1 - Get beeper status, returned in Z-flag:
          ZF = 0 if beeper is currently enabled
          ZF = 1 if beeper is not enabled (no sound)
        2 - Set beep frequency
          CX - timer value (see routine header for explanation)
        3 - Beeper ON
        4 - Beeper OFF
        5 - Halt subroutine
        6 - CRC 16 subroutine ES:BX=data address, BP=block size
          DX=returned CRC, ZF=1 if DX=0
        7 - Print message @F400:SI terminated by zero.
        8 - Display system error code in reg BX
        9 - Get pointer to system configuration in ES:BX
        A - Get pointer to extra system configuration in ES:BX
        B - Return Extra sys config. info in AX,BX,CX
          )8 - Invalid opcode

OUTPUT: as noted above
USED:  AX, CX (see individual routine headers)
STACK: 14 bytes (BEEPER is worst-case)
ASSUME DB:ROMDAT
-----
0000' FB
0000' 1E
0001' 2EBE 1E C180
0002' EB 001A
000A' 1F
000B' CA 0002
-000C
000E' 004C"
0010' 0052"
0012' 0062"
0014' 0070"
0016' 0004"
0018' 0003"
001A' 0006"

BEEPIO PROC FAR
BTI
PUSH DB
MOV DB,WORD PTR CB:DSADDR+CSHRAP ; Set up my DB
CALL DOBEEP ; Do it
POP DB
RET 2 ; *** RETURN *** (Simulated IRET to pass flags)
EQU $-2
JMPTBL
DW OFFSET BEEPST ;1 Beep status
DW OFFSET BEEPF0 ;2 Get beeper frequency
DW OFFSET BEEPON ;3 Turn on beeper (does not go off)
DW OFFSET BEEPOF ;4 Turn off beeper (used by timer dsr)
DW OFFSET DELAY ;5 Delay subroutine
DW OFFSET ROMTST ;6 CRC16 calculation
DW OFFSET MS0 ;7 Print message in ROMCOD CS
  
```

```

001C' 0005"
001E' 0007"
0020' 0008"
0022' 0009"
-001B
0024' 08 E4
0026' 74 12
0028' BA C4
002A' 98
002B' D1 E0
002D' 3C 18
002F' 77 1A
0031' 93
0032' 2EBB 9F 000C"
0037' 93
003B' FF E0
003A'
003A' 3C 00
003C' 74 0D
003E' 30 E4
0040' A3 0000"
0043' 80 0E 0000" B0
004B' EB 0017
004B' C3
004C'
004C' F6 06 0002" 01
0051' C3

303 DW OFFSET DSPERZ ;B Display system error code
304 DW OFFSET GCONF0 ;9 Get pointer to system configuration
305 DW OFFSET GCONF1 ;A Get pointer to extra system configuration
306 DW OFFSET RCONF0 ;B Return extra configuration info
307 JMPSTZ EQU
308 ,
309 DOBEEP PROC
310 OR AH, AH ; Dispatch to function specified by AH
311 JZ BEEPER ; AH = 0
312 MOV AL, AH ; Convert opcode to jump table index
313 CBW ; Make into a word
314 SHL AX, 1 ; Index into word table
315 CMP AL, OFFSET JMPTSZ; 0: Legal routine?
316 JA BPRET ; N: Just return
317 XCHG AX, BX ; Y: Get routine entry without screwing BX
318 MOV BX, WORD PTR CS:JMPTBL[BX]; Get routine address
319 XCHG AX, BX
320 JMP AX ; And go to it
321 ; *****
322 ; BEEPER - Sound the beeper for the specified time (at current frequency)
323 ;
324 ; INPUT: DS points to ROMDAT
325 ; AL = Beep time in 25 millisecond increments
326 ; OUTPUT: ** beeeeeep **
327 ; USED: AX
328 ; STACK: 6 bytes
329 ASSUME DS: ROMDAT
330 ;
331 BEEPER PROC NEAR
332 CMP AL, 00
333 JZ BPRET ; 0: Did the user say 00 ?
334 XOR AH, AH ; Y: Then exit without sound
335 MOV BELEV+IEVCTR, AX; Put the count into the event block
336 OR BYTE PTR BELEV+IEVFLG, 1 SHL EVTSTA ; Set the active flag
337 CALL BEEPON ; Turn on the beeper
338 BPRET:
339 RET ; *** RETURN ***
340 ;
341 ; *****
342 ; BEEPST - Return beeper status
343 ;
344 ; INPUT: DS points to ROMDAT
345 ; OUTPUT: ZF = 0 if speaker is enabled
346 ; ZF = 1 if speaker is not enabled (no sound)
347 ; USED: (none)
348 ; STACK: 2 bytes
349 ASSUME DS: ROMDAT
350 ;
351 BEEPST PROC NEAR
352 TEST DS:SKC_M, SPKREN ; Look at the speaker bit in memory
353 RET ; *** RETURN ***
354 ; *****

```

```

355 ; BEEPF0 - This subroutine is used to set the frequency of the timer
356 ; that drives the beeper speaker. The timer's input frequency is
357 ; 1.25 MHz and the value passed in CX is used as a divider for this
358 ; frequency. For example, the system timer (800 HZ) uses a value
359 ; of (1,250,000 Hz / 800 Hz) = 1563. Note that the caller's interrupt
360 ; status is preserved.
361 ;
362 ; INPUT: CX = Frequency 'value' ( 1.25 MHz / CX = true freq.)
363 ; OUTPUT: (none)
364 ; USED: AL,CX
365 ; STACK: 4 bytes
366 ; ASSUME DS:ROMDAT
367 ;

```

```

0052' 9C
0053' FA
0054' B0 36
0056' E6 17
0058' BA C1
005A' E6 14
005C' BA C5
005E' E6 14
0060' 9D
0061' C3

BEEPF0 PROC NEAR
    PUSHF
    CLI
    MOV AL,(TC_BCO + TC_WRD + TC_MD3) ; ; Protect this
    OUT TIMCMD,AL ; ;
    MOV AL,CL ; ; 1.25 MHz / CX = frequency
    OUT TIMERO,AL ; ;
    MOV AL,CH ; ;
    OUT TIMERO,AL ; ;
    POPF
    RET
; ***** ; *** RETURN ***
; BEEPON - This subroutine is called to turn the system beeper on.
; Timing services typically is used to turn it back off.
; Note that the caller's interrupt status is preserved.
;
; INPUT: DS points to ROMDAT
; OUTPUT: (beeper turned on)
; USED: AL
; STACK: 4 bytes
; ASSUME CB:ROMCOD, DS:ROMDAT
;

```

```

0062' 9C
0063' FA
0064' A0 0002
0067' 0C 01
0069' E6 00
006B' A2 0002
006E' 9D
006F' C3

BEEPON PROC NEAR
    PUSHF
    CLI
    MOV AL,DSKC_M
    OR AL,BPKREN
    OUT DSKC_P,AL
    MOV DSKC_M,AL
    POPF
    RET
; ***** ;
; BEEPF0 - This subroutine is called by timing services to turn the
; system beeper off. The event obviously has to be (re)started
; before this can happen. Note that the caller's interrupt
; status is preserved.
;
; INPUT: DS points to ROMDAT (only because that's where event block is)
;

```

```

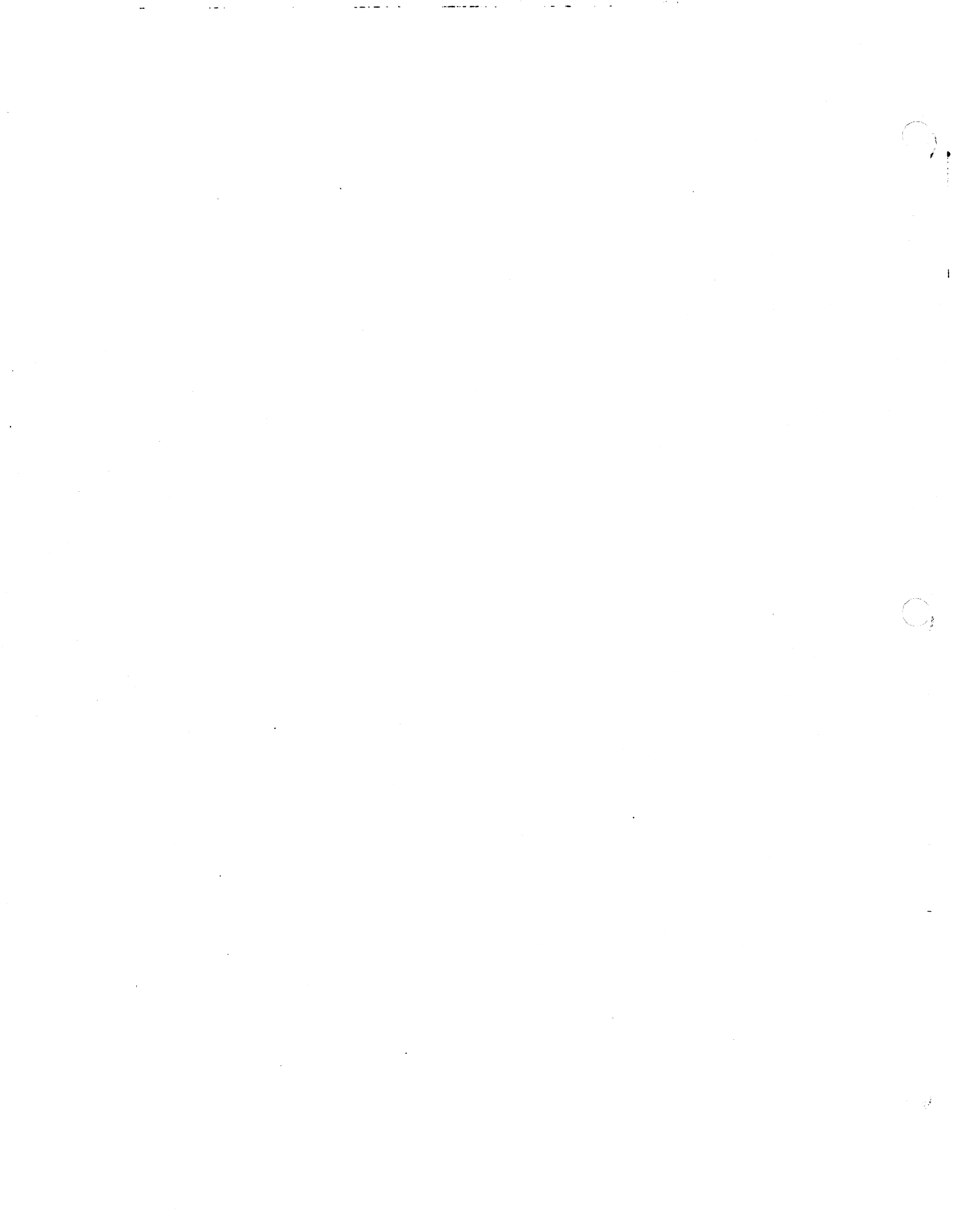
; Save interrupt status
; Protect this
; Get copy from RAM
; Turn the speaker on
;
; Interrupts back on
; *** RETURN ***
; *****
; BEEPF0 - This subroutine is called by timing services to turn the
; system beeper off. The event obviously has to be (re)started
; before this can happen. Note that the caller's interrupt
; status is preserved.
;
; INPUT: DS points to ROMDAT (only because that's where event block is)

```

```

0070' 9C
0071' FA
0072' A0 0002"
0073' 24 FE
0077' E6 00
0079' A2 0002"
007C' 9D
007D' C3

407 ; OUTPUT: (beeper turned off)
408 ; USED: AL
409 ; STACK: 4 bytes
410 ; ASSUME CB: ROMCOD, DS: ROMDAT
411 ;
412 BEEPOF PROC NEAR
413 PUSHF
414 CLI
415 MOV AL, DSKC_M
416 AND AL, NOT SPKREN
417 OUT DSKC_P.AL
418 MOV DSKC_M.AL
419 POPF
420 RET
421 ;
422 ; BEEP - BEEP the system bell. Note the three entry points:
423 ;
424 ; CALL LBEEP - Approx. 1 sec (used for error conditions)
425 ; CALL SBEEP - Approx. 75 msec (used for prompting situations)
426 ; CALL BEEP - Approx. 250 msec (normal bell)
427 ;
428 ; INPUT: (none)
429 ; OUTPUT: *** BEEP ***
430 ; USED: AX
431 ; STACK: 14 bytes
432 ; ASSUME CB: ROMCOD, DS: ROMDAT
433 ;
434 LBEEP PROC NEAR
435 MOV AH, LBPLEN
436 JMP SHORT BEEP1
437 ;
438 SBEEP PROC NEAR
439 MOV AH, SBPLEN
440 JMP SHORT BEEP1
441 ;
442 BEEP PROC NEAR
443 MOV AH, BEPLEN
444 JMP SHORT BEEP1
445 BEEP1:
446 PUSH DS
447 MOV DS, WORD PTR CS: DSADDR+CSWRAP ; Set up my DS
448 CX
449 MOV CX, BELTMR
450 CALL BEEPFQ
451 POP CX
452 MOV AL, AH
453 CALL BEEPER
454 POP DS
455 RET
456 ;
457 END
    
```




```

1 ; *****
2 ; TITLE - CLKDSR - Time-of-day clock I/O and service routines *****
3 ; ABSTRACT - This module contains the time-of-day clock routines; the
4 ; setting and reading routines, as well as the service routine which
5 ; is called every 100 milliseconds by the timer interrupt service
6 ; routine to keep the T.O.D. clock ticking.
7 ; *****
8 ; NAME CLKDSR - Time-of-day clock I/O and service routines *****
9 ; SUBTTL EQU OFFFHH
11 ; DEBUG EQU OFFFHH
12 ;
13 ;
14 ; PUBLIC DEFINITIONS
15 ;
16 ; PUBLIC CLK_IO
17 ; PUBLIC CLKSRV
18 ; PUBLIC SETDAT
19 ; PUBLIC SETTIM
20 ;
21 ;
22 ; EXTERNAL REFERENCES
23 ;
24 ; SECT ROMDAT ; Time-of-day clock - days since 1/1/80(ROMDAT)
25 ; EXTRN DATE:WORD ; Time-of-day clock - hours (ROMDAT)
26 ; EXTRN HOURS:BYTE ; Time-of-day clock - hundredths (ROMDAT)
27 ; EXTRN HUNB:BYTE ; Time-of-day clock - minutes (ROMDAT)
28 ; EXTRN MINS:BYTE ; Time-of-day clock - seconds (ROMDAT)
29 ;
30 ; INCLUDE PEG:VECTOR.EQU
31 ;
124 ; *****
125 ; MODULE ENTRY POINT *****
126 ;
127 ; *****
128 ;
129 ; SECT ROMCOD
130 ; ASSUME CB:ROMCOD
131 ;
132 ; TIME AND DATE ROUTINES - INT 4EH
133 ;
134 ; INPUT: AH = Function:
135 ; 0 - Set the date
136 ; 1 - Set the time
137 ; 2 - Get the date & time
138 ; 12 - NULL call
139 ; BX = Count of days since January 1, 1980
140 ; CH = Hours (0-23)
141 ; CL = Minutes (0-59)
142 ; DH = Seconds (0-59)
143 ; DL = Hundredths of seconds (0-99)
144 ; OUTPUT: (depends on function, registers same as above)
145 ; USED: AX,BX,CX,DX
146 ; STACK:

```



```

147 ABSUME CB:ROMCOD, DS:NOTHING
148 ,
149 CLK_IO PROC FAR
150 STI
151 PUSH
152 MOV DS,WORD PTR CS:DSADDR+CSWRAP ; point to our DS
153 PUSH BX
154 MOV BH,AH ; 'opcode' in BH
155 POP AX ; date, if any, in AX
156 CALL DO_CLK ; do the function
157 POP DS
158 IRET
159 ; *** RETURN ***
160 DO_CLK PROC NEAR ; dispatch to function specified by BH
161 BH,BH ; BH = 0
162 JZ SETDAT ; BH = 1
163 DEC BH
164 JZ SETTIM ; BH = 2
165 DEC BH
166 JZ GETDAT
167 RET
168 ; *** RETURN *** (Invalid command)
169
170 ; SET THE DATE
171 ; INPUT: AX = Count of days since January 1, 1980
172 ; OUTPUT: (none)
173 USED: AX
174 STACK: 2 bytes
175 ASSUME CB:ROMCOD, DS:ROMDAT
176
177 SETDAT PROC NEAR
178 MOV DATE,AX ; Store the date
179 RET
180
181 ; SET THE TIME
182 ; INPUT: CH = hours (0-23)
183 ; CL = minutes (0-59) (all numbers are binary)
184 ; DH = seconds (0-59)
185 ; DL = hundredths of seconds (0-99)
186
187 ; OUTPUT: (none)
188 USED: CX,DX
189 STACK: 2 bytes
190 ASSUME CB:ROMCOD, DS:ROMDAT
191
192 SETTIM PROC NEAR
193 CLI
194 MOV HOURS,CH
195 MOV MINS,CL
196 MOV SECS,DH
197 MOV HUNS,DL
198
199
200
201
202
203
204
205
206
207
208
209
210
211
212
213
214
215
216
217
218
219
220
221
222
223
224
225
226
227
228
229
230
231
232
233
234
235
236
237
238
239
240
241
242
243
244
245
246
247
248
249
250
251
252
253
254
255
256
257
258
259
260
261
262
263
264
265
266
267
268
269
270
271
272
273
274
275
276
277
278
279
280
281
282
283
284
285
286
287
288
289
290
291
292
293
294
295
296
297
298
299
300
301
302
303
304
305
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331
332
333
334
335
336
337
338
339
340
341
342
343
344
345
346
347
348
349
350
351
352
353
354
355
356
357
358
359
360
361
362
363
364
365
366
367
368
369
370
371
372
373
374
375
376
377
378
379
380
381
382
383
384
385
386
387
388
389
390
391
392
393
394
395
396
397
398
399
400
401
402
403
404
405
406
407
408
409
410
411
412
413
414
415
416
417
418
419
420
421
422
423
424
425
426
427
428
429
430
431
432
433
434
435
436
437
438
439
440
441
442
443
444
445
446
447
448
449
450
451
452
453
454
455
456
457
458
459
460
461
462
463
464
465
466
467
468
469
470
471
472
473
474
475
476
477
478
479
480
481
482
483
484
485
486
487
488
489
490
491
492
493
494
495
496
497
498
499
500
501
502
503
504
505
506
507
508
509
510
511
512
513
514
515
516
517
518
519
520
521
522
523
524
525
526
527
528
529
530
531
532
533
534
535
536
537
538
539
540
541
542
543
544
545
546
547
548
549
550
551
552
553
554
555
556
557
558
559
560
561
562
563
564
565
566
567
568
569
570
571
572
573
574
575
576
577
578
579
580
581
582
583
584
585
586
587
588
589
590
591
592
593
594
595
596
597
598
599
600
601
602
603
604
605
606
607
608
609
610
611
612
613
614
615
616
617
618
619
620
621
622
623
624
625
626
627
628
629
630
631
632
633
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648
649
650
651
652
653
654
655
656
657
658
659
660
661
662
663
664
665
666
667
668
669
670
671
672
673
674
675
676
677
678
679
680
681
682
683
684
685
686
687
688
689
690
691
692
693
694
695
696
697
698
699
700
701
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719
720
721
722
723
724
725
726
727
728
729
730
731
732
733
734
735
736
737
738
739
740
741
742
743
744
745
746
747
748
749
750
751
752
753
754
755
756
757
758
759
760
761
762
763
764
765
766
767
768
769
770
771
772
773
774
775
776
777
778
779
780
781
782
783
784
785
786
787
788
789
790
791
792
793
794
795
796
797
798
799
800
801
802
803
804
805
806
807
808
809
810
811
812
813
814
815
816
817
818
819
820
821
822
823
824
825
826
827
828
829
830
831
832
833
834
835
836
837
838
839
840
841
842
843
844
845
846
847
848
849
850
851
852
853
854
855
856
857
858
859
860
861
862
863
864
865
866
867
868
869
870
871
872
873
874
875
876
877
878
879
880
881
882
883
884
885
886
887
888
889
890
891
892
893
894
895
896
897
898
899
900
901
902
903
904
905
906
907
908
909
910
911
912
913
914
915
916
917
918
919
920
921
922
923
924
925
926
927
928
929
930
931
932
933
934
935
936
937
938
939
940
941
942
943
944
945
946
947
948
949
950
951
952
953
954
955
956
957
958
959
960
961
962
963
964
965
966
967
968
969
970
971
972
973
974
975
976
977
978
979
980
981
982
983
984
985
986
987
988
989
990
991
992
993
994
995
996
997
998
999
1000
    
```

```

0032' FB          199      BTI
0033' C3          200      RET
201
202
203
204
205      INPUT:    (none)
206      OUTPUT:  AX = count of days since 01-01-80
207              CH = hours
208              DL = minutes
209              DH = seconds
210              DL = hundredths of seconds
211      USED:    AX,CX,DX
212      STACK:  2 bytes
213      ASSUME  CB:ROMCOD, DS:ROMDAT
214
215      OETDAT PROC NEAR
216      CLI
217      MOV     AX,DATE
218      MOV     CH,HOURS
219      MOV     CL,MINS
220      MOV     DH,SECS
221      MOV     DL,HUNS
222      BTI
223      RET
224
225      CLKBRV - Time-of-day clock service routine. This routine is called
226              every 100 milliseconds by the system timer interrupt service
227              routine. It updates the time and date information kept in RAM.
228
229      INPUT:  DS points to ROMDAT
230      OUTPUT: T-o-D info in RAM is updated
231      USED:   BX
232      STACK: 2 bytes
233      ASSUME  CB:ROMCOD, DS:ROMDAT
234
235      CLKBRV PROC NEAR
236      CLI
237      MOV     BX,OFFSET MUNS
238      ADD     BYTE PTR [BX],10
239      CMP     BYTE PTR [BX],100
240      JB      CLXXIT
241      MOV     BYTE PTR [BX],0
242
243      INC     BX
244      INC     BYTE PTR [BX]
245      CMP     BYTE PTR [BX],60
246      JB      CLXXIT
247      MOV     BYTE PTR [BX],0
248
249      INC     BX
250      INC     BYTE PTR [BX]
251      CMP     BYTE PTR [BX],60
252
253
254
255
256
257
258
259
260
261
262
263
264
265
266
267
268
269
270
271
272
273
274
275
276
277
278
279
280
281
282
283
284
285
286
287
288
289
290
291
292
293
294
295
296
297
298
299
300
301
302
303
304
305
306
307
308
309
310
311
312
313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331
332
333
334
335
336
337
338
339
340
341
342
343
344
345
346
347
348
349
350
351
352
353
354
355
356
357
358
359
360
361
362
363
364
365
366
367
368
369
370
371
372
373
374
375
376
377
378
379
380
381
382
383
384
385
386
387
388
389
390
391
392
393
394
395
396
397
398
399
400
401
402
403
404
405
406
407
408
409
410
411
412
413
414
415
416
417
418
419
420
421
422
423
424
425
426
427
428
429
430
431
432
433
434
435
436
437
438
439
440
441
442
443
444
445
446
447
448
449
450
451
452
453
454
455
456
457
458
459
460
461
462
463
464
465
466
467
468
469
470
471
472
473
474
475
476
477
478
479
480
481
482
483
484
485
486
487
488
489
490
491
492
493
494
495
496
497
498
499
500
501
502
503
504
505
506
507
508
509
510
511
512
513
514
515
516
517
518
519
520
521
522
523
524
525
526
527
528
529
530
531
532
533
534
535
536
537
538
539
540
541
542
543
544
545
546
547
548
549
550
551
552
553
554
555
556
557
558
559
560
561
562
563
564
565
566
567
568
569
570
571
572
573
574
575
576
577
578
579
580
581
582
583
584
585
586
587
588
589
590
591
592
593
594
595
596
597
598
599
600
601
602
603
604
605
606
607
608
609
610
611
612
613
614
615
616
617
618
619
620
621
622
623
624
625
626
627
628
629
630
631
632
633
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648
649
650
651
652
653
654
655
656
657
658
659
660
661
662
663
664
665
666
667
668
669
670
671
672
673
674
675
676
677
678
679
680
681
682
683
684
685
686
687
688
689
690
691
692
693
694
695
696
697
698
699
700
701
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719
720
721
722
723
724
725
726
727
728
729
730
731
732
733
734
735
736
737
738
739
740
741
742
743
744
745
746
747
748
749
750
751
752
753
754
755
756
757
758
759
760
761
762
763
764
765
766
767
768
769
770
771
772
773
774
775
776
777
778
779
780
781
782
783
784
785
786
787
788
789
790
791
792
793
794
795
796
797
798
799
800
801
802
803
804
805
806
807
808
809
810
811
812
813
814
815
816
817
818
819
820
821
822
823
824
825
826
827
828
829
830
831
832
833
834
835
836
837
838
839
840
841
842
843
844
845
846
847
848
849
850
851
852
853
854
855
856
857
858
859
860
861
862
863
864
865
866
867
868
869
870
871
872
873
874
875
876
877
878
879
880
881
882
883
884
885
886
887
888
889
890
891
892
893
894
895
896
897
898
899
900
901
902
903
904
905
906
907
908
909
910
911
912
913
914
915
916
917
918
919
920
921
922
923
924
925
926
927
928
929
930
931
932
933
934
935
936
937
938
939
940
941
942
943
944
945
946
947
948
949
950
951
952
953
954
955
956
957
958
959
960
961
962
963
964
965
966
967
968
969
970
971
972
973
974
975
976
977
978
979
980
981
982
983
984
985
986
987
988
989
990
991
992
993
994
995
996
997
998
999
1000

```

006A'	72 11	251	JB	CLKXIT	...	N: that's all
006C'	C6 07 00	252	MOV	BYTE PTR [BX],0	...	Y: reset minutes
006F'	43	253				
0070'	FE 07	254	INC	BX	...	point to hours
0072'	80 3F 18	255	INC	BYTE PTR [BX]		
0073'	72 06	256	CMP	BYTE PTR [BX],24	...	G: 24 7
0077'	C6 07 00	257	JB	CLKXIT	...	N: that's all
		258	MOV	BYTE PTR [BX],0	...	Y: reset hours
007A'	43	259				
007B'	FF 07	260	INC	BX	...	point to days
007D'	FB	261	INC	WORD PTR [BX]	...	bump it
007E'	C3	262				
		263	STI			
		264	RET			
		265			...	*** RETURN ***
		266	END			

No errors detected


```

1 *****
2 ; TITLE - CONFIO - Return system configuration
3 ; ABSTRACT - This routine is a ROM function (software interrupt) that
4 ; returns the current system configuration, as determined during
5 ; powerup initialization.
6 *****
7 NAME CONFIO - Return System Configuration
8 SUBTTL
9
10
11
12 PUBLIC CFG_IO
13 PUBLIC OCONFG
14 PUBLIC OCONF1
15 PUBLIC RCONF0
16
17
18
19
20 SECT ROMDAT
21 EXTRN SYSCON:WORD ; System configuration storage
22 *****
23 LOCAL CONSTANTS
24 *****
25 INCLUDE PEG:VECTOR.EGQU
26
27
28
29
30
31
32
33
34
35
36
37
38
39
40
41
42
43
44
45
46
47
48
49
50
51
52
53
54
55
56
57
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100
101
102
103
104
105
106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
121
122
123
124
125
126
127
128
129
130
131
132
133
134
135
136
137
138
139
140
141
142
143
144
145

```

-0000

-0000

0000' FB
 0000' IE
 0001' 2EBE IE C180
 0002' A1 0001"
 0007' 2EBB IE C198

CFIO_IO PROC FAR
 STI
 PUSH DB ; Interrupts back on
 MOV DS,word ptr CS:DSADDR+CSWRAP ; Point to ROMDAT
 MOV AX,SYSCON ; Pick up config word
 MOV BX,word ptr CS:MEMSIZ+CSWRAP ; Pick up memory size


```
000F' IF DB
0010' CF IRET
146 POP DB
147 IRET
148
149 *****
150 RCONFO - Returns a pointer to system configuration word and other useful
151 information. Called NEAR from general ROM I/O handler in BELDSR.
152
153 INPUT: (none)
154 OUTPUT: ES:BX = pointer to system configuration info
155 ES:[BX-3] = DSKC_M copy of DSKC_P latch
156 ES:[BX-2] = PRCD_M copy of PRCD_P latch
157 ES:[BX-1] = DSKS_M copy of DSKS_P latch
158
159 USED: BX,ES
160 STACK:
161
162 RCONFO PROC NEAR
163 MOV BX,offset SYSCON ; point to config area (OFFSET)
164 PUSH DS ; and set up segment
165 POP ES
166 RET
167 *****
168 RCONF1 - Returns a pointer to other system information.
169 Called NEAR from general ROM I/O handler in BELDSR.
170
171 INPUT: (none)
172 OUTPUT: ES:BX = pointer to additional system configuration info
173 ES:[BX-3] = (MEMSIZ) - Size of memory in 16 byte words (word)
174 ES:[BX-1] = (INTCTR) - Pending interrupt count (byte)
175 ES:[BX+0] = (DRVBYT) - Drive type byte
176 ES:[BX+1] = (SYSC01) - Additional system configuration byte
177 ES:[BX+2] = (SYSC02) - Additional system configuration word
178
179 USED: BX,ES
180 STACK:
181
182 RCONF1 PROC NEAR
183 XOR BX,BX ; Set up segment of config area
184 MOV ES,BX
185 MOV BX,offset DRVBYT ; Offset of config area
186 RET
187 *****
188 RCONFO - Return extra system configuration information in registers.
189 Called NEAR from general ROM I/O handler in BELDSR.
190
191 INPUT: (none)
192 OUTPUT: AL = DRVBYT - Drive type byte
193 AH = INTCTR - Pending interrupt count (not very useful)
194 BX = SYSC01 - Extra system configuration word #1
195 CX = SYSC02 - Extra system configuration word #2
196
197 USED: AX,BX,CX
198 STACK:
```

001F'	2EA1 C19A	198	RCONFO	PROC	NEAR		
001F'	86 C4	199	MOV	MOV	AX:word ptr CB:INTCTR+CBWRAP		; Pick up INTCTR and DRVBYT
0023'	2EBB 1E C19C	200	XCHG	AL:AH			; Put in correct registers
0025'	2EBB 1E C19E	201	MOV	BX:word ptr CB:BYSCD1+CSWRAP			; Pick up BYSCD1
002A'	2EBB 0E C19E	202	MOV	CX:word ptr CB:BYSCD2+CBWRAP			; Pick up BYSCD2
002F'	C3	203	RET				
		204	END				

No errors detected

1 *****
2 TITLE - CRT DSR
3
4 COMPUTER - BOBB ASSEMBLY LANGUAGE
5
6 ABSTRACT - This module contains the necessary set of routines needed
7 for interacting with the CRT. The interface to these routines
8 is via a software interrupt (INT 49H) with the CRT function
9 number in register AH. Additional parameters required by each
10 function and their register designations are described below
11 as INPUT under the given function.
12 *****

13
14 SET CRT MODE
15 AH = 00H
16

17 THIS ROUTINE IS USED TO SET THE CRT MODE (NOTE: this is not
18 applicable to PEGASUS but is provided for compatibility with
19 the IBM CRT calling conventions), IT DOES NOTHING AND SIMPLY
20 RETURNS.
21

22 INPUT - AH = 0
23 AL = 1-7 (ignored)
24
25 OUTPUT - None
26
27 USED - None
28
29

30
31 SET CURSOR TYPE BASED ON REGISTER CX
32 AH = 01H
33

34 INPUT - CH = Bits 4-0 cursor start line (0-B hex)
35 Bits 6-5 cursor type i.e. 00 = no blink
36 01 = no cursor
37 10 = 1/16 blink
38 11 = 1/32 blink
39

40 CL = Bits 4-0 cursor end line (0-B hex)
41 Bits 7-5 not used
42

43 OUTPUT - none
44
45 USED - AX, CX, DL
46
47

48 SET CURSOR AT COLUMN(DH),ROW(DL) POSITION
49 AH = 02H
50

51 INPUT - DH = X coordinate (column)
52 DL = Y coordinate (row)

53
54
55
56
57
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100
101
102
103
104

OUTPUT - CURSOR WRITTEN AT (X,Y) COORDINATES

READ CURRENT CURSOR POSITION
AH = 03H

THIS ROUTINE READS THE CURRENT CURSOR POSITION AND RETURNS
THE POSITION IN DX AS (COLUMN,ROW), (X,Y).

INPUT - NONE

OUTPUT - DH = (column), DL = (row)
CH, CL = Cursor type

USED - DX, CX, AX, BX,

READ LIGHT PEN POSITION
AH = 04H

THIS ROUTINE READS THE LIGHT PEN AND RETURNS IT'S POSITION
TO THE CALLER.

INPUT - NONE

OUTPUT - RETURN NO ACTION FOR NOW

USED -

SET ACTIVE DISPLAY PAGE
AH = 05H

THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE (IT DOES NOTHING)
AND IS PROVIDED FOR IBM COMPATIBILITY ONLY.

SCROLL TEXT BLOCK UP
AH = 06H

SCROLL TEXT BLOCK DOWN
AH = 07H

THIS ROUTINE "SCROLLS" THE SCREEN IN EITHER DIRECTION BY
SIMPLY PERFORMING A MOVES FROM THE SOURCE TO THE DESTINATION
LOCATION OF THE SCREEN. THIS METHOD PROVIDES FOR DEFINING

A WINDOW AND MOVING IT TO ANOTHER POSITION ON THE SCREEN.
THE SOURCE WINDOW BOUNDARY COORDINATES ARE AS FOLLOWS AND
THE SAME APPLIES FOR THE DESTINATION COORDINATES:

Upper left = (src. col., src row)
Upper right = (src. col. + col. cnt., src. row)
Lower left = (src. col., src. row + line cnt.)
Lower right = (src. col. + col. cnt., src. row + line cnt.)

INPUT - AL = 0 blank out source text
* AL) 0 Don't blank source
** (DH,DL) = (Source column, Source row) of scroll
(BH,BL) = (Destination column, Destination row) of scroll
CH = Column count(1-80) CL = Line count(1-25)

* NOTE: If the source and destination windows
overlap then some of the source will
be overwritten.

** NOTE: If destination is less than source
then the scroll is a scroll up and
to the left, if source is less than
destination then the scroll is a scroll
down and to the right.

Example: You want to scroll the screen up "n"
lines.

input - source col,row (DH,DL) = (0,n)
dest. col,row (BH,BL) = (0,0)
column count (CH) = 80
line count (CL) = 25-n

OUTPUT - NONE

USED -

READ CHARACTER AND ATTRIBUTE AT CURRENT CURSOR POSITION
AH = OBH

THIS ROUTINE READS AND RETURNS THE CHARACTER AND ATTRIBUTE AT
THE CURRENT CURSOR POSITION.

INPUT - NONE

OUTPUT - AH = Attribute of character read
AL = character read

USED - AX,

105 ,
106 ,
107 ,
108 ,
109 ,
110 ,
111 ,
112 ,
113 ,
114 ,
115 ,
116 ,
117 ,
118 ,
119 ,
120 ,
121 ,
122 ,
123 ,
124 ,
125 ,
126 ,
127 ,
128 ,
129 ,
130 ,
131 ,
132 ,
133 ,
134 ,
135 ,
136 ,
137 ,
138 ,
139 ,
140 ,
141 ,
142 ,
143 ,
144 ,
145 ,
146 ,
147 ,
148 ,
149 ,
150 ,
151 ,
152 ,
153 ,
154 ,
155 ,
156 ,

157 ,
158 ,
159 ,
160 ,
161 ,
162 ,
163 ,
164 ,
165 ,
166 ,
167 ,
168 ,
169 ,
170 ,
171 ,
172 ,
173 ,
174 ,
175 ,
176 ,
177 ,
178 ,
179 ,
180 ,
181 ,
182 ,
183 ,
184 ,
185 ,
186 ,
187 ,
188 ,
189 ,
190 ,
191 ,
192 ,
193 ,
194 ,
195 ,
196 ,
197 ,
198 ,
199 ,
200 ,
201 ,
202 ,
203 ,
204 ,
205 ,
206 ,
207 ,
208 ,

WRITE CHARACTER AND ATTRIBUTE AT CURRENT CURSOR POSITION
AH = 09H

THIS ROUTINE IS USED TO WRITE A CHARACTER AND ASSOCIATED
ATTRIBUTE AT THE CURRENT CURSOR POSITION. THE USER SHOULD
NOTE THAT THE CURSOR IS NOT AUTOMATICALLY ADVANCED WITH THIS
ROUTINE.

INPUT - AL = Character to write
BL = Attribute of character
CX = Count of chars. to write

OUTPUT - None

USED - AX, BL, CX

WRITE CHARACTER ONLY AT CURRENT CURSOR POSITION
AH = 0AH

THIS ROUTINE IS USED FOR WRITING A CHARACTER ONLY AT
THE CURRENT CURSOR POSITION. IT IS ALSO USED BY THE
ABOVE PROCEDURE BUT IGNORES THE ATTRIBUTE PARAMETER.
AGAIN THE USER SHOULD NOTE THAT THE CURSOR IS NOT AUTO-
MATICALLY ADVANCED AND IS LEFT AT IT'S ORIGINAL POSITION.

INPUT - AL = Character to write
CX = Count of chars. to write

OUTPUT - None

USED - AX, CX

SET GRAPHICS COLOR PALETTE
AH = 0BH

THIS ROUTINE IS USED TO SET THE COLOR PALETTE FOR THE GRAPHICS
BANKS A, B, C.

INPUT -

OUTPUT - Palette set accordingly

USED -

209 |
210 |
211 |
212 |
213 |
214 |
215 |
216 |
217 |
218 |
219 |
220 |
221 |
222 |
223 |
224 |
225 |
226 |
227 |
228 |
229 |
230 |
231 |
232 |
233 |
234 |
235 |
236 |
237 |
238 |
239 |
240 |
241 |
242 |
243 |
244 |
245 |
246 |
247 |
248 |
249 |
250 |
251 |
252 |
253 |
254 |
255 |
256 |
257 |
258 |
259 |
260 |

WRITE GRAPHICS PIXEL AT (X,Y) LOCATION

AH = OCH

THIS ROUTINE IS USED TO WRITE A GRAPHICS PIXEL AT THE SPECIFIED LOCATION.

INPUT - (To be determined)

OUTPUT -

USED -

READ GRAPHICS PIXEL AT (X,Y) LOCATION

AH = ODH

THIS ROUTINE IS USED TO READ A GRAPHICS PIXEL AT THE SPECIFIED LOCATION.

INPUT - (To be determined)

OUTPUT -

USED -

ASCII TELETYPE WRITE ROUTINE

AH = OEH

THIS ROUTINE IS USED TO WRITE CHARACTERS TO THE SCREEN IN AN ASCII TELETYPE MANNER. WRITING STARTS AT THE CURRENT CURSOR POSITION AND THE CURSOR IS ADVANCED AUTOMATICALLY TO ITS NEXT LOCATION. THE SCREEN IS SCROLLED AUTOMATICALLY IF NEED BE (i.e. writing past the end of screen)* AND CONTROL CHARACTERS ARE EXECUTED (CR,LF,BB and BEL) INSTEAD OF WRITTEN.

* NOTE: If a status line is currently being implemented a scroll will occur on the line previous to the start of the status region as if that line were the end of the screen.

INPUT - AL = Character to write

OUTPUT - None

USED -

261 |
262 |
263 |
264 |
265 |
266 |
267 |
268 |
269 |
270 |
271 |
272 |
273 |
274 |
275 |
276 |
277 |
278 |
279 |
280 |
281 |
282 |
283 |
284 |
285 |
286 |
287 |
288 |
289 |
290 |
291 |
292 |
293 |
294 |
295 |
296 |
297 |
298 |
299 |
300 |
301 |
302 |
303 |
304 |
305 |
306 |
307 |
308 |
309 |
310 |
311 |
312 |

CURRENT VIDED STATE
AH = OFH

THIS ROUTINE IS USED TO RETURN THE STATE (OR MODE) OF THE CRT TO THE CALLER (NOTE: THIS IS NOT APPLICABLE TO PEGASUS BUT IS PROVIDED FOR COMPATIBILITY WITH THE IBM CRT CALLING CONVENTIONS). IT DOES NOTHING AND SIMPLY RETURNS.

INPUT - None

OUTPUT - (To be determined)

USED -

WRITE BLOCK OF ATTRIBUTES & CHARS @ CURSOR
AH = 10H

THIS ROUTINE IS USED TO WRITE A GIVEN BLOCK OF CHARACTERS TO THE SCREEN STARTING AT THE CURSOR USING THE SPECIFIED ATTRIBUTE. ALL CONTROL CHARACTERS (I.E. CR, LF, BEL ETC.) ARE IGNORED AS SUCH AND PRINTED ON THE SCREEN AS ASCII CHARACTERS.

INPUT - AL = Attribute of characters to write
DX = Segment location of character block
BX = Offset location of character block
CX = Length of character block (not to exceed 2000)

NOTE: This routine does not increment the cursor location. The caller must be aware of the cursor location before using this routine and make sure that the block will "fit" on the screen or he will lose what won't "fit". *

OUTPUT - None

USED - None

* CX must be (or = the number of char. positions left on the screen. CX (block length) must be (or = 2000 - ((cur. row - 1 * 80) + cur. col.) Attempting to write characters beyond the end of screen will cause them to be lost (not written).

WRITE BLOCK OF CHARS. ONLY @ CURSOR
AH = 11H

THIS ROUTINE IS USED TO WRITE A GIVEN BLOCK OF CHARACTERS TO THE SCREEN STARTING AT THE CURSOR ALL CONTROL CHARACTERS

(i.e. CR,LF,BEL etc.) ARE IGNORED AS SUCH AND PRINTED ON THE SCREEN AS ASCII CHARACTERS.

INPUT - DX = Segment location of character block
BX = Offset location of character block
CX = Length of character block (not to exceed 2000)

NOTE: This routine does not increment the cursor location. The caller must be aware of the cursor location before using this routine and make sure that the block will "fit" on the screen or he will lose what won't "fit". * With this type of write the characters will take on the attributes of the last attribute change.

OUTPUT - None

USED - None

* CX must be (or = the number of char. positions left on the screen.
CX (block length) must be (or = 2000 - ((cur. row - 1 * 80) + cur. col.)
Attempting to write characters beyond the end of screen will cause them to be lost (not written).

CHANGE ENTIRE SCREEN ATTRIBUTES

AH = 12H

THIS ROUTINE CHANGES THE ENTIRE SCREEN ATTRIBUTES
RIGHT BEFORE YOUR VERY EYES

INPUT - AL = ATTRIBUTE TO USE

OUTPUT - None

USED - None

CLEAR THE CRT SCREEN MEMORY AND HOME CURSOR

AH = 13H

INPUT - None

OUTPUT - None

USED - AX, CX, DL, DI

313
314
315
316
317
318
319
320
321
322
323
324
325
326
327
328
329
330
331
332
333
334
335
336
337
338
339
340
341
342
343
344
345
346
347
348
349
350
351
352
353
354
355
356
357
358
359
360
361
362
363
364

365 ,
366 , CLEAR GRAPHICS MEMORY AND HOME THE PIXEL CURSOR
367 , AH = 14H
368 ,

369 , THIS ROUTINE IS USED TO CLEAR ALL THREE GRAPHICS BANKS AND HOME
370 , THE PIXEL CURSOR (i.e. place cursor at (0,0)).

371 , INPUT - None

372 ,
373 ,
374 , OUTPUT - None

375 ,
376 , USED -
377 ,
378 ,

379 , SET TTY STATUS LINE.
380 , AH = 15H
381 ,
382 ,

383 , THIS ROUTINE IS USED TO DEFINE THE STARTING LINE OF THE STATUS
384 , REGION. IT IS ASSUMED THAT THIS LINE THROUGH THE END OF THE
385 , SCREEN IS RESERVED FOR STATUS INFORMATION. WHEN USING THIS IN
386 , CONJUNCTION WITH A TTY WRITE THE END OF SCREEN FOR THE TTY ROUTINE
387 , IS SET AT THE STATUS LINE MINUS ONE.

388 , INPUT - CH = 0 (always)

389 , CL = Start line number of status line (0-24 decimal)

390 ,
391 ,
392 , NOTE: CH must always be zero. This represents
393 , the first column of the status line.
394 , If an attempt is made to set a status
395 , line which falls prior to the current
396 , line of the cursor then no status line
397 , is implemented.

398 , CX = 0 = Full screen TTY.

399 ,
400 ,
401 , OUTPUT - None

402 ,
403 , USED - None
404 ,
405 ,

406 , SET ATTRIBUTE LATCH
407 , AH = 16H
408 ,
409 ,

410 , THIS ROUTINE IS USED TO WRITE THE ATTRIBUTE LATCH WITHOUT
411 , HAVING TO WRITE A CHARACTER.

412 , INPUT - BL = Attribute to use

413 ,
414 , OUTPUT - Latch set accordingly
415 ,
416 ,

```
417 |-----|
418 | *****|
419 | *** ITEMS BELOW THIS LINE WERE ADDED OPCODES IN THE V1.20 ROM ***|
420 | *****|
421 |-----|
422 | OET PHYSICAL DISPLAY BEGIN OFFSET|
423 |     AH = 17H|
424 |-----|
425 | THIS ROUTINE IS USED TO RETURN THE OFFSET (FROM DE00H) OF|
426 | THE PHYSICAL BEGINNING OF DISPLAY.|
427 |-----|
428 | INPUT - NONE|
429 |-----|
430 | OUTPUT - DX = Display begin offset|
431 |-----|
432 |-----|
433 |-----|
434 |-----|
435 | PRINT TTY STRING|
436 |     AH = 18H|
437 |-----|
438 |-----|
439 | THIS ROUTINE IS USED TO PRINT A STRING OF CHARACTERS IN THE|
440 | USERS CB: (GOTTEN FROM THE STACK) WITH A TTY WRITE.|
441 |-----|
442 | INPUT - BX = Address (offset) of the string|
443 |           Where: [BX] byte 0 = length of string|
444 |           [BX] byte 1 = first char. of string|
445 |-----|
446 | OUTPUT - NONE|
447 |-----|
448 |-----|
449 |-----|
450 |-----|
451 | NAME CRTDSR (CRT DSR CODE)|
452 | SUBTTL EQU OFFFFH|
453 | DEBUO EQU OFFFFH|
```

-FFFF

```

455 | *****
456 | PUBLIC DEFINITIONS
457 | *****
458 |
459 | PUBLIC CRTOUT
460 | PUBLIC CRT_IO
461 | PUBLIC CRTRET
462 |
463 | *****
464 | EXTERNAL REFERENCES
465 | *****
466 |
467 | SECT      ROMCOD
468 |
469 | EXTRN     BEEP: NEAR          ; BEEP BELL ROUTINE      (BELDSR)
470 | EXTRN     INILST: NEAR       ; INIT LOCAL STORAGE    (CRTTST)
471 |
472 | SECT
473 |
474 | EXTRN     CURPOS: WORD       ; CURSOR POSITION POINTER (ROMDAT)
475 | EXTRN     CURTYP: WORD       ; CURRENT CURSOR TYPE    (ROMDAT)
476 | EXTRN     THSLIN: WORD       ; CURRENT LINE POINTER   (ROMDAT)
477 | EXTRN     DISBEG: WORD       ; CURRENT DISPLAY BEGIN POINTER(ROMDAT)
478 | EXTRN     DISEND: WORD       ; CURRENT DISPLAY END POINTER (ROMDAT)
479 | EXTRN     CRTCNT: WORD       ; CURRENT CHARS ON LINE COUNT (ROMDAT)
480 | EXTRN     STATLN: WORD       ; STATUS LINE BEGINNING   (ROMDAT)
481 | EXTRN     RETFLO: BYTE       ; LOCAL RETURN FLAG      (ROMDAT)
482 | EXTRN     CRTHLD: BYTE       ; PAUSE STATUS BYTE      (ROMDAT)
483 | EXTRN     ATTSAV: BYTE       ; ATTRIBUTE LATCH SAVE   (ROMDAT)
484 |
485 | *****
486 | LOCAL CONSTANTS
487 | *****
488 |
489 | INCLUDE PEG: ASCII.EGU
490 | INCLUDE PEG: CRT.EGU
491 | INCLUDE PEG: VECTOR.EGU
492 |
493 | *****
494 | LOCAL MACROS
495 | *****
496 |
497 | SUBTTL MAIN
498 | *****
499 | MODULE ENTRY POINT
500 | *****
501 |
502 | CRT_IO - INT 49H
503 |
504 | SECT      ROMCOD
505 | *****

```


Address	Label	Code	Comments
0000'			
0000'	FB		
0001'	FC		
0002'	EB 0033		
0005'	CF		
0006'			
0006'	0066"		
0008'	0073"		
000A'	007F"		
000C'	0080"		
000E'	0066"		
0010'	0066"		
0012'	00C3"		
0014'	00C3"		
0016'	0189"		
0018'	01C8"		
001A'	01CD"		
001C'	0066"		
001E'	0066"		
0020'	0066"		
0022'	0218"		
0024'	0066"		
0026'	039C"		
0028'	03A0"		
002A'	03C4"		
002C'	03DB"		
002E'	0405"		
0030'	01EF"		
0032'	042A"		
0034'	0432"		
0036'	0439"		
		-0032	
0038'			
0038'	57		
0039'	56		
003A'	1E		
003B'	06		
003C'	53		
003D'	50		
003E'	53		
003F'	2EBE 1E C180		
0044'	BB 0000'		
0047'	BE C3		
0049'	5B		
004A'			
004A'	80 3E 000B" 00		
004F'	75 F9		
0051'	CD 57		
696			
697			
698			
699			
700			
701			
702			
703			
704			
705			
706			
707			
708			
709			
710			
711			
712			
713			
714			
715			
716			
717			
718			
719			
720			
721			
722			
723			
724			
725			
726			
727			
728			
729			
730			
731			
732			
733			
734			
735			
736			
737			
738			
739			
740			
741			
742			
743			
744			
745			
746			
747			

ASSUME CS:ROMCOD, DS:ROMDAT, ES:CRTDAT

INTERRUPTS BACK ON
 CLEAR DIRECTION FLAG
 DO IT
 *** RETURN ***

JUMP TABLE OF CRT FUNCTIONS
 SET SCREEN MODE (DOES A RETURN)
 SET CURSOR TYPE
 SET CURSOR POSITION
 READ CURSOR POSITION
 READ LIGHT PEN POS. (DOES A RETURN)
 SET ACTIVE DISP. PAGE (DOES A RETURN)
 SCROLL UP
 SCROLL DOWN
 READ ATTRIBUTE & CHARACTER @ CURSOR
 WRITE ATTRIBUTE & CHARACTER @ CURSOR
 WRITE CHARACTER ONLY @ CURSOR
 SET COLOR PALETTE (DOES A RETURN)
 WRITE DOT (DOES A RETURN)
 READ DOT (DOES A RETURN)
 ASCII TELETYPE WRITE
 CURRENT VIDEO STATE
 WRITE BLOCK OF CHARS & ATTRIBUTES
 WRITE BLOCK OF CHARS ONLY
 CHANGE SCREEN ATTRIBUTES
 CLEAR ALPHA SCREEN
 CLEAR GRAPHICS SCREEN
 SET TTY STATUS LINE
 SET ATTRIBUTE LATCH
 GET DISPLAY BEGIN
 PRINT TTY STRING
 END OF JUMP TABLE

SAVE DI
 SAVE SI
 SAVE OLD DS
 SAVE OLD ES
 SAVE BX
 SAVE AX
 SAVE BX (TEMPORARILY)
 SET UP ES
 AS CRTDAT
 RESTORE BX

DS:WORD PTR CS:DSADDR+CSWRAP, SET UP DS AS ROMDAT
 BX,CRTDAT
 ES,BX
 BX

CRTTABL
 NEAR
 DI
 SI
 DS
 ES
 BX
 AX
 BX
 DS:WORD PTR CS:DSADDR+CSWRAP, SET UP DS AS ROMDAT
 BX,CRTDAT
 ES,BX
 BX

ENDTAB EGU
 CRTTABL

PUSH PROC
 PUSH DI
 PUSH SI
 PUSH DS
 PUSH ES
 PUSH BX
 PUSH AX
 PUSH BX
 MOV DS:WORD PTR CS:DSADDR+CSWRAP, SET UP DS AS ROMDAT
 MOV BX,CRTDAT
 MOV ES,BX
 POP BX

HOLDUP:
 CMP CRTHLD,0
 JNE HOLDUP
 INT CMPINT

```
0053' 8A C4      MOV      AL, AH      ; GET INTO LOW BYTE
0055' 30 E4      XOR      AH, AH      ; ZERO TO HIGH BYTE
0057' D1 E0      BAL      AX, 1       ; WORD OFFSET FOR TABLE LOOKUP
0059' 88 F0      MOV      SI, AX     ; SI = OFFSET FOR JUMP
005B' 3D 0032    CMP      AX, OFFSET ENDTAB ; SEE IF VALID FUNCTION
005E' 58        POP      AX       ; GET PARAMETER BACK
005F' 73 05      JAE      CRTRET     ; NO, DO NOTHING IF NOT IN RANGE

0061'          CRT00:
0061' 2EFF A4 0006" JMP      CRTABL(BI) ; TAKE JUMP BASED ON FUNCTION

*****
***** THIS IS THE COMMON RETURN POINT FOR ALL CRT CALLS
***** IF THE RETFLO IS SET THEN THE RETURN IS A "LOCAL" RETURN
***** (I.e. DO NOT POP THE STACK AND RETURN OUT OF CRT CODE AREA BUT
***** INSTEAD RETURN WITHIN CRT CODE FROM LOCAL CALLER)
*****

0066'          CRTRET PROC      NEAR
0066' 80 3E 000A" 00 CMP      RETFLO, 00H ; 0: CONDITIONAL RETURN FLAG SET 1
0068' 75 05      JNE      LCRET     ; Y: DO A "LOCAL" RETURN
006D' 58        POP      BX       ;
006E' 07        POP      ES       ;
006F' 1F        POP      DS       ;
0070' 5E        POP      SI       ; ALL CRT CODE JUMPS TO CRTRET AND
0071' 5F        POP      DI       ;
0072' C3      LCRET: RET      ; RETURNS THRU HERE TO CLEAR THE STACK

775 ,
776 ,
777 *****
778 ***** THIS ROUTINE IS USED TO SET THE CRT MODE (NOTE: this is not
779 ***** applicable to PEGASUS but is provided for compatibility with
780 ***** the IBM CRT calling conventions), IT DOES NOTHING AND SIMPLY
781 ***** RETURNS.
782 *****
783 ***** INPUT - AH = 0
784 ***** AL = 1-7 (ignored)
785 *****
786 ***** OUTPUT - None
787 *****
788 ***** USED - None
789 *****
790 *****
791 *****
792 ***** SETMOD PROC      NEAR
793 ***** ASSUME ES: CRTDAT
794 ***** JMP      CRTRET     ; DO NOTHING
795 *****
796 ***** SET CURSOR TYPE BASED ON REGISTER CX
797 *****
798 *****
799 ***** INPUT - CH = Bits 4-0 cursor start line (0 - 0BH)
```



```
00AE' EB B6  
852 JMP CRTRET ; ALL DONE  
853 ;  
854 ; *****  
855 ; THIS ROUTINE READS THE CURRENT CURSOR POSITION AND RETURNS  
856 ; THE POSITION IN DX AS (COLUMN,ROW), (X,Y).  
857 ;  
858 ; INPUT - NONE  
859 ;  
860 ; OUTPUT - DH = (column),DL = (row)  
861 ; CH,CL = Cursor type  
862 ;  
863 ; USED - DX,CX,AX,BX.  
864 ;  
865 ; *****  
866 ;  
0080' RDCPOB NEAR ; READ CURSOR POSITION  
867 ASSUME EB: CRTDAT ; AX = ABSOLUTE CURSOR POSITION  
868 MOV AX,CURPOB ; ADJUST TO LOGICAL POSITION  
869 CALL PTRADJ ; AX = OFFSET FROM DISBEQ  
870 SUB AX,BX ; (BX = DISBEQ RETURNED FROM CALL)  
871 ;  
872 MOV BL,80 ; DIVIDE AX BY 80  
873 DIV BL ; PUT COL,ROW IN DH,DL  
874 XCHQ DX,AX ; GET CURSOR TYPE IN CX  
875 MOV CX,CURTYP ; RETURN  
876 JMP CRTRET ;  
877 ;  
878 ; *****  
879 ; THIS ROUTINE READS THE LIGHT PEN AND RETURNS IT'S POSITION  
880 ; TO THE CALLER.  
881 ;  
882 ; INPUT - NONE  
883 ;  
884 ; OUTPUT - RETURN NO ACTION FOR NOW  
885 ;  
886 ; USED -  
887 ;  
888 ; *****  
889 ;  
890 ; RLPP0B PROC NEAR  
891 ASSUME EB: CRTDAT  
892 JMP CRTRET  
893 ;  
894 ;  
895 ; *****  
896 ; THIS ROUTINE SETS THE ACTIVE DISPLAY PAGE (IT DOES NOTHING)  
897 ; AND IS PROVIDED FOR IBM COMPATIBILITY ONLY.  
898 ; *****  
899 ;  
900 ; BTDSP0 PROC NEAR  
901 ASSUME EB: CRTDAT  
902 JMP CRTRET  
903 ;
```

```

904 /*****
905 / THIS ROUTINE "SCROLLS" THE SCREEN IN EITHER DIRECTION BY
906 / SIMPLY PERFORMING A MOVES FROM THE SOURCE TO THE DESTINATION
907 / LOCATION OF THE SCREEN. THIS METHOD PROVIDES FOR DEFINING
908 / A WINDOW AND MOVING IT TO ANOTHER POSITION ON THE SCREEN.
909 / THE SOURCE WINDOW BOUNDARY COORDINATES ARE AS FOLLOWS AND
910 / THE SAME APPLIES FOR THE DESTINATION COORDINATES:
911 /
912 / Upper left = (src. col.,src row)
913 / Upper right = (src. col. + col. cnt.,src. row)
914 / Lower left = (src. col.,src. row + line cnt.)
915 / Lower right = ( src. col. + col. cnt.,src. row + line cnt.)
916 /
917 / INPUT - AL = 0 blank out source text
918 / * AL ) 0 Don't blank source
919 / ** (DH,DL) = (Source column,Source row) of scroll
920 / (BH,BL) = (Destination column, Destination row) of scroll
921 / CH = Column count(1-80) CL = Line count(1-25)
922 /
923 / * NOTE: If the source and destination windows
924 / overlap then some of the source will
925 / be overwritten.
926 /
927 / ** NOTE: If destination is less than source
928 / then the scroll is a scroll up and
929 / to the left, if source is less than
930 / destination then the scroll is a scroll
931 / down and to the right.
932 /
933 /
934 /
935 /
936 /
937 /
938 /
939 /
940 /
941 /
942 /
943 /
944 /
945 /
946 /
947 /
948 /
949 /
950 /
951 /
952 /
953 /
954 /
955 /

```

Example: You want to scroll the screen up "n" lines.
 input - source col,row (DH,DL) = (0,n)
 dest. col,row (BH,BL) = (0,0)
 column count (CH) = 80
 line count (CL) = 25-n

```

OUTPUT - NONE
USED -
*****
SCROLL PROC NEAR
ASSUME ES: CRTDAT
CMP BH,79
JA LEAVE
CMP DH,79
JA LEAVE
CMP BL,24
JA LEAVE

```

; SCROLL THE SCREEN UP OR DOWN
 ; Q: DEST. COL } 79 ?
 ; Y: THEN DO NOTHING
 ; Q: SRC. COL } 79 ?
 ; Y: THEN DO NOTHING
 ; Q: DEST. ROW } 24 ?
 ; Y: THEN DO NOTHING

```

00C3'
00C6' 80 FF 4F
00C8' 77 OF
00CB' 80 FE 4F
00CD' 77 OA
00DD' 80 FB 1B
00DD' 77 05

```

Address	Code	Label	Instruction	Comments
00D2'	80 FA 18			
00D3'	76 02			
00D7'	EB 8D	LEAVE:		
00D9'		OO_ON:		
00D9'	50		PUSH	
00DA'	53		PUSH	
00DB'	52		PUSH	
00DC'	31 C0		XOR AX, AX	
00DE'	80 50		MOV AL, 80	
00E0'	F6 E2		MUL DL	
00E2'	BA D6		MOV DL, DH	
00E4'	30 F6		XOR DH, DH	
00E6'	01 D0		ADD AX, DX	
00E8'	96		SI, AX	
00E9'	03 36 0006*		SI, DISBEO	
00ED'	5A		DX	
00EE'	31 C0		XOR AX, AX	
00F0'	80 50		MOV AL, 80	
00F2'	F6 E3		MUL BL	
00F4'	8A DF		MOV BL, BH	
00F6'	30 FF		MOV BH, BH	
00F8'	01 D8		XOR AX, BX	
00FA'	97		ADD AX, BX	
00FB'	03 3E 0006*		XCHG DI, AX	
00FF'	5B		ADD DI, DISBEO	
0100'	58		POP BX	
0101'	39 FE		POP AX	
0103'	7C 3D		POP AX	
0105'		BCRLUP:		
0105'	88 D9		MOV BX, CX	
0107'	31 C9		XOR CX, CX	
0109'	8A C8		MOV CL, BL	
010B'				
010B'	88 16 0006*			
010F'	81 C2 07CF			
0113'				
0113'	31		CX	
0114'	56		SI	
0115'	57		DI	
0116'	31 C9		XOR CX, CX	
0118'	8A CF		MOV CL, BH	
011A'				
011A'	39 D6		SI, DX	
011C'	7F 1E		QUIT	
011E'	3C 00		AL, 0	
0120'	74 06		SPECUP	
0122'	26A4		BYTE PTR [DI], EB: BYTE PTR [BI], MOV8 OF COL. COUNT LENGTH	
0124'	E2 F4		NRMP1	
0126'	EB 07		SHORT NXTUP	

; 0: SRC. ROW) 24 7
; N: THEN CONTINUE
; QUIT IF ANY VALUES OUT OF RANGE

; SAVE AX
; SAVE DESTINATION COL/ROW
; SAVE SOURCE COL/ROW
; CLEAR AX

; AX = SRC. ROW OFFSET FROM 0
; DX = SRC. COL.
; CLEAR DH
; AX = SRC COL/ROW ABS LOCATION
; SI = SRC COL/ROW ABS LOCATION
; SI = SRC ABS LOCATION FROM DISBEO
; RESTORE DX
; CLEAR AX

; AX = DEST. ROW OFFSET FROM 0
; BX = DEST. COL.
; CLEAR BH

; AX = DEBT. COL/ROW ABS LOCATION
; DI = DEST. COL/ROW ABS LOCATION
; DI = DEST. ABS LOCATION FROM DISBEO
; RESTORE BX
; RESTORE AX
; 0: SRC. BEG. IN FRONT OF DEST. BEG. ?
; Y: THEN SCROLL DOWN

; N: THEN SCROLL UP
; BX = COL. AND LINE COUNTS
; CLEAR CX FOR REP AND LOOP
; SET UP CX FOR LOOP

; N: THEN NORMAL SCROLL UP
; DX = BEGINNING OF DISPLAY ADDRESS
; DX = END OF DISPLAY ADDRESS

; SAVE IT
; SAVE BRC. LOCATION
; SAVE DEST. LOCATION
; CLEAR CX FOR REP
; CX = COL. COUNT

; 0: SOURCE OFF THE END OF SCREEN ?
; Y: THEN QUIT IT
; 0: BLANKING SOURCE TEXT ?
; Y: THEN SPECIAL UP CONDITION
; MOV8 OF COL. COUNT LENGTH
; NEXT CHARACTER
; NEXT LINE

```
0128' EB 006B  
0128' 46  
012C' 47  
012D' E2 EB  
012F'  
012F' 5F  
0130' B3 C7 50  
0133' 5E  
0434' B3 C6 50  
0137' 59  
0138' E2 D9  
013A' EB 03  
  
013C' 5F  
013C' 5E  
013E' 59  
  
013F' E9 FF24  
013F'
```

SPECUP: CALL MOVEIT
INC SI
INC DI
LOOP NRMUP1

NXTUP: POP DI
ADD DI,80
POP SI
ADD SI,80
POP CX
NRMUPO
SHORT SCRLRT
JMP

QUIT: POP DI
POP SI
POP CX

SCRLRT: JMP CRTRET

SCRLDN: PUSH CX
PUSH AX
XOR AH,AH
MOV AL,80
DEC CL
DEC CH
MUL CL
MOV CL,CH
XOR CH,CH
ADD AX,CX
ADD SI,AX
ADD DI,AX
POP AX
POP CX
MOV BX,CX
XOR CH,CH
MOV CL,BL

NORMDN: MOV DX,DISBEQ
ADD DX,7CFH
STD

***** THIS IS THE SCROLL DOWN PORTION OF THE SCROLL. IT DOES THE
THE MOV8 SEQUENCE STARTING FROM THE END OF THE SOURCE
WINDOW TO THE END OF THE DESTINATION WINDOW BACKWARDS. *****

DO DO THE MOVING AND LATCH SETTING
NEXT SOURCE CHARACTER
NEXT DESTINATION LOCATION
DO IT FOR COL. COUNT TIMES

RESTORE DEST. LOC.
NEXT DEBTINATION LINE
RESTORE SRC. LOC.
NEXT SOURCE LINE
RESTORE LINE COUNT
DO IT LINE COUNT TIMES
...AND RETURN

CLEAR THE STACK

...AND LEAVE

SAVE CX
SAVE AX
CLEAR AH

MAKE LINE COUNT 0 RELATIVE
MAKE COL. COUNT 0 RELATIVE

CLEAR CH
AX = SRC. END OFFSET FROM SRC. BEGIN
SI = SRC. END COL/ROW ABS. LOCATION
DI = DEST. END COL/ROW ABS. LOCATION
RESTORE AX
RESTORE COL. AND LINE COUNTS
BX = COL. AND LINE COUNTS
CLEAR CX FOR REP AND LOOP
SET UP CX FOR LOOP

AX = BEGINNING OF DISPLAY ADDRESS
AX = END OF DISPLAY ADDRESS
SET DIRECTION FLAG FOR MOV8

```
0169' 1060 NRRMDNO:
0169' 51 PUSH
0169' 56 PUSH
016A' 56 PUSH
016B' 57 PUSH
016C' 30 ED XOR
016E' 8A CF MOV
0170' 1066 NRRMDNI:
0170' 39 D7 CMP
0172' 7F 1F J0
0174' 3C 00 CMP
0176' 74 06 JE
0178' 26A4 MOV8
017A' E2 F4 LOOP
017C' EB 07 JMP
017E' 1074 SPECDN:
017E' EB 0015 CALL
0181' 4E DEC
0182' 4F DEC
0183' E2 EB LOOP
0185' 1079 NXTDN:
0185' 5F POP
0186' 83 EF 50 SUB
0189' 5E POP
018A' 83 EE 50 SUB
018D' 59 POP
018E' E2 D9 LOOP
0190' FC CLD
0191' EB AC JMP
0193' 1088 QUIT2:
0193' FC CLD
0194' EB A6 JMP
0196' 50

; SAVE IT
; SAVE SRC. LOCATION
; SAVE DEBT. LOCATION
; CLEAR CX FOR REP
; CX = COL. COUNT
; O: DEST. OFF THE END OF SCREEN ?
; Y: THEN QUIT IT
; O: BLANK SOURCE TEXT ?
; Y: THEN SCROLL DOWN WITH BLANKING
; N: THEN KEEP GOING
; NEXT LINE
; O: DO THE MOVING AND LATCH BETTING
; NEXT SOURCE CHARACTER
; NEXT DESTINATION POSITION
; N: DO IT FOR COL. COUNT TIMES
; RESTORE DEST. LOC.
; NEXT DESTINATION LINE
; RESTORE SRC. LOC.
; NEXT SOURCE LINE
; RESTORE LINE COUNT
; DO IT LINE COUNT TIMES
; CLEAR DIRECTION FLAG
; ...AND RETURN
; CLEAR DIRECTION FLAG
; ...AND LEAVE
; *****
; THIS ROUTINE IS USED TO DO THE BLANKING MANUEVER REQUIRED BY
; THE SCROLLING ROUTINE WHEN BLANKING OF SOURCE TEXT IS REQUIRED.
; IT SAVES THE ATTRIBUTE LATCH, SAVES THE CHAR'S ATTRIBUTE, SETS THE
; LATCH TO THE DEFAULT ATTRIBUTE (HIGH INTENSITY), BLANKS THE SRC.
; CHAR. @ THE DESTINATION AND THEN RESTORES THE LATCH TO IT'S
; ORIGINAL SETTING.
; INPUT - ES = CRTDAT
; DS = ROMDAT
; OUTPUT - IT DID IT
; USED - NONE
; *****
; MOVEIT PROC NEAR
; PUSH AX
; *****
```

```

0197' EB 0307
019A' 26BA 04
019D' 268A 26 1800
01A2' 26C6 06 1800 OF
01A8' 26C6 04 20
01AC' 2688 26 1800
01B1' 2688 09
01B4' EB 02FC
01B7' 98
01B8' C3

1112 CALL SAVATT ; SAVE THE LATCH
1113 MOV AL,ES:BYTE PTR [SI] ; GET FIRST SRC. CHAR.
1114 MOV AH,ES:BYTE PTR ATTLAT ; SAVE THE SOURCE CHAR'S ATTRIBUTE
1115 MOV EB:BYTE PTR ATTLAT,HIGHAT ; SET THE LATCH TO THE DEFAULT
1116 MOV ES:BYTE PTR [SI],SPACE ; CLEAR SOURCE TEXT
1117 MOV EB:BYTE PTR ATTLAT,AH ; RESTORE THE CHAR'S ATTRIBUTE
1118 MOV ES:BYTE PTR [DI],AL ; WRITE IT AT DEST. LOCATION
1119 CALL RSTATT ; RESET THE ATTRIBUTE LATCH
1120 POP AX
1121 RET
1122
1123 ; *****
1124 ; THIS ROUTINE READS AND RETURNS THE CHARACTER AND ATTRIBUTE AT
1125 ; THE CURRENT CURSOR POSITION.
1126 ;
1127 ; INPUT - NONE
1128 ;
1129 ; OUTPUT - AH = Attribute of character read
1130 ; AL = character read
1131 ;
1132 ; USED - AX,DI
1133 ;
1134 ; *****
1135 ;
1136 RATCHR. PROC NEAR ; READ CHAR. & ATTRIBUTE @ CURSOR
1137 ASSUME ES:CRTDAT ; DI = CURRENT CURSOR POSITION
1138 MOV DI,CURPOS ; AL = CHARACTER @ CURSOR
1139 MOV AL,ES:BYTE PTR [DI] ; AH = ATTRIBUTE
1140 MOV AH,ES:BYTE PTR ATTLAT ;
1141 JMP CRTRET ;
1142
1143 ; *****
1144 ; THIS ROUTINE IS USED TO WRITE A CHARACTER AND ASSOCIATED
1145 ; ATTRIBUTE AT THE CURRENT CURSOR POSITION. THE USER SHOULD
1146 ; NOTE THAT THE CURSOR IS NOT AUTOMATICALLY ADVANCED WITH THIS
1147 ; ROUTINE.
1148 ;
1149 INPUT - AL = Character to write
1150 ; BL = Attribute of character
1151 ; CX = Count of chars. to write
1152 ;
1153 ; OUTPUT - None
1154 ;
1155 ; USED - AX,BL,CX
1156 ;
1157 ; *****
1158 ;
1159 WATCHR PROC NEAR ; WRITE ATT. & CHAR. @ CURSOR
1160 ASSUME ES:CRTDAT ;
1161 MOV ES:BYTE PTR ATTLAT,BL ; WRITE THE ATTRIBUTE LATCH
1162 ;
1163 ; *****

```

```
1164 /
1165 /
1166 /
1167 /
1168 /
1169 /
1170 /
1171 /
1172 /
1173 /
1174 /
1175 /
1176 /
1177 /
1178 /
1179 /
1180 /
1181 /
1182 /
1183 /
1184 /
1185 /
1186 /
1187 /
1188 /
1189 /
1190 /
1191 /
1192 /
1193 /
1194 /
1195 /
1196 /
1197 /
1198 /
1199 /
1200 /
1201 /
1202 /
1203 /
1204 /
1205 /
1206 /
1207 /
1208 /
1209 /
1210 /
1211 /
1212 /
1213 /
1214 /
1215 /

01CD'
01CD' 83 F9 00
01D0' 76 1A
01D2' 50
01D3' A1 0003"
01D6' E8 0298
01D9' 97
01DA' A1 0007"
01DD' E8 0291
01E0' 93
01E1' 58
01E2'
01E2' 2688 05
01E5' 47
01E6' 39 DF
01E8' 77 02
01EA' E2 F6
01EC'
01EC' E9 FE77

; THIS IS THE ENTRY POINT FOR WRITING A CHARACTER ONLY
; @ THE CURRENT CURSOR POSITION. IT IS ALSO USED BY THE
; ABOVE PROCEDURE BUT IGNORES THE ATTRIBUTE PARAMETER.
; AGAIN THE USER SHOULD NOTE THAT THE CURSOR IS NOT AUTO-
; Matically ADVANCED AND IS LEFT AT IT'S ORIGINAL POSITION.

INPUT - AL = Character to write
      CX = Count of chars. to write

OUTPUT - None
USED - AX,CX

*****
; WRITE CHAR. ONLY @ CURSOR
; @: ANY CHARACTERS TO WRITE ?
; N: THEN DON'T WRITE ANY
; SAVE AX
; AX = CURPOS PHYSICAL LOCATION
; GO GET IT'S LOGICAL POSITION
; PUT IT IN DI
; AX = DISPLAY END PHYSICAL LOCATION
; GET IT'S LOGICAL LOCATION
; PUT IT IN BX
; RESTORE AX

; WRITE CHAR. STARTING AT CURSOR
; NEXT LOCATION
; @: NEXT LOCATION OFF END OF SCREEN ?
; Y: THEN DON'T WRITE IT THERE
; DO IT CX TIMES
; ...AND RETURN

*****
; MEMBER(DI),AL
; DI
; DI,BX
; NOWRIT
; WRTLOP
; CRTRET

*****
; THIS ROUTINE IS USED TO SET THE COLOR PALETTE FOR THE GRAPHICS
; BANKS A,B,C.

INPUT -
OUTPUT - Palette set accordingly
USED -

*****
; BETPAL PROC NEAR
; ASSUME EB: CRTDAT
; JMP CRTRET

*****
```



```
1216 ; *****  
1217 ; THIS ROUTINE IS USED TO WRITE A GRAPHICS PIXEL AT THE  
1218 ; SPECIFIED LOCATION.  
1219 ;  
1220 ; INPUT -  
1221 ;  
1222 ; OUTPUT -  
1223 ;  
1224 ; USED -  
1225 ;  
1226 ; *****  
1227 ;  
1228 ; WRPIXL PROC NEAR  
1229 ; ASSUME ES: CRTDAT  
1230 ; JMP CRTRET  
1231 ;  
1232 ; *****  
1233 ; THIS ROUTINE IS USED TO READ A GRAPHICS PIXEL AT THE  
1234 ; SPECIFIED LOCATION.  
1235 ;  
1236 ; INPUT -  
1237 ;  
1238 ; OUTPUT -  
1239 ;  
1240 ; USED -  
1241 ;  
1242 ; *****  
1243 ;  
1244 ; RDPIXL PROC NEAR  
1245 ; ASSUME ES: CRTDAT  
1246 ; JMP CRTRET  
1247 ;  
1248 ; *****  
1249 ; THIS ROUTINE IS USED TO SET THE COLOR PALETTE FOR THE GRAPHICS  
1250 ; BANKS A,B,C.  
1251 ;  
1252 ; INPUT - BH = COLOR ID  
1253 ; BL = COLOR VALUE (0000000B)  
1254 ;  
1255 ; OUTPUT - Palette set accordingly  
1256 ;  
1257 ; USED - CL, BL IS DESTROYED  
1258 ;  
1259 ; *****  
1260 ;  
1261 ; SETPAL PROC NEAR  
1262 ; ASSUME ES: CRTDAT, DS: ROMDAT  
1263 ; MOV CL, BH  
1264 ; MOV DI, GR_RED  
1265 ; MOV SI, OFFSET REDPAL  
1266 ; MOV AL, 03H  
1267 ; PALOOP:  
; COPY BIT ADDRESS TO SHIFT COUNT  
; DI = POINTS TO RED PALETTE LATCH  
; SI = POINTS TO LOCAL COPY OF PALETTEB  
; AL = LOOP COUNT
```

```

1268 , MOV AH,[SI]
1269 , CALL DOPAL
1270 , MOV ES:BYTE PTR [DI],AH
1271 , INC SI
1272 , INC DI
1273 , DEC AL
1274 , JNZ PALOOP
1275 , JMP CRTRET
1276 , DOPAL:
1277 , ROR AH,CL
1278 , RCR BL,1
1279 , RCR AH,1
1280 , ROL AH,1
1281 , ROL AH,CL
1282 , RET
1283 ,
1284 , *****
1285 , THIS ROUTINE IS USED TO WRITE A GRAPHICS PIXEL AT THE
1286 , SPECIFIED LOCATION.
1287 ,
1288 , INPUT - CX = COLUMN ADDRESS
1289 , DX = ROW ADDRESS
1290 , AL = COLOR ID , THAT IS VALUE TO PUT INTO PLANES
1291 , NOT COLOR THAT WILL COME FROM PALETTE
1292 ,
1293 , OUTPUT - TO BIT8 IN FRAME BUFFER MEMORY (GRAPHICS BIT MAP)
1294 ,
1295 , USED -
1296 ,
1297 ,
1298 , FORMULAE - BYTE ADDRESS=(ROW*LINELEN+(COLUMN/8)) XOR 1
1299 , BIT ADDRESS = COLUMN MOD 8
1300 , *****
1301 , WRPIXL PROC NEAR
1302 , ASSUME DS:ROMDAT
1303 , CALL MKCURS
1304 ,
1305 , DI,DX
1306 , BX,GRASEQ
1307 , ES,BX
1308 , CALL WRTPXL
1309 , MOV BX,GRBSEQ
1310 , MOV ES,BX
1311 , CALL WRTPXL
1312 , MOV BX,GRCSEQ
1313 , MOV ES,BX
1314 , CALL WRTPXL
1315 , JMP CRTRET
1316 , WRTPXL:
1317 , RCR AL,1
1318 , OR EB:BYTE PTR [DI],CL
1319 , JNB WREND
    
```

```

; AH = CURRENT RED PALETTE BETTING
;
; SET THE LATCH
;
;
; DO ALL THREE PALETTES
; ...AND LEAVE
;
; MOVE BIT TO CHANGE TO LSB
; GET NEW BIT INTO CARRY
; MERGE BIT INTO MSB, DROP OLD BIT
; BACK IN SHIFTED POSITION
; PUT BACK INTO PROPER POSITION
    
```

 THIS ROUTINE IS USED TO WRITE A GRAPHICS PIXEL AT THE
 SPECIFIED LOCATION.

INPUT - CX = COLUMN ADDRESS
 DX = ROW ADDRESS
 AL = COLOR ID , THAT IS VALUE TO PUT INTO PLANES
 NOT COLOR THAT WILL COME FROM PALETTE

OUTPUT - TO BIT8 IN FRAME BUFFER MEMORY (GRAPHICS BIT MAP)
 USED -

 FORMULAE - BYTE ADDRESS=(ROW*LINELEN+(COLUMN/8)) XOR 1
 BIT ADDRESS = COLUMN MOD 8

; A LOCAL ROUTINE FOR CONVERSION OF
 ; ROW/COLUMN TO BYTE/ BIT

; POINT ES TO PLANE 1
 ; DO IT FOR BANK A

; DO IT FOR BANK B

; DO IT FOR BANK C
 ; ...AND LEAVE

; BIT TO ENTER TO CARRY FLAG
 ; SET THE BIT THEN

```
1320 , AND ES:BYTE PTR [DI].CH ,  
1321 , WREND: RET ,  
1322 ,  
1323 ,  
1324 , *****  
1325 , THIS SUBROUTINE CONVERTS ROW COLUMN ADDRESS INTO BYTE ADDRESS AND  
1326 , HIGH AND LOW ACTIVE BIT MASKS  
1327 , INPUT - CX = COLUMN ADDRESS (= 736)  
1328 , DX = ROW ADDRESS (= 300)  
1329 , OUTPUT - CH = ONE BIT LOW  
1330 , CL = ONE BIT HIGH  
1331 , DX = BYTE ADDRESS  
1332 ,  
1333 , MKCURS PROC NEAR  
1334 , MOV BX,CX , MOVE COLUMN OUT OF CX  
1335 , PUSH AX , NEED A FOR MULTIPLY  
1336 , MOV CX,BLNLEN , CX = BYTES PER ROW  
1337 , MOV AX,DX , MOVE ROW TO MULTIPLIER  
1338 , MUL CX , TIMES NUMBER OF BYTES PER ROW  
1339 , MOV CL,3 , DIVIDE COLUMNS BY 8 TO GET BYTE COLUMN  
1340 , MOV DX,BX , COPY COLUMN VALUE  
1341 , SHR DX,CL , BYTE PART OF COLUMN  
1342 , ADD DX,AX , FINAL BYTE ADDRESS RETURNED IN DX  
1343 , MOV CL,BL , LS BYTE OF COLUMN ADDRESS  
1344 , AND CL,07H , SNATCH BIT ADDRESS  
1345 , MOV BL,01H , CREATE A BIT  
1346 , SHL BL,CL , SHIFT IT INTO BIT POSITION  
1347 , MOV BH,BL , COPY TO MS BYTE TO  
1348 , NOT BH , CREATE COMPLEMENT FOR ANDING  
1349 , MOV CX,BX , RETURNED IN CX  
1350 , POP AX , RECOVER IN CASE NEEDED  
1351 , RET  
1352 ,  
1353 , *****  
1354 , THIS ROUTINE IS USED TO READ A GRAPHICS PIXEL AT THE  
1355 , SPECIFIED LOCATION.  
1356 ,  
1357 , INPUT - CX = COLUMN  
1358 , DX = ROW  
1359 , OUTPUT - AL = COLOR NUMBER FROM BIT MAP, NOT RGB VALUE  
1360 ,  
1361 , USED - A, B, C, D 3 BYTES OF STACK (IN MKCURS)  
1362 ,  
1363 , *****  
1364 ,  
1365 , RDPXL PROC NEAR  
1366 , CALL MKCURS ,  
1367 , MOV DI,DX ,  
1368 , XOR AL,AL , PREPARE AL TO RECEIVE DATA  
1369 , MOV BX,GRGSEQ ,  
1370 , MOV ES,BX , POINT TO FIRST PLANE  
1371 , CALL REAPXL , DO IT FOR BANK C
```

```

1372 , MOV BX,CRBSEQ
1373 , MOV EB,BX
1374 , CALL REAPXL
1375 , MOV BX,GRASEQ
1376 , MOV EB,BX
1377 , CALL REAPXL
1378 , JMP CRTRET
1379 , REAPXL:
1380 , MOV AH,EB:BYTE PTR [DI],OET THE BYTE
1381 , AND AH,CL
1382 , JZ REAEND
1383 , OR AL,OIH
1384 , ROL AL,1
1385 , REAEND:
1386 , RET
1387 ,
1388 ,
1389 ,
1390 ,
1391 ,
1392 ,
1393 ,
1394 ,
1395 ,
1396 ,
1397 ,
1398 ,
1399 ,
1400 ,
1401 ,
1402 ,
1403 ,
1404 ,
1405 ,
1406 ,
1407 ,
1408 ,
1409 ,

```

 BET TTY STATUS LINE.

INPUT - CH = 0 (always)
 CL = Start line number of status line (0-24 decimal)

NOTE: CH must always be zero. This represents
 the first column of the status line.

CX = 0 = Full screen TTY.

NOTE: If an attempt is made to cause a status
 line which doesn't fall after the current
 line of the cursor the then no status line
 is implemented.

OUTPUT - None

USED - None

 BETTY

```

01EF' A1 0009"
01EF' E8 027C
01F9' 97
01F6' 88 D1
01FB' 30 F6
01FA' 30 E4
01FC' 80 50
01FC' F6 E2
0200' 03 06 0006"
0204' 39 F8
0206' 76 07
0208' 89 0E 0009"
020C' E9 FE57

1410 , MOV NEAR AX,THSLIN
1411 , CALL PTRADJ
1412 , XCHG DI,AX
1413 , MOV DX,CX
1414 , XOR DH,DH
1415 , XOR AH,AH
1416 , MOV AL,B0
1417 , MUL DL
1418 , ADD AX,DISBEO
1419 , CMP AX,DI
1420 , JBE STTTY1
1421 , MOV STATLN,CX
1422 , JMP CRTRET
1423 ,

```

; BET TTY STATUS LINE BEGINNING
 ; AX = THSLIN PHYSICAL LOCATION
 ; GET IT'S LOGICAL LOCATION
 ; PUT IT IN DI
 ; DX = STATUS LINE COL,ROW
 ; MAKE SURE COL = 0
 ; CLEAR AH
 ;
 ; AX = STAT. ROW OFFSET FROM 0
 ; SI = STAT ABS LOCATION FROM DISBEO
 ; G: STATUS LINE (= CURRENT LINE + 7
 ; Y: THEN SET NO STATUS LINE.
 ; SAVE STATUS LINE BEGINNING
 ; ...AND RETURN

```
020F' 1424 STTTV1: MOV STATLN,0 ; SET NO STATUS LINE
020F' 1425 JMP CRTRET ; ...LEAVE
0215' 1426
0215' 1427
0215' 1428 ; *****
0215' 1429 ; ASCII TELETYPE WRITE ROUTINE *****
0215' 1430 ;
0215' 1431 ; INPUT - AL = Character to write
0215' 1432 ;
0215' 1433 ; OUTPUT - None
0215' 1434 ;
0215' 1435 ; USED -
0215' 1436 ; *****
0215' 1437 ; *****
0218' 1438 WRTTY PROC NEAR ; ASCII TELETYPE WRITE
0218' 1439 ASBUME ES: CRTDAT ;
0218' 1440 CMP AL, BEL ; IS CHAR A BELL CHARACTER ?
0218' 1441 JE BEPBEL ; YES, Then go beep the bell
0218' 1442 CMP AL, 89 ; IS CHAR A BACKSPACE
0218' 1443 JE BAKSP ; YES, DO BSHORT JUMP
0218' 1444 CMP AL, LF ; IS IT A LINE FEED
0218' 1445 JE LINFED ; YES, DO BSHORT JUMP
0218' 1446 CMP AL, CR ; IS CHAR A CARRIAGE RETURN
0218' 1447 JE CARET ; YES, DO SHORT JUMP
0218' 1448 JMP CHRVRT ; NO, THEN WRITE CHAR @ CURSOR POS.
0218' 1449
0218' 1450 BEPBEL: BEEP ; GO BEEP THE BELL
0218' 1451 JMP CRTRET ; ...AND LEAVE
0218' 1452
0218' 1453 ; *****
0218' 1454 ; PERFORM A BACKSPACE *****
0218' 1455 ; *****
0218' 1456 ; *****
0231' 1457 BAKSP PROC NEAR
0231' 1458 ASBUME ES: CRTDAT
0231' 1459 MOV AX, CURPOS
0231' 1460 MOV BX, DISBEO
0231' 1461 CMP AX, BX
0231' 1462 JE NOBKSP
0231' 1463 MOV BX, THSLIN
0231' 1464 CMP AX, BX
0231' 1465 JE BAKLIN
0231' 1466
0231' 1467 BKSP1:
0231' 1468 AX
0231' 1469 AX, 7FFF
0231' 1470 MOV CURPOS, AX
0231' 1471 MOV DL, CURPSH
0231' 1472
0231' 1473 CALL PTRADJ
0231' 1474 CALL PTRUP
0231' 1475 MOV BX, CRTCNT
0231' 1476 DEC BX
0231' 1477
0231' 1478 ; GET CURRENT CURSOR POSITION
0231' 1479 ; GET BEGINNING OF DISPLAY POINTER
0231' 1480 ; SEE IF THE SAME
0231' 1481 ; DO NOTHING (RETURN)
0231' 1482 ; GET THISLIN POINTER
0231' 1483 ; SEE IF AT BEGINNING OF LINE
0231' 1484 ; WRAP BACK TO PREVIOUS LINE
0231' 1485
0231' 1486 ; BACK-UP ONE CHAR POSITION
0231' 1487 ; MAGIC NUMBER !!
0231' 1488 ; SAVE NEW CURSOR POSITION
0231' 1489 ; DL = CURS. POS. REG. HIGH
0231' 1490 ; AH = CURS. POS. ADD. HIGH
0231' 1491 ; GET CURSOR LOGICAL POSITION
0231' 1492 ; DO DOUBLE WRITE TO CRT
0231' 1493 ; GET NUMBER OF CHRS ON LINE
0231' 1494 ; NOW THERE IS ONE LESS
0231' 1495
```

025B'	B9 1E 0008"	MOV	CRTCNT,BX	;	SAVE NEW COUNT
025C'	E9 FE07	NOBKBP: JMP	CRTRET	;	ALL DONE
025F'		BACKLIN:		;	
025F'	B3 EB 50	BUB	BX,80	;	MOVE THSLIN BACK ONE LINE
0262'	B1 E3 07FF	AND	BX,7FFH	;	MAGIC NUMBER !!
0266'	B9 1E 0003"	MOV	THSLIN,BX	;	SAVE NEW THSLIN POINTER
026A'	C7 06 0008" 0050	MOV	CRTCNT,80	;	SET CHAR COUNT - TO A FULL LINE
0270'	EB D2	JMP	SHORT BKSP1	;	GO BACK AND FINISH
0272'		CARET		;	
0272'	B2 0E	PROC	NEAR	;	
0274'	A1 0003"	ASSUME	EB: CRTDAT	;	GET CURSOR POS. ADD. HIGH
0277'	A3 000C	MOV	DL, CURPSH	;	GET CURRENT LINE ADDRESS
027A'	EB 01FA	MOV	AX, THSLIN	;	SAVE NEW CUR. POS. = THSLIN
027D'	EB 0209	CALL	CURPOS,AX	;	GET CURSOR LOGICAL POSITION
0280'	C7 06 0008" 0000	CALL	PTRADJ	;	DO DOUBLE WRITE TO CRT
		MOV	PTRUP	;	BET CHARACTER COUNT TO ZERO
		MOV	CRTCNT,0	;	IMPLIES CURSOR AT POS. 0 ON LINE
0286'	E9 FDDD	JMP	CRTRET	;	ALL DONE
0289'		LINFED		;	
0289'	A1 0003"	PROC	NEAR	;	
028C'	05 0050	ASSUME	EB: CRTDAT	;	GET CURRENT CURPOS
028F'	25 07FF	MOV	AX, CURPOS	;	ADJUST CURSOR FORWARD 80 CHARB.
0292'	A3 0003"	ADD	AX,80	;	MAGIC NUMBER !!
0295'	A1 0003"	AND	AX,7FFH	;	SAVE NEW CURSOR
0298'	05 0050	MOV	CURPOS,AX	;	GET CURRENT LINE ADDRESS
029B'	25 07FF	MOV	AX, THSLIN	;	ADJUST THSLINE FORWARD ONE LINE
029E'	A3 0003"	ADD	AX,80	;	MAGIC NUMBER
02A1'	EB 01CD	AND	AX,7FFH	;	SAVE NEW LINE ADDRESS
02A4'	97	MOV	THSLIN,AX	;	GET LINE LOGICAL LOCATION
02A5'	B3 3E 0009" 00	CALL	PTRADJ	;	PUT IT IN DI
02AA'	75 58	XCHG	DI,AX	;	
02AC'	A1 0007"	CMP	STATLN,0	;	Q: FULL SCREEN TTY ?
02AF'	EB 018F	JNE	SPECLF	;	N: THEN DO SPECIAL LINFED
0282'	39 C7	MOV	AX, DIBEND	;	GET END OF DISPLAY POINTER
0284'	77 0E	CALL	PTRADJ	;	GET DISPLAY END LOGICAL LOCATION
0286'	A1 0003"	CMP	DI,AX	;	SEE IF WENT PAST END OF DISPLAY
0289'	B2 0E	JA	ADJBEG	;	GO CAUSE A SCROLL OF ONE LINE
028B'	EB 0183	MOV	AX, CURPOS	;	GET NEW CURSOR PHYSICAL POSITION
028E'	EB 01C8	MOV	DL, CURPSH	;	DL = CUR. POS. ADDRESS HIGH REGISTER
		CALL	PTRADJ	;	GET CURSOR LOGICAL POSITION
		CALL	PTRUP	;	GO UPDATE SCREEN POINTERS

02C1'	E9 FDA2	1528	JMP	CRTRET	, ALL DONE
02C4'	B9 0050	1529	MOV	CX,80	, SET UP CX FOR REP
02C7'	B1 E7 07FF	1530	AND	DI,7FFH	, MAGIC NUMBER !!
02CB'	EB 01C8	1531	CALL	REPIT	, CLEAR ONE LINE W/ DEFAULT ATTRIBUTES
02CE'	A1 0006"	1532	MOV	AX,DI8BEO	, GET BEGINNING OF DISPLAY
02D1'	05 0050	1533	ADD	AX,80	, MOVE FORWARD 80 CHARS
02D4'	25 07FF	1534	AND	AX,7FFH	, MAGIC NUMBER !!
02D7'	A3 0006"	1535	MOV	DISBEG,AX	, SAVE NEW DISPLAY BEGINNING
02DA'	A1 0007"	1536	MOV	AX,DI8BEO	, GET END OF DISPLAY POINTER
02DD'	05 0050	1537	ADD	AX,80	, MOVE FORWARD 80 CHARS
02E0'	25 07FF	1538	AND	AX,7FFH	, MAGIC NUMBER !!
02E3'	A3 0007"	1539	MOV	DIBEND,AX	, SAVE NEW END OF DISPLAY
02E6'	B2 0C	1540	MOV	DL,DISSTH	, DL-DISPLAY START ADDRESS
02E8'	A1 0006"	1541	MOV	AX,DISBEO	, AX-DISPLAY BEGIN ADDRESS
02EB'		1542			
02EB'	26F6 06 1811 20	1543	TEST	EB:BYTE PTR CRTSTA,CRTVBL	; G: VERTICAL NON-BLANK ??
02F1'	75 FB	1544	JNZ	BLKT81	; N: WAIT FOR NON-BLANK
		1545			
		1546			
		1547			
		1548			
		1549			
		1550			
		1551			
		1552			
02F3'		1553	TEST	ES:BYTE PTR CRTSTA,CRTVBL	; G: VERTICAL BLANK ??
02F3'	26F6 06 1811 20	1554	JZ	BLKT82	; N: WAIT UNTIL BLANK
02F9'	74 FB	1555			
		1556			
02FB'	EB 0188	1557	CALL	PTRUP	, ...UPDATE BEGIN OF DISPLAY
02FE'	F6 06 000A" 02	1558	TEST	RETFLQ,02H	; G: DOING SPECIAL SCROLL ??
0303'	75 22	1559	JNZ	SPCLF1	; Y: THEN MOVE STATUS LINE DOWN
0305'	EB AF	1560	JMP	SHORT LF1	; N: THEN GO BACK
		1561			
0307'		1562	MOV	DX,STATLN	; DX = STATUS LINE COL,ROW
0307'	8B 16 0009"	1563	XOR	DH,DH	; MAKE SURE COL = 0
0308'	30 F6	1564	XOR	AH,AH	; CLEAR AH
030D'	30 E4	1565	MOV	AL,80	
030F'	B0 50	1566	MUL	DL	
0311'	F6 E2	1567	ADD	AX,DISBEO	; AX = STAT. ROW OFFSET FROM 0
0313'	03 06 0006"	1568	CMP	DI,AX	; SI = STAT ABS LOCATION FROM DISBEO
0317'	39 C7	1569	JB	LF1	; G: CURRENT LINE (STATUS LINE ?
0319'	72 9B	1570			; Y: THEN UPDATE CURSOR POINTER
031B'		1571			; N: THEN SCROLL TTY REGION
031B'	80 0E 000A" 02	1572	OR	RETFLQ,02H	; SET CONDITIONAL RETURN FLAG
0320'	8B 3E 0007"	1573	MOV	DI,DI8BEO	; GET END OF DISPLAY
032A'	47	1574	INC	DI	; START WRITING BLANKS @ DISEND + 1
0325'	EB 9D	1575	JMP	SHORT ADJBEG	; GO SCROLL THE SCREEN AND COME BACK
0327'		1576			
0327'	8B 16 0009"	1577	MOV	DX,STATLN	; DX = SOURCE COL,ROW = STATLN - 1
0328'	4A	1578	DEC	DX	; BX = DEST. COL,ROW = STATLN
032C'	8B 1E 0009"	1579	MOV	BX,STATLN	
0330'	A1 0009"	1579	MOV	AX,STATLN	

1632 / CRT TO THE CALLER (NOTE: this is not applicable to PEGASUS
1633 / but is provided for compatibility with the IBM CRT calling
1634 / conventions), IT DOES NOTHING AND SIMPLY RETURNS.
1635 /

1636 / INPUT - AH = 15
1637 /

1638 / OUTPUT - (To be determined)
1639 /

1640 / USED -
1641 /

1642 / *****
1643 /

1644 / VIDSTA PROC NEAR
1645 / JMP CRTRET

1646 / *****
1647 / THIS ROUTINE IS USED TO WRITE A GIVEN BLOCK OF CHARACTERS TO
1648 / THE SCREEN STARTING AT THE CURSOR USING THE SPECIFIED ATTRIBUTE.
1649 / ALL CONTROL CHARACTERS (i.e. CR,LF,BEL etc.) ARE IGNORED AS SUCH
1650 / AND PRINTED ON THE SCREEN AS ASCII CHARACTERS.
1651 /

1652 / INPUT - AL = Attribute of characters to write
1653 / DX = Segment location of character block
1654 / BX = Starting location of character block
1655 / CX = Length of character block (not to exceed 2000)
1656 /

1657 / NOTE: This routine does not increment the cursor
1658 / location. The caller must be aware of the
1659 / cursor location before using this routine
1660 / and make sure that the block will "fit" on
1661 / the screen or he will lose what want "fit". *
1662 /

1663 / OUTPUT - None
1664 /

1665 / USED - None
1666 /

1667 / * CX (block length) must be (or = 2000-((cur. row - 1 * 80)+cur. col.)
1668 / Attempting to write characters beyond the end of screen will cause
1669 / them to be lost (not written).
1670 /

1671 / *****
1672 /

1673 / WATBLK PROC NEAR
1674 / ASSUME ES:CRIDAT
1675 / MOV ES:BYTE PTR ATLAT,AL ; WRITE THE ATTRIBUTE LATCH
1676 / ; ... AND FALL THROUGH TO DO
1677 / ; ... THE BLOCK WRITE
1678 / *****

1679 / THIS ROUTINE IS USED TO WRITE A GIVEN BLOCK OF CHARACTERS TO
1680 / THE SCREEN STARTING AT THE CURSOR ALL CONTROL CHARACTERS TO
1681 / (i.e. CR,LF,BEL etc.) ARE IGNORED AS SUCH AND PRINTED ON THE
1682 / SCREEN AS ASCII CHARACTERS.
1683 /

039C' 26A2 1800

```
1684 ,  
1685 ,  
1686 ,  
1687 ,  
1688 ,  
1689 ,  
1690 ,  
1691 ,  
1692 ,  
1693 ,  
1694 ,  
1695 ,  
1696 ,  
1697 ,  
1698 ,  
1699 ,  
1700 ,  
1701 ,  
1702 ,  
1703 ,  
1704 ,  
1705 ,  
1706 ,  
1707 ,  
1708 ,  
1709 ,  
1710 ,  
1711 ,  
1712 ,  
1713 ,  
1714 ,  
1715 ,  
1716 ,  
1717 ,  
1718 ,  
1719 ,  
1720 ,  
1721 ,  
1722 ,  
1723 ,  
1724 ,  
1725 ,  
1726 ,  
1727 ,  
1728 ,  
1729 ,  
1730 ,  
1731 ,  
1732 ,  
1733 ,  
1734 ,  
1735 ,  
  
03A0'      ,  
03A0' 83 F9 00  
03A3' 76 1B  
03A5' 53  
03A6' A1 0003"  
03A9' EB 00C5  
03AC' 97  
03AD' A1 0007"  
03B0' EB 00BE  
03B3' 58  
03B4' 88 F3  
03B6' 1E  
03B7' 8E DA  
03B9'      ,  
03B9' A4  
03BA' 39 C7  
03BC' 77 02  
03BE' E2 F9  
03C0'      ,  
03C0' 1F  
03C1' E9 FCA2  
  
INPUT - DX = Segment location of character block  
        BX = Starting location of character block  
        CX = Length of character block (not to exceed 2000)  
  
NOTE: This routine does not increment the cursor  
       location. The caller must be aware of the  
       cursor location before using this routine  
       and make sure that the block will "fit" on  
       the screen or he will lose what want "fit". *  
       With this type of write the characters will  
       take on the attributes of the last attribute  
       change.  
  
OUTPUT - None  
  
USED - None  
  
* CX (block length) must be ( or = 2000-((cur. row - 1 * 80)+cur. col.)  
  Attempting to write characters beyond the end of screen will cause  
  them to be lost (not written).  
  
*****  
WBLOCK      PROC      NEAR  
ABSUME      ES: CRTDAT  
CMP         CX, 0  
JBE        TOOFAR  
           BX  
PUSH       AX, CURPOS  
CALL       PTRADJ  
XCHQ       DI, AX  
MOV        AX, DIBEND  
CALL       PTRADJ  
POP        BX  
MOV        SI, BX  
PUSH       DS  
MOV        DS, DX  
  
BLKLOP:  
MOV        BYTE PTR [DI], BYTE PTR [BI] , ...BLOCK LENGTH TIMES  
CMP        DI, AX  
JBE        TOOFAR  
           BLKLOP  
  
TOOFAR:  
POP        DS  
JMP        CRTRET , ...AND RETURN  
*****  
THIS ROUTINE CHANGES THE ENTIRE SCREEN ATTRIBUTES  
RIGHT BEFORE YOUR VERY EYES  
  
INPUT - AL = ATTRIBUTE TO USE  
  
; O: ANY CHARACTER8 TO WRITE ?  
; N: THEN DON'T WRITE ANY  
; SAVE BX  
; GET CURSOR PHYSICAL POSITION  
; GET IT'S LOGICAL POSITION  
; PUT IT IN DI  
; GET DISPLAY END PHYSICAL LOCATION  
; GET IT'S LOGICAL LOCATION  
; RESTORE BX  
; BI = BLOCK STARTING ADDRESS  
; DS = BLOCK SEGMENT  
  
; G: OFF THE END OF SCREEN ?  
; Y: THEN QUIT WRITING  
; N: KEEP GOING  
  
; RESTORE MY DS  
; ...AND RETURN  
*****
```

```
1736 ,  
1737 ,  
1738 ,  
1739 ,  
1740 ,  
1741 ,  
1742 ,  
1743 ,  
1744 ,  
1745 ,  
1746 ,  
1747 ,  
1748 ,  
1749 ,  
1750 ,  
1751 ,  
1752 ,  
1753 ,  
1754 ,  
1755 ,  
1756 ,  
1757 ,  
1758 ,  
1759 ,  
1760 ,  
1761 ,  
1762 ,  
1763 ,  
1764 ,  
1765 ,  
1766 ,  
1767 ,  
1768 ,  
1769 ,  
1770 ,  
1771 ,  
1772 ,  
1773 ,  
1774 ,  
1775 ,  
1776 ,  
1777 ,  
1778 ,  
1779 ,  
1780 ,  
1781 ,  
1782 ,  
1783 ,  
1784 ,  
1785 ,  
1786 ,  
1787 ,  
  
03C4' 03C8' 03CB' 03C8' 03CE' 03D2' 03D5' 03D6' 03DB' 03DB' 03E0' 03E2' 03E5' 03EA' 03ED' 03EF' 03F2' 03F5' 03F7' 03FA' 03FC' 03FF' 0402'  
BB 36 0006"  B9 07D0  268A 24  26A2 1800  268B 24  46  E2 F3  E9 FC8B  80 0E 000A" 02  31 CO  E8 0002"  C6 06 000A" 00  31 FF  E8 00A4  A1 0006"  B2 OC  E8 008F  B2 0E  A1 0003"  E8 0087  E9 FC61  
CHSCAT PROC NEAR ES: CRTDAT, DS: RONDAT ; GET DISBEQ IN BI  
ASSUME SI, DISBEQ ; SET UP CX FOR LOOP  
MOV CX, 2000  
AGAIN: MOV AH, ES: MEMBEQ(SI) ; READ A CHARACTER  
MOV MOV PTR EB: ATLAT, AL ; WRITE THE ATTRIBUTE LATCH  
MOV EB: MEMBEQ(SI), AH ; WRITE THE CHARACTER BACK TO MEMORY  
INC SI ; NEXT CHARACTER IN MEMORY  
LOOP AGAIN ; LOOP THROUGH ENTIRE SCREEN  
JMP CRTRET ; ALL DONE  
; *****  
; CLEAR THE CRT SCREEN MEMORY AND HOME CURSOR  
; *****  
INPUT - None  
NOTE: This routine will also clear the status line  
but will not clear the status line setting.  
If the user wishes to keep what is on the  
status line he must rewrite it after clearing  
the screen.  
OUTPUT - None  
USED - AX, CX, DL, DI  
; *****  
CLRTXT PROC NEAR ; CLEAR SCREEN PROCEDURE  
OR ES: CRTDAT ;  
XOR AX, AX ; Set conditional return flag  
CALL INILST ; Clear AX  
MOV RETFLG, 00H ; Init local storage  
MOV CX, 2048 ; SET UP CX FOR REP  
XOR DI, DI ; CLEAR DI FOR REP  
CALL REPIT ; Go clear the screen using HIGHAT  
MOV AX, DISBEQ ; GET BEGIN OF DISPLAY POINTER  
MOV DL, DIS9TH ; GET DISPLAY START ADD. REG HIGH  
CALL PTRUP ; GO UPDATE DISPLAY BEGIN  
MOV DL, CURPSH ; GET CUR. POS. ADDR. REG. HIGH  
MOV AX, CURPOS ; GET CURSOR POSITION  
CALL PTRUP ; GO HOME THE CURSOR  
JMP CRTRET ; ALL DONE  
; *****
```

THIS ROUTINE IS USED TO CLEAR ALL THREE GRAPHICS BANKS.

INPUT - None
OUTPUT - None
USED -

```
1788  
1789  
1790  
1791  
1792  
1793  
1794  
1795  
1796  
1797  
1798  
1799  
1800  
1801  
1802  
1803  
1804  
1805  
1806  
1807  
1808  
1809  
1810  
1811  
1812  
1813  
1814  
1815  
1816  
1817  
1818  
1819  
1820  
1821  
1822  
1823  
1824  
1825  
1826  
1827  
1828  
1829  
1830  
1831  
1832  
1833  
1834  
1835  
1836  
1837  
1838  
1839
```

```
0403'          PROC NEAR          ; CLEAR GRAPHICS MEMORY  
0403'          ASSUME EB:NOTHING  
0408'          MOV AX,ORABEO  
040A'          MOV EB,AX  
040A'          CALL CLRIT  
040D'          MOV AX,ORBBEO  
0410'          MOV EB,AX  
0412'          CALL CLRIT  
0415'          MOV AX,CRCBEO  
0418'          MOV EB,AX  
041A'          CALL CLRIT  
041D'          JMP CRTRET  
0420'          CLRIT:  
0420'          XOR AX,AX  
0422'          XOR DI,DI  
0424'          MOV CX,SCRLEN  
0427'          REP  
0428'          BT08 WORD PTR [DI]  
0429'          RET  
0429'          ; DO IT  
0429'          ; ...AND RETURN  
0429'          ; CLEAR AX  
0429'          ; CLEAR DI  
0429'          ; CX = BANK LENGTH FOR REP
```

```
*****  
BET ATTRIBUTE LATCH  
AH = 16H  
*****
```

THIS ROUTINE IS USED TO WRITE THE ATTRIBUTE LATCH WITHOUT HAVING TO WRITE A CHARACTER.

INPUT - BL = Attribute to use
OUTPUT - Latch set accordingly

```
1829  
1830  
1831  
1832  
1833  
1834  
1835  
1836  
1837  
1838  
1839  
042A'          PROC NEAR          ; SET ATTRIBUTE LATCH  
042A'          ASSUME EB:CRIDAT  
042F'          MOV EB:BYTE PTR ATTATL,BL ; WRITE THE ATTRIBUTE LATCH  
042F'          JMP CRTRET          ; ...AND RETURN  
*****
```

GET PHYSICAL DISPLAY BEGIN OFFSET
AH = 17H

THIS ROUTINE IS USED TO RETURN THE OFFSET (FROM DE00H) OF THE PHYSICAL BEGINNING OF DISPLAY.

INPUT - NONE

OUTPUT - DX = Display begin offset

0432' BB 16 0006"
0432' E9 FC2D
0436'

```

1840 ;
1841 ;
1842 ;
1843 ;
1844 ;
1845 ;
1846 ;
1847 ;
1848 ;
1849 ;
1850 OTDSBO PROC NEAR
1851 MOV DX,DISBEG ; DX = display begin
1852 JMP CRTRET ; ...adn return
1853 ;
1854 ;
1855 ;
1856 ;
1857 ;
1858 ;
1859 ;
1860 ;
1861 ;
1862 ;
1863 ;
1864 ;
1865 ;
1866 ;
1867 ;
1868 ;
1869 ;
1870 ;

```

PRINT TTY STRING
AH = 1BH

THIS ROUTINE IS USED TO PRINT A STRING OF CHARACTERB IN THE USERB CB: (GOTTEN FROM THE STACK) WITH A TTY WRITE.

INPUT - BX = Address (offset) of the string
Where: [BX] byte 0 = length of string
[BX] byte 1 = first char. of string

OUTPUT - NONE

```

0439' 1E
0439' 55
043A' 8B EC
043B' B3 C5 12
043D' 8E 9E 00
0440' 8B EB
0443' 30 ED
0445' 3EBA 4E 00
0447' 21 C9
0448' 74 1D
044D' 45
044F' 3EBA 46 00
0450' 51
0454' 1E
0455' 2E8E 1E C180
0456' 80 0E 000A 04
0458' EB FDB5
0460' 80 26 000A FB
0463' 1F
0468'

1871 PROC NEAR
1872 DS
1873 BP
1874 BP, BP
1875 BP, IB
1876 DS, 89: WORD PTR [BP]
1877 BP, BX
1878 CH, CH
1879 CL, DS: BYTE PTR [BP]
1880 CX, CX
1881 PTYEND
1882 ;
1883 INC BP
1884 AL, DS: BYTE PTR [BP]
1885 CX
1886 PUSH DS
1887 DS, WORD PTR CS: DSADDR+CSWRAP ; SET UP DS AS ROMDAT
1888 RETFLG, 04H ; Set conditional return flag
1889 WRITTY ; Do TTY character write
1890 RETFLG, NOT 04H ; Reset flag in case of last char.
1891 POP DS

```

```
0469' 59      POP      CX      ; Restore CX
046A' E2 E3   LOOP     PTYLP     ; Do entire string
1894
1895          PTYEND:
1896          POP      BP      ; Restore BP
1897          POP      DS      ; Restore DS (ROMDAT pointer)
1898          JMP      CRTRET  ; ...and return
1899
1900          ;*****
1901          ; FOLLOING ARE VARIOUS SUBROUTINES THAT THE CRT
1902          ; FUNCTIONS USE AT DIFFERENT TIMES. I CALL THIS
1903          ; SET OF ROUTINES THE "CRT UTILITIES". NOT TO BE
1904          ; CONFUSED WITH "WATER WORKS" AND "ELECTRIC COMPANY".
1905          ;*****
1906
1907          ;*****
1908          ; THIS SUBROUTINE FIGURES OUT ANY POINTER POSITION IN FRONT OF THE
1909          ; BEGINNING OF THE DISPLAY. THE POINTERS ARE STORED LOCALLY IN AN
1910          ; ABSOLUTE ADDRESS FORM AND ARE SOMETIMES BEHIND THE DISBEO POINTER.
1911          ; FOR THAT CASE, THIS CODE FIGURES OUT THE ABSOLUTE ADDRESS OF THE
1912          ; POINTER IN FRONT OF DISBEO AND RETURNS IT IN REGISTER AX.
1913
1914
1915
1916
1917          INPUT - AX = POINTER ABSOLUTE ADDRESS
1918
1919          OUTPUT - AX = POINTER ADDRESS IN FRONT OF DISBEO
1920                  BX = DISBEO POINTER
1921
1922          REGISTERS USED - AX, BX
1923          ;*****
1924
1925          PTRADJ  PROC      NEAR
1926                  ASSUME  ES: CRTDAT
1927                  MOV     BX, DISBEO
1928                  CMP     AX, BX
1929                  JL      ADJPTR
1930                  RET
1931
1932          ADJPTR:  ADD     AX, 2048
1933                  RET
1934
1935          ;*****
1936          ; THIS SUBROUTINE PERFORMS THE LOOPING MECHANISM REQUIRED TO WRITE
1937          ; DATA TO THE CRT CONTROLLER. IT WRITES THE REGISTER NUMBER PASSED
1938          ; TO IT INTO THE CRT ADDRESS REGISTER. INCREMENTS TO THE CRT WRITE
1939          ; REGISTER AND WRITES THE VALUE PASSED INTO IT AND DECREMENTS BACK
1940          ; TO THE CRT ADDRESS REGISTER.
1941
1942          INPUT - DL = THE CRT REGISTER NUMBER TO PUT IN THE CRTADR
1943          ; AH = THE VALUE TO PUT IN THE CRT WRITE REG.
```

```
1944 ,  
1945 ,  
1946 ,  
1947 ,  
1948 ,  
1949 ,  
1950 ,  
1951 ,  
1952 ,  
1953 ,  
1954 ,  
1955 ,  
1956 ,  
1957 ,  
1958 ,  
1959 ,  
1960 ,  
1961 ,  
1962 ,  
1963 ,  
1964 ,  
1965 ,  
1966 ,  
1967 ,  
1968 ,  
1969 ,  
1970 ,  
1971 ,  
1972 ,  
1973 ,  
1974 ,  
1975 ,  
1976 ,  
1977 ,  
1978 ,  
1979 ,  
1980 ,  
1981 ,  
1982 ,  
1983 ,  
1984 ,  
1985 ,  
1986 ,  
1987 ,  
1988 ,  
1989 ,  
1990 ,  
1991 ,  
1992 ,  
1993 ,  
1994 ,  
1995 ,  
  
047E' ,  
047E' 268B 16 1810 ,  
0483' 268B 26 1812 ,  
048B' C3 ,  
  
0489' ,  
0489' FA FFF1 ,  
048A' EB E0 ,  
048D' BA E0 ,  
048F' FE C2 ,  
0491' EB FFEA ,  
0494' FB ,  
0495' C3 ,  
  
1944 ,  
1945 ,  
1946 ,  
1947 ,  
1948 ,  
1949 ,  
1950 ,  
1951 ,  
1952 ,  
1953 ,  
1954 ,  
1955 ,  
1956 ,  
1957 ,  
1958 ,  
1959 ,  
1960 ,  
1961 ,  
1962 ,  
1963 ,  
1964 ,  
1965 ,  
1966 ,  
1967 ,  
1968 ,  
1969 ,  
1970 ,  
1971 ,  
1972 ,  
1973 ,  
1974 ,  
1975 ,  
1976 ,  
1977 ,  
1978 ,  
1979 ,  
1980 ,  
1981 ,  
1982 ,  
1983 ,  
1984 ,  
1985 ,  
1986 ,  
1987 ,  
1988 ,  
1989 ,  
1990 ,  
1991 ,  
1992 ,  
1993 ,  
1994 ,  
1995 ,  
  
OUTPUT - DL = UNCHANGED  
AH = UNCHANGED  
  
REGISTERS USED - DL, AH  
  
*****  
CTRLMR PROC NEAR  
ASSUME ES: CRTDAT  
MOV BYTE PTR EB: CRTADR, DL ;;; PUT CRT REG. # INTO CRTADR.  
MOV BYTE PTR ES: WRTREG, AH ;;; PUT VALUE IN CRT WRITE REG.  
RET ;;; RETURN  
  
*****  
THIS SUBROUTINE PERFORMS THE DOUBLE CALL TO THE CTRLMR SUBROUTINE.  
THIS DOUBLE CALL IS REQUIRED TO UPDATE CRT SCREEN POINTERS THAT  
ARE ACCESSED VIA TWO CRT ADDRESS REGISTERS (HIGH AND LOW).  
  
INPUT - DL = THE CRT REGISTER (HIGH) TO WRITE INTO THE CRTADR.  
* NOTE: LOW REGISTER IS ASSUMED TO BE NEXT BY INCREMENTING DL  
AX = ADDRESS OF SCREEN POINTER TO BE CHANGED  
  
OUTPUT - DL = POINTS TO CRT LOW REGISTER  
AH = AL = LOW ORDER ADDRESS OF SCREEN POINTER IN QUESTION  
  
REGISTERS USED - DL, AX  
  
SUBROUTINES CALLED : CTRLMR  
  
*****  
PTRUP PROC NEAR  
ASSUME ES: CRTDAT  
CLI  
CALL CTRLMR  
MOV AH, AL  
INC DL  
CALL CTRLMR  
STI  
RET  
  
; Shields up  
; WRITE TO CRTC  
; AH = START ADD. OF DISPLAY LOW  
; DL = DISPLAY STRT ADD. REG LOW  
; WRITE TO CRTC  
; Shields down  
; ALL DONE  
  
*****  
THIS ROUTINE IS USED TO CLEAR "CX" AMOUNT OF SCREEN MEMORY  
USING THE DEFAULT (i.e. HIGHAT) ATTRIBUTES ON THE BLANKS  
THAT IT WRITES.  
  
INPUT - DI = POINTS TO THE STARTING MEMORY ADDRESS OF THE REP  
CX = NUMBER OF BYTES TO CLEAR  
EB = CRTDAT  
DS = ROMDAT  
  
OUTPUT - DI = DI+CX
```

```

1996 ,
1997 ,
1998 ,
1999 ,
2000 ,
2001 ,
2002 ,
2003 ,
2004 ,
2005 ,
2006 ,
2007 ,
2008 ,
2009 ,
2010 ,
2011 ,
2012 ,
2013 ,
2014 ,
2015 ,
2016 ,
2017 ,
2018 ,
2019 ,
2020 ,
2021 ,
2022 ,
2023 ,
2024 ,
2025 ,
2026 ,
2027 ,
2028 ,
2029 ,
2030 ,
2031 ,
2032 ,
2033 ,
2034 ,
2035 ,
2036 ,
2037 ,
2038 ,
2039 ,
2040 ,
2041 ,
2042 ,
2043 ,
2044 ,
2045 ,
2046 ,
2047 ,

0496' ,
0496' EB 0008
0499' B0 20
0498' F2
049C' AA
049D' EB 0013
04A0' C3

CX = 0
AH = SAVED ATTRIBUTE
AL = SPACE

*****
USED - AX,DI,CX
*****

REPIT PROC NEAR
CALL SAVATT
MOV AL,SPACE
REP
STOB BYTE PTR [DI]
CALL RSTATT
RET

*****
THIS ROUTINE IS USED TO SAVE THE CURRENT ATTRIBUTE LATCH
AND RESET IT TO THE DEFAULT SETTING FOR SUBSEQUENT WRITES.

INPUT - ES = CRTDAT
DB = ROMDAT

OUTPUT - LATCH SET TO HIGHAT (OFH)

USED - NONE
*****

SAVATT PROC NEAR
PUSH AX
MOV AH,EB:BYTE PTR ATTATL ; SAVE ATTRIBUTE IN AH
MOV ATTSV,AH
MOV EB:BYTE PTR ATTATL,HIGHAT ; SET LATCH TO DEFAULT
POP AX
RET ; ...AND LEAVE

*****
THIS ROUTINE IS USED TO RESTORE THE ATTRIBUTE LATCH TO IT'S
LAST SETTING.

INPUT - ES = CRTDAT

OUTPUT - LATCH SET TO LAST SAVED BETTING

USED - NONE
*****

RSTATT PROC NEAR
PUSH AX
MOV AH,ATTSV
MOV EB:BYTE PTR ATTATL,AH
POP AX
RET ; GET THE OLD LATCH BETTING
; RESTORE THE LATCH
; ...AND LEAVE

```


CRTDSR (CRT DSR CODE)
MAIN CRTDSR.SRC

CR8086/11 version 10.34.17

3-Aug-83 16:34:59

Page 2-27

2048 ,
2049 SUBTTL
2050 END

No errors detected


```

1 *****
2 ; TITLE - CRTTBL (CRT INITIALIZATION TABLES)
3 ; ABSTRACT - THIS MODULE CONTAINS THE INITIALIZATION TABLES FOR EACH OF THE
4 ;           VARIOUS MONITORS THAT MIGHT BE PRESENT WITH PEGASUS. THE TYPE
5 ;           OF MONITOR IS DETERMINED BY READING A JUMPER AND THE CONTROLLER
6 ;           WILL BE SET UP WITH THE APPROPRIATE VALUES FROM THE APPROPRIATE
7 ;           TABLE CONTAINED HEREIN.
8 ; *****
9 NAME CRTTBL (CRT INITIALIZATION TABLES)
10 SUBTTL
11 DEBUO EGU OFFFFH
12 *****
13 ; *****
14 ; PUBLIC DEFINITIONS
15 ; *****
16 ;
17 PUBLIC STAMON
18 PUBLIC ALTMON
19 PUBLIC TSTPAT
20 *****
21 ; *****
22 ; MODULE ENTRY POINT
23 ; *****
24 ;
25 ; THIS IS THE 60 HZ CRT PROGRAMMING TABLE
26 ;
27 ;
28 ;
29 ;
30 ;
31 ;
32 ;
33 ;
34 ;
35 ;
36 ;
37 ;
38 ;
39 ;
40 ;
41 ;
42 ;
43 ;
44 ;
45 ;
46 ; *****
47 ;
48 ;
49 ;
50 ;
51 ;
52 ;
53 ; *****

```

--FFFF

--0000

0000'	STAMON	DB	104-1	;;	INITIALIZATION	H TOTAL CHAR
0001'	DB	80	;;		H DISP CHAR	
0002'	DB	84	;;	VALUES	H SYNC POSITION	
0003'	DB	3*16+9	;;	FOR	V SYNC, HSYNC WIDTHS	
0004'	DB	25-1	;;	THE	V TOTAL ROWS	
0005'	DB	20	;;	6545	V TOTAL LINE ADJUST	
0006'	DB	25	;;	CRT	V DISP ROWS	
0007'	DB	25	;;	CONTROLLER	V SYNC POSITION	
0008'	DB	00H	;;		MODE CONTROL	
0009'	DB	12-1	;;		SCAN LINES PER ROW	
000A'	DB	40H+0	;;		CURSOR START	
000B'	DB	11	;;		CURSOR END	
000C'	DB	00H	;;		DISPLAY START HIGH	
000D'	DB	00H	;;		DISPLAY START LOW	
000E'	DB	00H	;;		CURSOR HIGH	
000F'	DB	00H	;;		CURSOR LOW	

THIS IS THE TABLE FOR 50HZ EUROPEAN OPERATION

0010'	ALTMON	DB	104-1	;;	INITIALIZATION	H TOTAL CHAR
0011'	DB	80	;;		H DISP CHAR	
0012'	DB	84	;;	VALUES	H SYNC POSITION	

0013' 39	DB	3*16+9	;;	VSYNC, HSYNC WIDTHS
0014' 19	DB	26-1	;;	V TOTAL ROWS
0015' 14	DB	20	;;	V TOTAL LINE ADJUST
0016' 19	DB	25	;;	V DISP ROWS
0017' 19	DB	25	;;	V SYNC POSITION
0018' 00	DB	00H	;;	MODE CONTROL
0019' 00	DB	14-1	;;	SCAN LINES PER ROW
001A' 40	DB	40H+0	;;	CURSOR START
001B' 08	DB	11	;;	CURSOR END
001C' 00	DB	00H	;;	DISPLAY START HIGH
001D' 00	DB	00H	;;	DISPLAY START LOW
001E' 00	DB	00H	;;	CURSOR HIGH
001F' 00	DB	00H	;;	CURSOR LOW

 THIS IS A TABLE OF TEST PATTERNS FOR THE VARIOUS CRTC REGISTERS
 WHICH IS USED DURING THE POWERUP DIAGNOSTICS.

0020' F0	DB	0FOH	;;	TBITPAT	DB	0FOH	;;	BYTE
0021' CC	DB	0CCH	;;		DB	0CCH	;;	
0022' AA	DB	0AAH	;;		DB	0AAH	;;	
0023' 55	DB	055H	;;		DB	055H	;;	
0024' F0	DB	0FOH	;;		DB	0FOH	;;	
0025' CC	DB	0CCH	;;		DB	0CCH	;;	
0026' AA	DB	0AAH	;;		DB	0AAH	;;	

No errors detected

```
1 *****  
2 ; TITLE - CRTTST (CRT DIAGNOSTICS)  
3 ; ABSTRACT - THIS MODULE IS RESPONSIBLE FOR THE CRT POWERUP DIAGNOSTIC  
4 ; TESTS. IF THE TEST PASSES THEN THE LED'S ARE DECREMENTED  
5 ; FROM THEIR CURRENT VALUE BY ONE AND POWERUP CONTINUES. IF  
6 ; THE TEST FAILS AN ERROR CODE IS OUTPUT TO THE PRINTER PORT  
7 ; AND THE POWERUP SEQUENCE AND TESTING STOPS.  
8 ; *****  
9 NAME CRTTST (CRT DIAGNOSTICS)  
10 SUBTTL  
11 EQU OFFFHH  
12  
13 *****  
14 ; PUBLIC DEFINITIONS  
15 ; *****  
16 PUBLIC INICRT  
17 PUBLIC INILST  
18 PUBLIC CRTTST  
19 PUBLIC CRTINI  
20 ; *****  
21 ; EXTERNAL REFERENCES  
22 ; *****  
23 ; EXTERNAL JUMPS AND CALLS  
24 ; *****  
25 EXTRN CRTRET:NEAR ; (CRTDSR)  
26 EXTRN DSPDIE:NEAR ; (OUTPUT)  
27 EXTRN DECLED:NEAR ; (ROMUTL)  
28 EXTRN MSCTST:NEAR ; (MSCTST)  
29 EXTRN PRTRST:NEAR ; (TINTTS)  
30 ; *****  
31 ; EXTERNAL EQUATES  
32 ; *****  
33 ;  
34 EXTRN ATMERR:ABS ; (TSERR)  
35 EXTRN ATTERR:ABS ; (TSERR)  
36 EXTRN CINTER:ABS ; (TSERR)  
37 EXTRN CURERR:ABS ; (TSERR)  
38 EXTRN GRAMER:ABS ; (TSERR)  
39 EXTRN VIDERR:ABS ; (TSERR)  
40 EXTRN VBLERR:ABS ; (TSERR)  
41 EXTRN LED010:ABS ; (ROMERR)  
42 ; *****  
43 ; EXTERNAL TABLES  
44 ; *****  
45 ;  
46 SECT ROMCOD  
47 ASSUME CS:ROMCOD  
48 ;  
49 EXTRN STAMON:BYTE ; (CRTTBL)  
50 EXTRN ALTMON:BYTE ; (CRTTBL)  
51 EXTRN TSPAT:BYTE ; (CRTTBL)  
52 ; *****  
53 ; *****
```

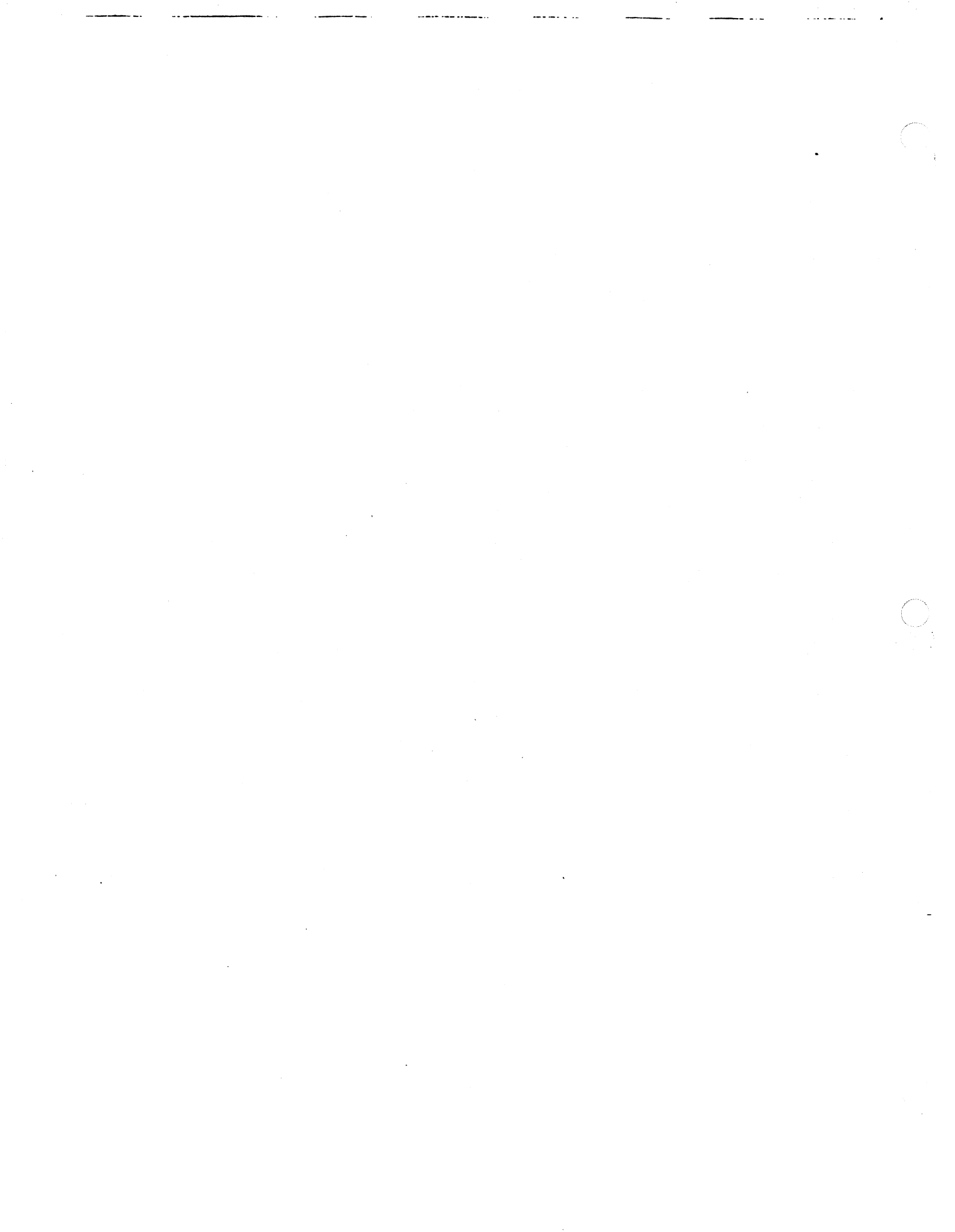
54 ;
55 ;
56 ;
57 ;
58 ;
59 ;
60 ;
61 ;
62 ;
63 ;
64 ;
65 ;
66 ;
67 ;
68 ;
69 ;

-0000

EXTERNAL STORAGE

BECT	ROMDAT	
EXTRN	CRTCNT: WORD	(ROMDAT)
EXTRN	CRTRR: BYTE	(ROMDAT)
EXTRN	CRTHLD: BYTE	(ROMDAT)
EXTRN	CURPOS: WORD	(ROMDAT)
EXTRN	CURTYP: WORD	(ROMDAT)
EXTRN	DISBEG: WORD	(ROMDAT)
EXTRN	DISEND: WORD	(ROMDAT)
EXTRN	PRCO_M: BYTE	(ROMDAT)
EXTRN	RETFLO: BYTE	(ROMDAT)
EXTRN	THSLIN: WORD	(ROMDAT)
EXTRN	STATLN: WORD	(ROMDAT)

```
71 ;*****  
72 ; LOCAL CONSTANTS  
73 ;*****  
74 ; INCLUDE PEG: ASCII.EQU  
75 ; INCLUDE PEG: PORTADDR.EQU  
76 ; INCLUDE PEG: LATCHES.EQU  
77 ; INCLUDE PEG: CRT.EQU  
78 ; INCLUDE PEG: VECTOR.EQU  
79 ;  
330 ;*****  
331 ; LOCAL MACROS  
332 ;*****  
333 ;*****  
334 ;  
335 ;  
336 ;  
337 ; SUBTTL MAIN  
338 ;*****  
339 ; MODULE ENTRY POINT  
340 ;*****  
341 ; THIS IS THE TEMPORARY INITIALIZATION DONE AT FIRST SIGN OF  
342 ; POWER ON TO THE UNIT. NO STACK OR RAM IS AVAILABLE AT THIS  
343 ; TIME FOR USE SO THIS IS QUICK AND DIRTY !!!  
344 ;*****  
345 ;  
346 ; SECT ROMCOD  
347 ; ASSUME CS:ROMCOD, DS:ROMCOD, ES:CRTDAT  
348 ;  
349 ; INICRT  
350 ;  
351 ; MOV BX,CRTDAT ; SET UP ES  
352 ; MOV ES,BX ; AS CRTDAT  
353 ; XOR DI,DI ; DI = BEGINNING OF SCREEN MEMORY (0)  
354 ; MOV BYTE PTR ES:ATLAT.HIGHAT, SET ATTRIBUTES TO HIGH BRILLIANCE  
355 ; MOV CX,2048 ; SET UP CX FOR REP STOS  
356 ; MOV AL,SPACE ; PUT A BLANK IN AL  
357 ; REP ; DI=MEMBEG  
358 ; STOSB ; CLEAR SCREEN MEMORY  
359 ; MOV BX,CS ; SET UP DS  
360 ; MOV DS,BX ; TO POINT TO ROMCOD  
361 ; MOV CL,16 ; 16 REGISTERS TO INIT.  
362 ; MOV SI,OFFSET ALTMON ; START AT CRIC REGISTER ZERO  
363 ; IN AL,PRCI_P ; ALTMON = ALTERNATE MONITOR PROG.  
364 ; TEST AL,MONTP ; READ LATCH FOR MONITOR TYPE  
365 ; JZ CRT2 ; Q: LATCH = ALTERNATE MONITOR 7  
366 ; MOV SI,OFFSET STAMON ; Y: THEN TABLE ALREADY SET UP  
367 ; ; N: THEN SI = STANDARD MONITOR  
368 ;  
369 ; MOV BYTE PTR ES:CRTDADR.DL ; OUT REG. NUMBER TO CRTADR REG.  
370 ; MOV BYTE PTR(SI) ; LOAD INIT TABLE VALUE IN REG. AL  
371 ; INC DL ; OUT INIT VALUE TO WRITE REG.  
372 ; LOOP CRT2 ; NEXT REGISTER TO INIT.  
373 ; MOV AL,00 ; LOOP UNTIL ALL INIT.  
374 ;  
375 ; MOV BYTE PTR ES:GR_RED.AL ; SET GRAPHICS PALETTES  
376 ;  
377 ;  
378 ;  
379 ;  
380 ;  
381 ;  
382 ;  
383 ;  
384 ;  
385 ;  
386 ;  
387 ;  
388 ;  
389 ;  
390 ;  
391 ;  
392 ;  
393 ;  
394 ;  
395 ;  
396 ;  
397 ;  
398 ;  
399 ;  
400 ;  
401 ;  
402 ;  
403 ;  
404 ;  
405 ;  
406 ;  
407 ;  
408 ;  
409 ;  
410 ;  
411 ;  
412 ;  
413 ;  
414 ;  
415 ;  
416 ;  
417 ;  
418 ;  
419 ;  
420 ;  
421 ;  
422 ;  
423 ;  
424 ;  
425 ;  
426 ;  
427 ;  
428 ;  
429 ;  
430 ;  
431 ;  
432 ;  
433 ;  
434 ;  
435 ;  
436 ;  
437 ;  
438 ;  
439 ;  
440 ;  
441 ;  
442 ;  
443 ;  
444 ;  
445 ;  
446 ;  
447 ;  
448 ;  
449 ;  
450 ;  
451 ;  
452 ;  
453 ;  
454 ;  
455 ;  
456 ;  
457 ;  
458 ;  
459 ;  
460 ;  
461 ;  
462 ;  
463 ;  
464 ;  
465 ;  
466 ;  
467 ;  
468 ;  
469 ;  
470 ;  
471 ;  
472 ;  
473 ;  
474 ;  
475 ;  
476 ;  
477 ;  
478 ;  
479 ;  
480 ;  
481 ;  
482 ;  
483 ;  
484 ;  
485 ;  
486 ;  
487 ;  
488 ;  
489 ;  
490 ;  
491 ;  
492 ;  
493 ;  
494 ;  
495 ;  
496 ;  
497 ;  
498 ;  
499 ;  
500 ;  
501 ;  
502 ;  
503 ;  
504 ;  
505 ;  
506 ;  
507 ;  
508 ;  
509 ;  
510 ;  
511 ;  
512 ;  
513 ;  
514 ;  
515 ;  
516 ;  
517 ;  
518 ;  
519 ;  
520 ;  
521 ;  
522 ;  
523 ;  
524 ;  
525 ;  
526 ;  
527 ;  
528 ;  
529 ;  
530 ;  
531 ;  
532 ;  
533 ;  
534 ;  
535 ;  
536 ;  
537 ;  
538 ;  
539 ;  
540 ;  
541 ;  
542 ;  
543 ;  
544 ;  
545 ;  
546 ;  
547 ;  
548 ;  
549 ;  
550 ;  
551 ;  
552 ;  
553 ;  
554 ;  
555 ;  
556 ;  
557 ;  
558 ;  
559 ;  
560 ;  
561 ;  
562 ;  
563 ;  
564 ;  
565 ;  
566 ;  
567 ;  
568 ;  
569 ;  
570 ;  
571 ;  
572 ;  
573 ;  
574 ;  
575 ;  
576 ;  
577 ;  
578 ;  
579 ;  
580 ;  
581 ;  
582 ;  
583 ;  
584 ;  
585 ;  
586 ;  
587 ;  
588 ;  
589 ;  
590 ;  
591 ;  
592 ;  
593 ;  
594 ;  
595 ;  
596 ;  
597 ;  
598 ;  
599 ;  
600 ;  
601 ;  
602 ;  
603 ;  
604 ;  
605 ;  
606 ;  
607 ;  
608 ;  
609 ;  
610 ;  
611 ;  
612 ;  
613 ;  
614 ;  
615 ;  
616 ;  
617 ;  
618 ;  
619 ;  
620 ;  
621 ;  
622 ;  
623 ;  
624 ;  
625 ;  
626 ;  
627 ;  
628 ;  
629 ;  
630 ;  
631 ;  
632 ;  
633 ;  
634 ;  
635 ;  
636 ;  
637 ;  
638 ;  
639 ;  
640 ;  
641 ;  
642 ;  
643 ;  
644 ;  
645 ;  
646 ;  
647 ;  
648 ;  
649 ;  
650 ;  
651 ;  
652 ;  
653 ;  
654 ;  
655 ;  
656 ;  
657 ;  
658 ;  
659 ;  
660 ;  
661 ;  
662 ;  
663 ;  
664 ;  
665 ;  
666 ;  
667 ;  
668 ;  
669 ;  
670 ;  
671 ;  
672 ;  
673 ;  
674 ;  
675 ;  
676 ;  
677 ;  
678 ;  
679 ;  
680 ;  
681 ;  
682 ;  
683 ;  
684 ;  
685 ;  
686 ;  
687 ;  
688 ;  
689 ;  
690 ;  
691 ;  
692 ;  
693 ;  
694 ;  
695 ;  
696 ;  
697 ;  
698 ;  
699 ;  
700 ;  
701 ;  
702 ;  
703 ;  
704 ;  
705 ;  
706 ;  
707 ;  
708 ;  
709 ;  
710 ;  
711 ;  
712 ;  
713 ;  
714 ;  
715 ;  
716 ;  
717 ;  
718 ;  
719 ;  
720 ;  
721 ;  
722 ;  
723 ;  
724 ;  
725 ;  
726 ;  
727 ;  
728 ;  
729 ;  
730 ;  
731 ;  
732 ;  
733 ;  
734 ;  
735 ;  
736 ;  
737 ;  
738 ;  
739 ;  
740 ;  
741 ;  
742 ;  
743 ;  
744 ;  
745 ;  
746 ;  
747 ;  
748 ;  
749 ;  
750 ;  
751 ;  
752 ;  
753 ;  
754 ;  
755 ;  
756 ;  
757 ;  
758 ;  
759 ;  
760 ;  
761 ;  
762 ;  
763 ;  
764 ;  
765 ;  
766 ;  
767 ;  
768 ;  
769 ;  
770 ;  
771 ;  
772 ;  
773 ;  
774 ;  
775 ;  
776 ;  
777 ;  
778 ;  
779 ;  
780 ;  
781 ;  
782 ;  
783 ;  
784 ;  
785 ;  
786 ;  
787 ;  
788 ;  
789 ;  
790 ;  
791 ;  
792 ;  
793 ;  
794 ;  
795 ;  
796 ;  
797 ;  
798 ;  
799 ;  
800 ;  
801 ;  
802 ;  
803 ;  
804 ;  
805 ;  
806 ;  
807 ;  
808 ;  
809 ;  
810 ;  
811 ;  
812 ;  
813 ;  
814 ;  
815 ;  
816 ;  
817 ;  
818 ;  
819 ;  
820 ;  
821 ;  
822 ;  
823 ;  
824 ;  
825 ;  
826 ;  
827 ;  
828 ;  
829 ;  
830 ;  
831 ;  
832 ;  
833 ;  
834 ;  
835 ;  
836 ;  
837 ;  
838 ;  
839 ;  
840 ;  
841 ;  
842 ;  
843 ;  
844 ;  
845 ;  
846 ;  
847 ;  
848 ;  
849 ;  
850 ;  
851 ;  
852 ;  
853 ;  
854 ;  
855 ;  
856 ;  
857 ;  
858 ;  
859 ;  
860 ;  
861 ;  
862 ;  
863 ;  
864 ;  
865 ;  
866 ;  
867 ;  
868 ;  
869 ;  
870 ;  
871 ;  
872 ;  
873 ;  
874 ;  
875 ;  
876 ;  
877 ;  
878 ;  
879 ;  
880 ;  
881 ;  
882 ;  
883 ;  
884 ;  
885 ;  
886 ;  
887 ;  
888 ;  
889 ;  
890 ;  
891 ;  
892 ;  
893 ;  
894 ;  
895 ;  
896 ;  
897 ;  
898 ;  
899 ;  
900 ;  
901 ;  
902 ;  
903 ;  
904 ;  
905 ;  
906 ;  
907 ;  
908 ;  
909 ;  
910 ;  
911 ;  
912 ;  
913 ;  
914 ;  
915 ;  
916 ;  
917 ;  
918 ;  
919 ;  
920 ;  
921 ;  
922 ;  
923 ;  
924 ;  
925 ;  
926 ;  
927 ;  
928 ;  
929 ;  
930 ;  
931 ;  
932 ;  
933 ;  
934 ;  
935 ;  
936 ;  
937 ;  
938 ;  
939 ;  
940 ;  
941 ;  
942 ;  
943 ;  
944 ;  
945 ;  
946 ;  
947 ;  
948 ;  
949 ;  
950 ;  
951 ;  
952 ;  
953 ;  
954 ;  
955 ;  
956 ;  
957 ;  
958 ;  
959 ;  
960 ;  
961 ;  
962 ;  
963 ;  
964 ;  
965 ;  
966 ;  
967 ;  
968 ;  
969 ;  
970 ;  
971 ;  
972 ;  
973 ;  
974 ;  
975 ;  
976 ;  
977 ;  
978 ;  
979 ;  
980 ;  
981 ;  
982 ;  
983 ;  
984 ;  
985 ;  
986 ;  
987 ;  
988 ;  
989 ;  
990 ;  
991 ;  
992 ;  
993 ;  
994 ;  
995 ;  
996 ;  
997 ;  
998 ;  
999 ;  
1000 ;
```



003A' 26A2 1020
003E' 26A2 1010
0042' 26C6 06 1820 40
004B' C3

MOV BYTE PTR EB:GR_ORN,AL ; ...ALL TO ZERO
MOV BYTE PTR ES:GR_PLU,AL ; ...INITIALLY
MOV BYTE PTR EB:MSCOUT,CDSPEN ; TURN ON SCREEN
RET ; RETURN TO CALLER

; THIS IS THE INITIALIZATION LOGIC FOR THE CRT CONTROLLER.
; IT PERFORMS THE CRT PUP TEST AND LEAVES THE CRT INITIALIZED
; IF NO ERRORS OCCURRED.

NOTE: THE PROCEDURE CRTINI IS THE FULL INITIALIZATION LOGIC FOR
THE CRIC CONTROLLER AND IT MAY BE CALLED AT ANY TIME FOR
THIS PURPOSE.

-0049

SECT ROMCOD
ASSUME CS:ROMCOD, DS:CRIDAT
ASSUME ES:NOTHING

0049'

CRTTST PROC NEAR
; ;
MOV AX,CRIDAT ; SET UP
MOV DS,AX ; DS AS CRIDAT

0049' BB 0000'
004C' BE DB

; BEGIN TESTING OF THE CRIC READABLE REGISTERS

MOV SI,OFFSET TSTPAT ; SI = TABLE OF TEST PATTERNS
MOV CX,4 ; NUMBER OF TESTS
XOR DL,DL ; CLEAR ERROR ACCUMULATOR
MOV BYTE PTR MSCOUT,CRTOFF ; SCREEN OFF SO NO TRASH WILL APPEAR
; ;
MOV AL,CS:[SI] ; AL=TEST PATTERN
MOV BYTE PTR ATTLAT,AL ; WRITE ATT. LATCH
MOV BYTE PTR CRTADR,CURPSH ;
MOV AL,CS:BYTE PTR 1[SI] ; AL=TEST PATTERN
MOV BYTE PTR WRTRREG,AL ; WRITE CURSOR POSITION HIGH
MOV AL,CS:BYTE PTR 2[SI] ; AL=TEST PATTERN
MOV BYTE PTR WRTRREG,AL ; WRITE CURSOR POSITION LOW

004E' BE 0010"
0051' B9 0004
0054' 30 D2
0056' C6 06 1820 20
005B' 2EBA 04
005E' A2 1800
0061' C6 06 1810 0E
0066' 2EBA 44 01
006A' A2 1812
006D' C6 06 1810 0F
0072' 2EBA 44 02
0076' A2 1812

CHECK THE REGISTERS

MOV AL,CS:[SI]
CMP BYTE PTR ATTLAT,AL ; AL=TEST PATTERN
JE CROT50 ; Q: IS ATT. LATCH WHAT I WROTE
; ; Y: JUMP AROUND EXIT
; ; N: THEN ATTRIBUTE LATCH FAILURE
OR DL,ATTERR ; ATTRIBUTE LATCH FAILURE
; ; CONTINUE TESTING
CROT50:
MOV BYTE PTR CRTADR,CURPSH ;
MOV AL,CS:BYTE PTR 1[SI]
XOR AL,BYTE PTR RD_REG ; Q: IS CURSOR HIGH WHAT I WROTE

0079' 2EBA 04
007C' 38 06 1800
0080' 74 03
0082' 80 CA 07* \
0085' C6 06 1810 0E
0088' 2EBA 44 01
008E' 32 06 1813

```

0092' 24 3F
0094' 74 03
0096'
0097' 80 CA 09*
0099'
0099' C6 06 1810 OF
009E' 2EBA 44 02
00A2' 38 06 1813
00A6' 74 03
00AB'
00AB' 80 CA 09*
00AB' 46
00AC' E2 AD

00AE' BB 0000'
00B1' BE C0
00B3' BD 0100
00B6'
00B6' B9 07FF
00B9' 31 FF
00BB' B8 F7
00BD' B8 C5
00BF' D1 E8
00C1'
00C1' A2 1800
00C4' AA
00C5' D0 D0
00C7' E2 F8
00C9' B8 DD
00CB' B9 07FF
00CE' D1 E8
00D0'
00D0' 9F
00D1' BA 04
00D3' 38 C3
00D5' 74 03
00D7'
00D7' 80 CA 0A*
00DA'
00DA' 3A 1E 1800
00DE' 74 03
00E0'
00E0' 80 CA 06*
00E3'
00E3' 46

425 AND AL,00111111B
426 JE CROT81
427
428 CRH8AD: DL,CURERR
429
429 CROT81:
430 MOV BYTE PTR CRTADR,CURPBL
431 MOV AL,C8:BYTE PTR 2(B1)
432 CMP BYTE PTR RD_REQ,AL
433 JE CROT82
434
434 CRL8AD: DL,CURERR
435
435 CROT82:
436 INC SI
437 LOOP CROT8T
438
439
440
440 *****
441 BEGIN TESTING OF THE CRT SCREEN RAM AND SIMULTANEOUSLY
442 TESTING THE ATTRIBUTE MEMORY.
443
443
444
444 ASSUME ES:CRTDAT
445
445 CRMT8T: MOV AX,CRTDAT
446 MOV EB,AX
447
447 CRMT8O: MOV BP,1000000008
448
448
449
449 CX,MEMEND
450 XOR DI,DI
451 MOV SI,DI
452 MOV AX,BP
453 SHR AX,1
454
454 CRMT81:
455 MOV BYTE PTR ATTLAT,AL
456 STOSB
457 RCL AL,1
458 LOOP CRMT81
459 MOV BX,BP
460 MOV CX,MEMEND
461 SHR BX,1
462
462 CRMT82:
463 LAHF
464 MOV AL,(B1)
465 CMP BL,AL
466 JE CRMT83
467
467 RAM8AD: DL,CRAMER
468
468 CRMT83:
469
469 BL, BYTE PTR ATTLAT
470 CRMT84
471
471 ATMBAD: DL,ATMERR
472
472 CRMT84:
473
473 INC SI
474
474
475
475
476

(CURSOR HIGH IS ONLY 6 BITB)
Y: JUMP AROUND EXIT
N: THEN CURSOR HIGH FAILURE
CURSOR ADDRESS REQ. FAILURE
CONTINUE TESTING
AL-TEST PATTERN
Q: IS CURSOR LOW WHAT I WROTE
Y: JUMP AROUND EXIT
N: THEN CURSOR LOW FAILURE
AL-CURSOR ADDRESS REQ. FAILURE
CONTINUE TESTING
POINT TO THE NEXT PATTERN TO CHECK
AND TRY AGAIN

SET UP EB ALSO
AS CRTDAT
WALKING ONE
TEST SCREEN RAM
SET UP START OF CRT RAM.
COPY TO SI FOR LATER.
GET WALKING PATTERN TO AX.
SETUP THE CARRY BIT
STORE WALKING ONE IN ATT. MEMORY
...AND IN SCREEN MEMORY
WALK IT
GET INITIAL PATTERN
SIZE OF SCREEN RAM
SETUP THE CARRY BIT
SAVE FLAGS
GET PATTERN IN AL
Q: IS THIS OK ?
Y: JUMP AROUND JUMP
N: THEN RAM FAILURE
CRT RAM FAILURE
CONTINUE TESTING
Q: IS ATTRIBUTE OK ?
Y: JUMP AROUND JUMP
N: THEN ATTRIBUTE MEMORY FAILURE
ATTRIBUTE MEMORY FAILURE
CONTINUE TESTING
NEXT LOCATION
    
```

```
00E4' 9E          ; SAHF          ; GET FLAGS  
00E5' D0 D3       RCL          ; NEXT PATTERN TO CHECK  
00E7' E2 E7       LOOP         ; CONTINUE TILL FINISHED  
00E9' 81 F5 FFFF  XOR          ; SET UP FOR WALKING ZERO  
                                ; Q; COMPLEMENTED?  
00ED' 78 C7       JS           ; N: JUMP AND TEST AGAIN  
00EF' D1 ED       SHR          ; Q; DONE NINE LOOPS YET?  
00F1' 73 C3       JNB          ; N; JUMP AND CONTINUE TESTING  
                                ;  
00F3' 31 FF       VDTST: XOR DI,DI ; START OF SCREEN MEMORY  
00F5' B9 0800     MOV          ; SET UP CX FOR REP  
00F8' C6 06 1800 OF  MOV PTR ATT, HIGHAT ; ATT,AT = HIGH INT.  
00FD' 80 20     MOV          ; PUT A BLANK IN AL  
00FF' F2         REP          ; DI=MEMBER  
0100' AA         STOS        ; CLEAR SCREEN MEMORY  
0101' C6 06 1820 40 MOV PTR MSCOUT, CDSPEN ; ENABLE CRT FOR VIDEO TEST  
0106' B6 02     MOV          ; DH = TEST COUNT  
0108' 80 DB     MOV          ; AL=BLOCK CHARACTER  
010A' B3 F7     MOV          ; TEST PATTERN FOR A BLOCK CHAR.  
010C'           VDTSTO: DH = NUMBER OF CURRENT TEST  
010E' 31 FF     XOR          ; GET BEGIN OF MEMORY  
0110' B9 0090   MOV          ; CX=80 CHARACTERS TO WRITE  
0113' F2         REP          ;  
0115' AA         STOS        ; WRITE 80 BLOCK CHARS. TO SCREEN  
0118' 80 FFFF   MOV          ; CX=25us LOOP COUNT  
011B'           VDTST1: BP, OFFFFH ;  
011B' 4D         DEC          ;  
011C' 74 1E     JZ           ; TRAP IN CASE CRTVBL IS NOT WORKING  
011E' F6 06 1811 20 TEST PTR CRTSTA, CRTVBL ; Q: VERTICAL BLANK ??  
0123' 74 F6     JZ           ; N: WAIT UNTIL BLANK  
0125'           VDTST2: BP, OFFFFH ;  
0125' 4D         DEC          ;  
0126' 74 14     JZ           ; TRAP IN CASE CRTVBL IS NOT WORKING  
0128' F6 06 1811 20 TEST PTR CRTSTA, CRTVBL ; Q: VERTICAL NON-BLANK ??  
012D' 75 F6     JNZ          ; N: WAIT UNTIL NON-BLANK  
012F' E2 FE     LOOP         ; Y: START 25us TIMEOUT  
0131' 3B 1E 1000 CMP PTR MSCINP, BL ; Q: VIDEO LOOPBACK = TEST PATTERN ??  
0135' 74 08     JE           ; Y: JUMP AROUND BRANCH  
0137'           VIDBAD: DL, VIDERR ; N: THEN VIDEO FAILURE  
0137' 80 CA 0B* OR          ; VIDEO FAILURE  
013A' EB 03     JMP          ;  
013C'           VBLBAD: DL, VBLERR ; VERTICAL BLANKING INTERRUPT BAD  
013C' 80 CA 0C* OR          ; CONTINUE TESTING  
013F' 0B F6     OR           ; Q: LAST TEST ??  
013F' 0B F6     OR           ;
```

```
0141' 74 06
0143' 80 20
0145' 83 F0
0147' E8 C3

0149'
0149' 06
014A' 31 D8
014C' 8E C3
014E' 89 1500
0151' FA

0152' 1E
0153' 2E8E 1E C180
015B' 8F 0009"
015B' 1F
015C' 268B 1E 0008
0161' 26C7 06 0008 01D8"
0168' 84 FF
016A' C6 06 182u 80
016F' E2 FE
0171' C6 06 182D 40
0176' 80 FC 00
0179' 74 03
017B' 80 CA 08*
017E' 2689 1E 0008
0183' 07

0184' 1E
0185' 2E8E 1E C180
018A' 80 0E 0018" 08
018F' A0 0018"
0192' E6 03
0194' 1F
0195' FB

0196' EB 0047
0196' EB 0003"
0199' EB 0003
019C' EB 0003
019F' E9 0004"

529
530
531
532
533
534
535
536
537
538
539
540
541
542
543
544
545
546
547
548
549
550
551
552
553
554
555
556
557
558
559
560
561
562
563
564
565
566
567
568
569
570
571
572
573
574
575
576
577
578
579
580

CINTBT
AL,SPACE
BL,OF0H
SHORT VDTBTO

*****
THIS PERFORMS A TEST OF THE CRT INTERRUPT
*****

ASSUME DS:CRTDAT, ES:ABS0

PUSH EB
XOR BX,BX
MOV EB,BX
MOV CX,CINTIM
CLI

PUSH DS
MOV DS,WORD PTR CB:DBADDR+CSWRAP ; SET UP DS AS ROMDAT
CALL PRTRBT
POP DS
MOV BX,EB;WORD PTR (NMIINT*4) ;;; SAVE CURRENT NMI VECTOR
MOV EB:WORD PTR (NMIINT*4),OFFSECT CRTVCT ;;; CRT INT VECTOR
MOV AH,OFFH ;;; FLAG TO CHECK FOR INTERRUPT
MOV BYTE PTR MSCOUT,CINTEN ;;; ENABLE CRT INTERRUPT
MOV WORD PTR MSCOUT,CDBPEN ;;; WAIT FOR INTERRUPT
CMP AH,OOH ;;; DISABLE CRT INTERRUPTS JUST IN CASE
JE CINTS0 ;;; N: THEN CONTINUE
OR DL,CINTER ;;; G: DID IT OCCUR ?
CINTS0: MOV ES:WORD PTR (NMIINT*4),BX ;;; RESTORE NMI VECTOR
POP EB ;;; RESTORE EB
ASSUME DS:ROMDAT
PUSH DS
MOV DS,WORD PTR CB:DBADDR+CSWRAP ; SET UP DS AS ROMDAT
OR PRCD,M,PARIEN ;;; RE-ENABLE PARITY NMI INTERRUPT
MOV AL,PRCD_M
MOV PRCD,P,AL
POP DS
BTI

*****
TESTING COMPLETE!!! ALL TESTS PASSED WITH HONORS.
*****

TBTEND:
CALL CRTHLT
CALL DECLED
CALL CRTINI
JMP MSCTBT

*****
GO SEE IF ANY ERRORS OCCURRED
TESTS PASSED YEA!!!!
LEAVE CRT FULLY INITIALIZED
GO TO MISCELLANEOUS TEST ROUTINE
*****
```

```

581 ; CRTINI - THIS IS THE CRT FULL INITIALIZATION SUBROUTINE.
582 ;
583 ; INPUT - NONE
584 ;
585 ; OUTPUT - CRT INITIALIZED
586 ;
587 ; REGISTERS USED - ALL REGISTERS PRESERVED
588 ;
589 ; STACK USED -
590 ;
591 ; *****
592 ;
593 SECT ROMCOD
594 ASSUME CB:ROMCOD, DS:ROMDAT
595 ASSUME ES:CRTDAT
596 ;
597 CRTINI NEAR
598 ; CLEAR DIRECTION FLA0
599 ; SAVE DI
600 ; SAVE SI
601 ; SAVE OLD DS
602 ; SAVE OLD ES
603 ; SAVE BX
604 ; INICRT
605 ; DS,WORD PTR CB:DSADDR+CBMRAP ; SET UP DS AS ROMDAT
606 ; AX,AX
607 ; RETFLO, AH
608 ; CRTHLD, AH
609 ; CURTYP, 400BH
610 ; STATLN, AX
611 ;
612 ; CURPOS, AX
613 ; THSLIN, AX
614 ; DISBEG, AX
615 ; DISEND, 07CFH
616 ; CRTCNT, AX
617 ; JMP CRTRET
618 ;
619 ; *****
620 ; THIS IS THE CRT INTERRUPT LOGIC
621 ; *****
622 ;
623 CRTVCT: MOV BYTE PTR MSCOUT, CDSPEN ; DISABLE CRT INTERRUPTS
624 ; MOV AH, 00H ; CLEAR AH
625 ; IRET ; RETURN FROM INTERRUPT
626 ;
627 ; *****
628 ; THIS ROUTINE IS CALLED UPON COMPLETION OF ALL CRT TESTS.
629 ; IT CHECKS TO SEE IF ANY ERRORS HAVE OCCURRED UP TILL NOW.
630 ; IF NO ERRORS HAVE OCCURRED THEN IT RETURNS, HOWEVER IF
631 ; THERE WERE ANY ERRORS THEN THEY ARE OUTPUT TO THE PARALLEL
632 ;

```

-01A2

01A2' FC
 01A3' 57
 01A4' 56
 01A5' 1E
 01A6' 06
 01A7' 53
 01A8' EB FE55
 01AB' 2EBE 1E C180
 01B0' 31 C0
 01B2' 8B 26 0019"
 01B6' 8B 26 0013"
 01BA' C7 06 0015" 400B
 01C0' A3 001B"
 01C3'
 01C3' A3 0014"
 01C6' A3 001A"
 01C9' A3 0016"
 01CC' C7 06 0017" 07CF
 01D2' A3 0011"
 01D3' E9 0001"

01D8' C6 06 1B20 40
 01DD' B4 00
 01DF' CF

633 ; PRINTER PORT AND A JUMP TO DSPDIE IS PERFORMED WHERE THE
634 ; THE ERROR IS PUT TO THE SCREEN (ACADEMIC AT THIS POINT)
635 ; AND ALL TESTING HALTS!!!

636 ;
637 ; NOTE: SEE MODULE TBERR.SRC FOR POSSIBLE ERROR CODES.
638 ; *****
639 ;

01E0' 08 D2
01E0' 08 D2
01E2' 74 05
01E4' 80 0D#
01E6' E9 0002"
01E9' C3

CRTHLT: OR DL,DL ; 0: ANY ERRORS TO REPORT ??
JE HLTRET ; N: THEN RETURN
MOV AL,LEDDIO ; AL = CRT ERROR CODE = LED'B
JMP DBPDIE ; 00 DISPLAY ERROR AND DIE
HLTRET: RET ; 00 BACK - NO ERRORS
END

No errors detected

```
1 *****  
2 TITLE - DKBOOT - Disk boot routine  
3 ABSTRACT - This logic is responsible for reading the first sector  
4 (The BOOT sector) from the disk, checking its validity and then  
5 transferring control to BOOTLO (the first location of the data  
6 just read).  
7  
8 *****  
9 NAME DKBOOT - Disk boot routine  
10 *****  
11 PUBLIC DEFINITIONS  
12 *****  
13  
14 PUBLIC DKBOOT  
15 PUBLIC PROMPT  
16  
17 *****  
18 EXTERNAL REFERENCES  
19 *****  
20  
21 EXTRN BOOTSZ:ABS ; Boot sector size (SYSORG)  
22 EXTRN DBCERR:ABS ; Bad CRC on boot sector error code (ROMERR)  
23 EXTRN DCRERR:ABS ; CRC error code (ROMERR)  
24 EXTRN DMERR:ABS ; Disk format error code (ROMERR)  
25 EXTRN DNIERR:ABS ; 'No drives installed' error code (ROMERR)  
26 EXTRN DNRERR:ABS ; 'Disk not ready' error code (ROMERR)  
27 EXTRN DNSERR:ABS ; 'Not a TI system disk' error code (ROMERR)  
28 EXTRN DSKERR:ABS ; Seek error code (ROMERR)  
29 EXTRN DSNERR:ABS ; Sector-not-found error code (ROMERR)  
30 EXTRN DUNERR:ABS ; 'Disk error' (unknown) error code (ROMERR)  
31 EXTRN DDMERR:ABS ; DRQ error (ROMERR)  
32  
33 EXTRN DSPERR:NEAR ; System error display routine (OUTPUT)  
34 EXTRN CRTOUT:NEAR ; System error display routine (CRTDSR)  
35 EXTRN BOOTLO:FAR ; First instr of boot sector (SYSORG)  
36 EXTRN FLUBH:NEAR ; Keyboard buffer flush routine (KEYDSR)  
37 EXTRN KEYIN:NEAR ; Keyboard character input routine (KEYDSR)  
38 EXTRN LBEEP:NEAR ; Long (error) beep routine (BELDSR)  
39 EXTRN MSG:NEAR ; CRT string output routine (OUTPUT)  
40 EXTRN ROMTST:NEAR ; CRC checking routine (PUPTST)  
41 EXTRN SBEEP:NEAR ; Short beep routine (BELDSR)  
42  
43 SECT ABSO  
44 ASSUME CS:ABSO  
45 EXTRN BOOTMV:BYTE ; Location of boot sector in memory (SYSORG)  
46  
47 SECT ROMDAT  
48 EXTRN SYSCON:WORD ; System Configuration word (ROMDAT)  
49  
50 *****  
51 LOCAL CONSTANTS  
52 *****
```

=0000

=0000




```
0000' 0D 0A 50 6C 65
0006' 61 73 65 20 69 6E
000C' 73 65 72 74 20 73
0012' 79 73 74 65 6D 20
0018' 64 69 73 6B 20 61
001E' 6E 64
0020' 0D 0A 53 74 72 69
0026' 68 65 20 61 6E 79
002C' 20 6B 65 79 20 77
0032' 68 65 6E 20 72 65
0038' 61 64 79 20 2E 2E
003E' 2E 00
0040' 20 6F 6E 20 44 72
0046' 69 76 65 20 00

004B' 40 0B*
004D' 10 03*
004F' 08 0B*
0051' 04 09*
0053' 02 04*
0055' 07* 07*
0057' 20 0A*
```

```
53 CR EQU ODH
54 LF EQU OAH
55
56
57 -----
58 INCLUDE PEG: DSKERR.EQU
59 INCLUDE PEG: DSKOPS.EQU
60 INCLUDE PEG: BYSCCELL.EQU
61 INCLUDE PEG: SYSCON.EQU
62 INCLUDE PEG: CRTOP.EQU
63 INCLUDE PEG: VECTOR.EQU
64 -----
302 *****
303 CODE SEGMENT DEFINITION
304 *****
305
306 SECT ROMCOD
307 ASSUME CS:ROMCOD
308
309 *****
310 LOCAL DATA AREA
311 *****
312 *****
313 INMS0 DB CR,LF,LF,'Please insert system disk and'

314 PROMPT DB CR,LF,'Strike any key when ready ...',0

315 DRVMS0 DB ' on Drive ',0

316
317 ; Error code mapping table to take Disk DSR errors to System error codes.
318 ; Each entry consists of 2 bytes - first byte is error code from disk
319 ; DSR, second byte is the boot error code to be displayed on the
320 ; CRT as '** System Error ** - xxxx on Drive x'.
321 ; Entries marked with '*' are either hardware failures or errors
322 ; which will never happen since I control the interface to the DSR.
323
324 ERRTAB DB DKSEEK,DSKERR ; Seek failed - track not found
325 DB DKCRCE,DCRERR ; CRC error on read
326 DB DKDMAE,DDMERR ; Data request error - controller failure
327 DB DKRNFE,DSNERR ; Sector not found error
328 DB DKFMTI,DFMERR ; Timeout - no data error - bad disk format
329 DB DNSERR,DNSERR ; Not a TI disk error.
330 DB DKFAIL,DUNERR ; * Controller hardware failure
```

```

331 ; DB DKNRDY,DNRERR ; Controller timeout - disk not ready
332 ; DB DKCMDE,DUNERR ; * Command error - bad command passed
333 ; DB DKWPRT,DUNERR ; * Write protect error
334 ; DB DKVFEY,DUNERR ; * Data verification error
335 ; DB DKBDUN,DUNERR ; * I/O transfer crosses 64k boundary
336 ;
337 ERRSZ EQU ($-ERRTAB)/2 ; Table size
338 ; *****
339 ; MODULE ENTRY POINT
340 ; *****
341 ;
342 ; DKBOOT - This routine gets control after powerup initialization in order
343 ; to 'boot' the disk system. It reads the first sector on the default
344 ; disk into memory, and transfers control it.
345 ;
346 ; INPUT: (none)
347 ; OUTPUT: BL = Drive to boot from
348 ; (transfers control to the code loaded from the boot sector)
349 ;
350 ; USED:
351 ; STACK:
352 ; ASSUME CS:ROMCOD, DS:ROMDAT, EB:ABBO
353 ;
354 ; Check if any drives are installed. If not, report the system error
355 ; and wait forever.
356 ;
357 ; DKBOOT PROC NEAR
358 ; XOR BX,BX
359 ; MOV EB,BX ; Set up EB to ABBO
360 ; MOV DB,WORD PTR CB:DSADDR+CBMRAP ; Set up my DS
361 ; MOV AL,BYTE PTR SYBCON ; Look at floppy drive status
362 ; AND AL,DROMBK+DR1MBK+DR2MBK+DR3MBK
363 ; JNZ DKBO ; G: Are any drives installed?
364 ; MOV AL,DNIERR ; Y: Continue boot process.
365 ; CALL DSPERR ; N: 'No drives' error code
366 ; ; Display the error
367 ;
368 ; CALL KEYIN ; Wait for any key
369 ; JMP WAITLP ; Loop forever (til they hit reset)
370 ;
371 ; Try to boot from an installed drive. If successful, do not return.
372 ; If NOT successful, try the next installed drive.
373 ; If no drive is successful, Prompt user for a disk installation.
374 ;
375 ; DKBO: XOR BL,BL ; Set initial boot drive to 00
376 ; MOV AL,BYTE PTR SYBCON ; Get configuration byte.
377 ; SHR AL,1 ; G: Is drive installed ?
378 ; JNB DKB2 ; N: Try another
379 ; PUSH AX ; Y: Save state
380 ; CALL BOOTDR ; Boot this drive
381 ; POP BX ; restore state
382 ; POP AX

```

```

0083' FE C3
0085' 80 FB 04
0088' 75 EE
008A' 80 06*
008C' EB 000C"
008F' BE 0000"
0092' EB 0012"
0095' EB 000F"
0098' EB 0010"
009B' EB D6

009D'
009D' 26C6 06 0000" 00
00A3' 06
00A4' 93
00A5' B0 01
00A7' B5 00
00A9' B1 01
00AB' BA D3
00AD' B6 00
00AF' B4 02
00B1' BB 0015"
00B4' CD 4D
00B6' 9B
00B7' 07
00B8' 72 2C

00BA' 53
00BB' BB 0015"
00BE' BD 0001*
00C1' EB 0013"
00C4' 5B
00C5' 74 0A
00C7' 2683 3E 0000" 00

00CD' B0 02*
00CF' 75 2B
00D1'
00D1' 2681 3E 0000" 6974

00D8' B0 07*
00DA' 75 20
00DC' EB 0014"
00DF' 53
00E0' 9A 000E" 0000"
00E5' 9B

383
384
385
386
387
388
389
390
391
392
393
394
395
396
397
398
399
400
401
402
403
404
405
406
407
408
409
410
411
412
413
414
415
416
417
418
419
420
421
422
423
424
425
426
427
428
429
430
431
432
433
434

DKB2:
INC
CMP
JNZ
MOV
CALL
MOV
CALL
CALL
JMP

; Try next drive
; G: Tried all drives?
; N: Jump and continue
; Not able to boot error.
; Beep the error bell
; Point to 'Insert disk' message
; Empty the keyboard buffer
; Wait for any key
; Try boot again

; Do a complete boot action from this drive.

BOOTDR:
MOV ES:BOOTMV+BOOTID.00
PUSH ES
PUSH BX
MOV AL,1
MOV CH,0
MOV CL,1
MOV DL,BL
MOV DH,00
MOV AH,DKREAD
MOV BX,OFFSET BOOTMV
INT DSKINT
POP BX
POP ES
JMP CHKERR

; Insure ID isn't there from last time
; Hang on to transfer segment
; Save boot drive
; One sector to load
; Starting at track 0
; ...sector 1
; Boot from available drive
; Head 0
; Sector read function
; Transfer offset (segment already set)
; Get the sector
; Restore boot drive
; Quit if error

; (ES already set to ABS0)
; Save the boot drive
; Offset of boot sector
; Size of sector
; G: Does the CRC calculate OK ?
; Save the boot drive
; Y: Go on and check the ID
; (this allows pre-CRC disks)
; ('Bad CRC on boot sector', error code)
; N: Blow it off with the error
; Y: Continue

; G: Is boot sector ID = 'ti' ?
; 'Not a TI system disk', error code
; N: error
; Beep the bell before going to boot
; Save boot drive
; *** EXIT *** to boot routine
; Should return to here only if ERROR

CHKID:
CMP WORD PTR ES:BOOTMV+BOOTID,'it'
MOV AL,DNSERR
JNE CHKERX
CALL SBEEP
PUSH BX
CALL BOOTLO
POP BX
JMP SHORT CHKERR

```

```

435 ; This routine prints the error message for a system error on boot
436 ;
437 ;
438 ;
439 ;
440 ;
441 ;
442 ;
443 ;
444 ;
445 ;
446 ;
447 ;
448 ;
449 ;
450 ;
451 ;
452 ;
453 ;
454 ;
455 ;
456 ;
457 ;
458 ;
459 ;
460 ;
    
```

```

00E6'
00E6' 80 FC 80
00E9' 74 24
00EB' BE 0049"
00EE' B9 0007
00F1' 46
00F2' 46
00F3' 2E3A 24
00F6' E0 F9
00F8' 46
00F9' 2EBA 04
00FC' 53
00FD' EB 000C"
0100' BE 0040"
0103' EB 0012"
0106' 58
0107' BB 0E41
010A' 00 DB
010C' EB 000D"
010F' C3
    
```

No errors detected

```

435 ; This routine prints the error message for a system error on boot
436 ;
437 ;
438 ;
439 ;
440 ;
441 ;
442 ;
443 ;
444 ;
445 ;
446 ;
447 ;
448 ;
449 ;
450 ;
451 ;
452 ;
453 ;
454 ;
455 ;
456 ;
457 ;
458 ;
459 ;
460 ;
    
```

```

CHKERR:
    CMP AH,DKNRDY
    JZ CE9
    MOV BI,OFFBET ERRTAB-2
    MOV CX,OFFBET ERRTSZ
    INC SI
    INC SI
    CMP AH,BYTE PTR CB:[BI]
    LOOPNZ CE1
    INC SI
    MOV AL,CB:[BI]
    MOV BX
    PUSH DSPERR
    CALL BI,OFFBET DRVMSG
    MOV MS0
    CALL POP BX
    MOV AX,CRTMTY*100H+'A'
    ADD AL,BL
    CALL CRTOUT
    RET
    CE9:
    END
    
```

```

; Q: Was the error 'Disk not ready' ?
; Y: Don't report as an error
; and let them try again.
; Point to error translation table
; BI: Size of table.
; Point to next entry
; Q: Is this the error ?
; N: Point to next entry
; Y: Point to translation
; Get new error code
; Save drive number
; Display the error
; Drive message
; Print it
; Get drive number
; Format number
; Print it.
    
```

```
1 *****  
2 ; TITLE - DRV1ST - Disk drive test routine  
3 ; ABSTRACT - This module is responsible for determining the number of  
4 ; disk drives in the system.  
5 ; *****  
6 NAME DRV1ST  
7 SUBTTL  
8 *****  
9 PUBLIC DEFINITIONS  
10 *****  
11  
12 PUBLIC DRV1ST  
13  
14 *****  
15 ; EXTERNAL REFERENCES  
16 ; *****  
17 ;  
18 EXTRN DELAY:NEAR ; 1 millisecond delay routine (ROMUTL)  
19 ;  
20 SECT ROMDAT  
21 EXTRN DSKS_M:BYTE ; RAM copy of disk select port (ROMDAT)  
22 EXTRN SYSCON:WORD ; System configuration word (ROMDAT)  
23 ; *****  
24 ; LOCAL CONSTANTS  
25 ; *****  
26 ;  
27 BSYNSK EQU 00000001B ; Busy bit mask in FDC status reg  
28 TROMSK EQU 00000100B ; Track 0 bit mask in FDC status reg  
29 NUMSTP EQU 86 ; Number of steps to be performed during recal  
30 ;  
31 BTDCMD EQU 61H ; FDC STEP OUT command (12 msec, no verify)  
32 BKNCMD EQU 10H ; FDC seek command (6 msec, no verify)  
33 ;  
34 ; INCLUDE PEG:PORTADDR.EQU  
35 ; INCLUDE PEG:LATCHES.EQU  
36 ; INCLUDE PEG:FLOPPY.EQU  
37 ; INCLUDE PEG:SYSCON.EQU  
38 ; *****  
188 ; CODE SEGMENT DEFINITION  
189 ; *****  
190 ;  
191 ;  
192 SECT ROMCOD  
193 ASSUME CS:ROMCOD  
194 ;  
195 ; *****  
196 ; MODULE ENTRY POINT  
197 ; *****  
198 ;  
199 ; DRV1ST - This routine determines the number of operative floppy drives  
200 ; in the system. This is done by attempting a restore operation on  
201 ; the drives and watching for TRACK 00 indication.
```



```

0032' F6 D2      254      NOT DL      ; restore sense of select
0034' D0 CA      255      RDR DL,1    ; Q: All drives checked?
0036' 72 EB      256      JB CD4      ; N: Try next drive
0038' FE CC      257      DEC AH      ; Q: Finished seeking?
003A' 75 DC      258      JNZ CD2      ; N: Continue
003C' 80 E6 OF      259      FINISHED REBTORINO.
003F' 80 26 0003" F0 260      AND DH,DRVMSK ; Mask out the diskdrive bits
0044' 08 36 0003" 261      AND BYTE PTR BYECON,NOT DRVMSK ; OR in the installed drive bits
0048' 80 0F      262      OR BYTE PTR BYECON,DH
004A' A2 0002" 263      MOV AL,OFH   ; Turn off everything
004D' E6 04      264      MOV DBKB_M,AL ; After all this messin' around...
004F' C3      265      OUT DSKB_P,AL ; Make sure to restore the select latch
0050' B0 D0      266      RET          ; *** RETURN ***
0052' E6 20      267      Clear the disk controller and set status into AL.
0054' B9 000A      268      CLRFDG      ;
0057' E2 FE      269      PROC NEAR   ; Clear the controller & return status
0059' E4 20      270      MOV AL,FORCED ; ... and force Type I status
005B' C3      271      OUT FDCMD,AL ; 10 * 3.4 usec/loop = 34 usec
005C' E6 20      272      MOV CX,10    ; Delay for FDC status to become valid
005E' B9 000A      273      LOOP $      ; Look at status reg
005F' E4 20      274      IN AL,FDCSTA ; *** RETURN ***
0060' C3      275      RET
0061' E2 FE      276      EXECUTE A FLOPPY DISK COMMAND
0063' AB 01      277      DO_CMD      ; Do it
0067' 75 F5      278      PROC NEAR   ; 10 * 3.4 usec/loop = 34 usec
0069' C3      279      OUT FDCMD,AL ; Delay for FDC status to become valid
006A' B9 000A      280      MOV CX,10    ; Look at floppy status
006B' E2 FE      281      IN AL,FDCSTA ; Q: Is controller still busy ?
006C' AB 01      282      JNZ CD1      ; Y: Spin till not busy
006D' 75 F5      283      RET          ; N: Return to user
006E' C3      284      END
006F' E6 20      285      PROC NEAR   ; Do it
0070' B9 000A      286      MOV CX,10    ; 10 * 3.4 usec/loop = 34 usec
0071' E2 FE      287      LOOP $      ; Delay for FDC status to become valid
0072' AB 01      288      IN AL,FDCSTA ; Look at floppy status
0073' 75 F5      289      TEST AL,BBYMSK ; Q: Is controller still busy ?
0074' C3      290      JNZ CD1      ; Y: Spin till not busy
0075' C3      291      RET          ; N: Return to user
0076' C3      292      END
0077' C3      293      END
0078' C3      294      END
0079' C3      295      END

```

No errors detected


```
1 ; *****
2 ; TITLE - DSK_ID *****
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE
4 ; ABSTRACT - Interface to the disk driver in ROM. Includes the routine DFINISH
5 ; called by the interrupt-driven, machine independent code to indi-
6 ; cate that interrupt level processing has completed.
7 ; Uses the IBM PC conventions.
8 ; INPUTS - AH = Function code
9 ;
10 ; IBM compatible function codes:
11 ; 00 = Reset diskette system
12 ; 01 = Return status code for last operation in AL
13 ; 02 = Read the specified sectors into memory
14 ; 03 = Write the specified sectors from memory
15 ; 04 = Verify the specified sectors CRC's
16 ; 05 = Null operation (format track on IBM PC)
17 ;
18 ; TI private function codes:
19 ; 06 = Verify data in specified sectors
20 ; 07 = Return retry status for last operation in AL
21 ; 08 = Set standard DIT for unit
22 ; 09 = Set DIT address for unit
23 ; 10 = Return DIT address for unit
24 ; 11 = Turn off all Floppy drive motors
25 ;
26 ; NOTE: DIT = Disk Interface Table
27 ;
28 ; Register assignments for Read/Write/Verify (crc & data):
29 ; AL = Number of sectors
30 ; CH = Cylinder ("track") number
31 ; CL = Sector number
32 ; DL = Drive number
33 ; DH = Track ("head") number
34 ; ES:BX = buffer address (except for Verify crc)
35 ;
36 ; Note the two equivalent nomenclatures for disk position:
37 ; Cylinder, track, & sector (==) "track", "head", & "sector"
38 ; This code uses cylinder, track, & sector
39 ;
40 ; Register assignments for Set standard DIT for unit:
41 ; AL = Disk DIT type:
42 ; 0 = Single sided, single track density
43 ; 1 = Double sided, single track density
44 ; 2 = Single sided, double track density
45 ; 3 = Double sided, double track density
46 ; DL = PEGASUS unit (drive) #
47 ;
48 ; Register assignments for Set DIT address for unit:
49 ; DL = PEGASUS unit (drive) #
50 ; EB:DX = address of DIT
51 ;
52 ; OUTPUTS: ;
```


53 ;
 54 ;
 55 ; AL = status return for functions 01 & 07
 56 ; AH = status return for functions 00, 02 - 06
 57 ; AL = number of unprocessed sectors for any I/O function
 58 ; BX = address of WORD with bad data for function 06
 59 ; ES = same as input ES (except for function 10)
 60 ; Carry flag is set (=1) to indicate an error condition

61 ; REGISTERS USED -
 62 ;
 63 ; STACK USED -
 64 ;

65 ; *****
 66 ; NAME DSK_IO *****
 67 ; SUBTTL ROM disk driver front end (uses IBM PC interface)
 68 ; EQU OFFFH
 69 ; *****
 70 ; SUBTTL ROM disk driver front end: STRUCTURE OF THE DISK DRIVER *****
 71 ; *****
 72 ; Structure of the disk driver:
 73 ;

74 ;
 75 ;
 76 ; I DSK_IO I
 77 ; -----
 78 ; I Y
 79 ;

Implements the standard (IBM PC) interface to all disk-type devices supported under this driver.

80 ;
 81 ;
 82 ; I DSKISR I
 83 ; -----
 84 ; I I
 85 ; I I
 86 ; I I
 87 ; Y

Implements a high-level generic disk interface which all disk-type devices are made to look like using software. Includes the basic disk functions and the basic interrupt structure for processing disk I/O.

88 ;
 89 ;
 90 ; I Generic disk I
 91 ; I operations I
 92 ; -----
 93 ;
 94 ;
 95 ;
 96 ;
 97 ;

Implements the generic disk operations for a specific disk-type device. Performs the device-specific operations necessary to emulate the high-level generic disk operations. This includes executing device-specific functions, programming of hardware interfaces and processing of interrupts not included in the basic interrupt structure.

98 ;
 99 ; *****
 100 ; SUBTTL ROM disk driver front end: THE DIT DATA STRUCTURE *****
 101 ; *****
 102 ; The Disk Interface Table structure is used to interface device-specific code
 103 ; to the generalized disk driver code.
 104 ;
 105 ; DIT's contain read-only data exclusively and may be ROMmed. If drive speci-

-FFFF

fic code requires RAM space, it may use the small workspace provided in the ROM BIOS' RAM data area or have its own RAM space allocated somewhere.

The structure of a DIT is as follows:

```

    (----16 bits----)
    00H |   DITDIR   | Long vector to Disk Interface Routine
    02H |           |
    04H |   DITBEC   | Sector size in bytes
    06H | IDITTRK IDITCYL | Track size in sectors; Cylinder size in tracks
    08H | IDITDSK IDITERR | Disk size in cylinders; Error retry limit
    0AH |           |
    
```

all other fields are dependent on the requirements of the code for the specific device

The DIT structure for all of the floppy drives is:

```

    (----16 bits----)
    |   FLPDIR   | Floppy Disk Interface Routine
    |           |
    |   DITBEC   | Sector size in bytes
    | IDITTRK IDITCYL | Track size in sectors; Cylinder size in tracks
    | IDITDSK IDITERR | Disk size in cylinders; Error retry limit
    | xPRCMP    | Threshold track # for changing write pre-comp
    
```

PUBLIC DEFINITIONS

```

    SECT ROMCOD
    PUBLIC DCALL
    PUBLIC DFINISH
    PUBLIC DSK_ID
    PUBLIC DSKINI
    PUBLIC INVALID
    PUBLIC SOFINI
    ; Call Generic Disk Operation
    ; Called at end of interrupt processing
    ; Main entry point for general disk I/O
    ; Initialization entry pt. (if needed)
    ; Invalid position value
    ; Software interrupt into interrupt code
    
```

```

158 PUBLIC TIMOUT ; Entry point for disk timeout event
159 ; *****
160 ; EXTERNAL REFERENCES *****
161 ; *****
162 SECT ROMCOD ; *****
163 EXTRN FLPDIR: FAR ; Disk Interface Routine for floppy disk
164 EXTRN MOTOFF: NEAR ; Kill motor and timeout
165 ; *****
166 SECT ROMDAT ; *****
167 EXTRN D_DBN: BYTE ; Beginning of data area
168 EXTRN D_DIT: WORD ; Table of drive DIT's
169 EXTRN D_DEND: BYTE ; End of data area
170 EXTRN D_POS: BYTE ; Table of drive current positions
171 EXTRN D_REQ: BYTE ; Disk driver Request Information Block
172 EXTRN D_SOFT: BYTE ; Software interrupt flag for DSKISR
173 EXTRN D_STAT: BYTE ; Disk driver current state
174 EXTRN D_WAIT: BYTE ; Waiting for interrupt processing flag
175 EXTRN NUMDRV: ABS ; Max number of drives in the system
176 EXTRN SYSCON: WORD ; System configuration word
177 ; *****
178 ; LOCAL CONSTANTS *****
179 ; *****
180 ; NOTE: These values have been assigned equates for mnemonic purposes. THIS
181 ; ABSOLUTELY, POSITIVELY #DOES NOT# mean that the values can be changed!!
182 ; DON'T CHANGE THE VALUES, the code WILL NOT work if you do!!!!!!
183 ; *****
184 BUSY EQU 1 ; Busy value for wait flag
185 INVALID EQU -1 ; "Invalid" pointer value
186 NUMDIT EQU 4 ; Number of standard DIT's
187 NUMFLP EQU 4 ; Number of floppy drives
188 READY EQU 0 ; Ready value for wait flag
189 ; *****
190 ; DISK DRIVER STATE DEFINITIONS *****
191 ; *****
192 ; INCLUDE PEG: DSKSTA.EQU *****
193 ; *****
205 ; *****
206 ; DISK INTERFACE TABLE DEFINITIONS *****
207 ; *****
208 ; INCLUDE PEG: DSKDIT.EQU *****
209 ; *****
224 ; *****
225 ; DISK OPCODES (IBM COMPATIBLE ROM BIOS) *****
226 ; *****
227 ; INCLUDE PEG: DSKOPS.EQU *****
228 ; *****
251 ; *****
252 ; DIT DEFINITIONS *****
253 D5CYL EQU 2 ; Double sided cylinder size (tracks)
254 S5CYL EQU 1 ; Single sided cylinder size (tracks)
255 DTDBK EQU 80 ; Double track density disk size (cyls)

```

-0000

-0000

-0001
 -FFFF
 -0004
 -0004
 -0000

-0002
 -0001
 -0050

```

-002B      256      BTDBK      EQU      40
-002B      257      DTPRCMP    EQU      DTDBK/2
-0002      258      FLPEERR    EQU      2
-0014      259      BTPRCMP    EQU      STDBK/2
-0200      260      XXSEC      EQU      512
-0008      261      XXTRK      EQU      8
          262      ; *****
          263      ; ERROR CODE DEFINITIONS
          264      ; *****
          265      ; INCLUDE PEO: DSKERR.EQU
          266

          304      ; *****
          305      ; GENERIC DISK FUNCTION CODEB (INTERNAL USE ONLY)
          306      ; *****
          307      ; INCLUDE PEO: DSKREQ.EQU
          308
          319      ; *****
          320      ; PEGASUS INTERRUPT VECTORS
          321      ; *****
          322      ; INCLUDE PEO: VECTOR.EQU
          323

          417      ; *****
          418      ; REQUEST INFORMATION BLOCK STRUCTURE DEFINITION
          419      ; *****
          420      ; INCLUDE PEO: DSKRIB.EQU
          421
          437
          438      SUBTTL      ROM disk driver front end: DSK_ID (ENTRY POINT)
          439      ; *****
          440      ; MODULE ENTRY POINT
          441      ; *****
          442      SECT      ROMCOD
          443      ASSUME      CB:ROMCOD, DB:ROMDAT, EB:NOTHING
          444      ; *****
          445      DSK_ID    PROC      FAR
          446      BTI
          447
          448      PUSH      DS
          449      PUSH      CX
          450      PUSH      SI
          451      PUSH      DI
          452      PUSH      BP
          453      PUSH      DX
          454      MOV        DB,word ptr CB:DBADDR+CBWRAP ; Set local DS
          455      ; Store request info (defined or not)
          456      MOV        word ptr D_REG+BUF+2,EB ; Buffer's segment base
          457      MOV        word ptr D_REG+BUF+0,BX ; Buffer's segment offset
          458      MOV        D_REG+SECKNT,AL ; Sector count
          459      MOV        D_REG+CYL,CH ; Cylinder # for request
          460      MOV        D_REG+SEC,CL ; Sector # for request
          461      MOV        D_REG+TRK,DH ; Track # for request
          462
          ; We entered via a software interrupt,
          ; so enable the interrupts
          ; Save registers used by DSKID
    
```

```

0023' BA C4      MOV     AL,AH
0025' 3C 0C      CMP     AL,DKBADC
0027' 73 25      JAE     CMDERR
0029' 98        CBW
002B' D1 E0      SHL     AX,1
002D' 8B F0      MOV     SI,AX
002E' EB 012C    CALL    CHGDRV
0031' 2EFF A4 0036" JMP     CS:CMDTBL[SI]
0036' 00A3"      CMDTBL DW     OPRESET
0038' 00AB"      DW     OPSTAT
003A' 00C9"      DW     OPREAD
003C' 00CD"      DW     OPWRITE
003E' 00D1"      DW     OPVERF
0040' 006D"      DW     OKEXIT
0042' 00D3"      DW     OPVRFY
0044' 0080"      DW     OPSSTA
0046' 0071"      DW     OPFSET
0048' 008B"      DW     OPXSET
004A' 0085"      DW     OPRDIT
004C' 00AB"      DW     OPKILM
004E' B0 01      SUBTTL ROM disk driver front end: EXIT (EXIT FROM BIOS CALL)
0050' A2 0000"    CMDERR: MOV     AL,DKCME
0053' BA 26 0000" short ptr EXITE
0057' 80 FC 00    EXITE:  D_REG+STAT,AL
005A' 74 01      EXIT:   AH,D_REG+STAT
005C' F9        CMP     AH,DKNORM
005D' A0 0000"    JE     RESRET
0060' C4 1E 0000"    STC
0064' 5A        RETRET: MOV     AL,D_REG+BECKNT
0065' 5D        LES     BX,dword ptr D_REG+BUF
0066' 5F        POPRET: POP     DX
0067' 5E        POP     BP
0068' 59        POP     DI
0069' 1F        POP     SI
006A' CA 0002    POP     CX
006D' 80 00      POP     DS
006F' EB DF      RET     2
0070' 80 00      MOV     AL,DKNORM
0071' EB DF      JMP     short ptr EXITE
0072' 80 00      SUBTTL ROM disk driver front end: NON-I/O FUNCTIONS
0073' 80 00      OPFSET - SET STANDARD (FLOPPY) DIT FOR DRIVE

```

```

; AL := Command code
; G: Is this a good command?
; N: Return bad command error
; Y: Convert to word value
; Put in index register for jump
; Call CHODRV to change drive
; Also sets error flags for use by
; the function routines
; Jump to return for command
; Vector for reset
; Vector for return status
; Vector for read sectors
; Vector for write
; Vector for verify CRC
; Vector for format (null operation)
; Vector for return retry status
; Vector for set floppy DIT for drive
; Vector for set DIT address for drive
; Vector for return DIT addr for unit
; Vector for turning off Floppy motors
; Return with command error
; EXITE immediately follows
; Slow set status & return to caller
; Set status & return to caller
; Fast set status & return to caller
; If no error, go restore & return
; Otherwise, set carry ist
; Set appropriate return values
; CL := count of unprocessed sectors
; ES:BX := Updated pointer in buffer
; Restore the saved registers
; Finally, restore calling DS
; And return (from software interrupt)
; Normal exit

```

```

515 , INPUT: * DL = Drive number
516 , * [D_REQ+BECKNT] (byte) = DIT index
517 , * ZF = NZ if drive number is bad
518 ,
519 OPFBET EQU *
520 JCHDER: CMDERR ; Set DIT addr to standard floppy DIT
521 MOV AL,D_REQ+BECKNT ; Return command error if drive # bad
522 CMP AL,NUMDIT-1 ; AL := DIT number
523 JA CMDERR ; G: Is it a valid standard DIT index?
524 ; N: Return command error
525 ; Y: Convert index to word value
526 ; BX,AX ; Convert index to word index
527 MOV AX,CS:DITBL[BX] ; BX := index into DIT table
528 PUSH CB ; EB:AX := @ desired DIT
529 POP EB
530 JMP short ptr SETDIT ; Go set DIT address
531 *****
532 OPXBET - SET DIT ADDRESS FOR DRIVE *****
533 , INPUT: * DL = Drive number
534 , * ZF = NZ if drive number is bad
535 ,
536 EQU *
537 JNZ CMDERR ; Set DIT addr to supplied DIT address
538 LEB AX,dword ptr D_REQ+BUF ; Return command error if drive # bad
539 JMP short ptr SETDIT ; And go set DIT address
540 *****
541 SETDIT - COMMON CODE FOR OPFBET & OPXBET *****
542 , INPUT: * DL = Drive number
543 ,
544 SETDIT: XOR DH,DH ; Convert drive number to word value
545 MOV BX,DX ; BX := Word value drive number
546 SHL BX,1 ; Convert to double word index
547 SHL BX,1
548 MOV D_DIT+0[BX],AX ; Store DIT address for drive
549 MOV D_DIT+2[BX],ES ; Now, go reset the data structures
550 CALL CHGDIO
551 JMP short ptr OKEXIT
552 *****
553 OPFBET - RESET DISK SYSTEM *****
554 ,
555 OPFBET: CALL INVPOB ; Invalidate all disk positions
556 JMP short ptr OKEXIT
557 *****
558 OPKILM - Kill the motor timeout for the floppies *****
559 ,
560 OPKILM: CALL MOTOFF
561 *****
562 SUBTTL ROM disk driver front end: INFORMATION RETURNS *****
563 *****
564 OPSTAT - RETURN STATUS CODE *****
565 ,
566 OPSTAT: MOV AL,D_REQ+STAT ; Return last status code

```



```
00AE' EB 12
567 JMP short ptr QEXIT
568 ; *****
569 ; OPSSTA - RETURN RETRY STATUS *****
570 ;
571 OPSSTA: MOV AL, D_REG+RSTAT ; Get retry status if any
572 JMP short ptr QEXIT
573 ; *****
574 ; OPRDIT - RETURN DIT ADDRESS FOR UNIT *****
575 ; INPUT: # [D_REG+DIT] (dword) = seg:offset of current drive's DIT
576 ; # ZF = NZ if the drive number is bad
577 ;
578 OPRDIT: JNZ CMDERR ; Return command error if there's error
579 XCHG AX, DX ; AL := "New" current drive number
580 CBW ; Convert to word value
581 SHL AX, 1 ; DI := Index to DIT table
582 SHL AX, 1 ;
583 XCHG AX, BX ; DI := New drive index
584 LES BX, dword ptr D_DIT[BX] ; ES:AX := @ drive's DIT
585 MOV AH, DKNORM ; AH := Normal status
586 CLC ; Set carry to indicate no error
587 JMP short ptr POPRET ; And return
588 ; *****
589 ;
590 SUBTTL ROM disk driver front end: REJUMP TO EXIT *****
591 ; *****
592 J_EXIT: JMP short ptr EXIT ; Rejump to EXIT *****
593 ; *****
594 ;
595 SUBTTL ROM disk driver front end: READ, WRITE & VERIFY *****
596 ; *****
597 ; INTERNAL FUNCTION PROCESSING ROUTINE *****
598 ; NAME: DO_IO *****
599 ; ABSTRACT: Process all disk I/O functions
600 ; PROCESSING: * Interpret indicators from CHQDRV into errors (if any)
601 ; * Initialize status & retry status bytes to DKNORM
602 ; * Check for attempt to transfer past 64k boundary error
603 ; * Perform a disk I/O function
604 ; * If needed, adjust cylinder, track, sector & buffer address
605 ; parameters for repeating the I/O function on consecutive
606 ; sectors & re-issue the I/O request
607 ; INPUT: * DL = Drive number
608 ; * CF = C (B for BSD) if drive is undefined
609 ; * ZF = NZ if drive is invalid
610 ; OUTPUT: * [D_REG+STAT] (byte) = Error code if error occurs
611 ; * [D_REG+CYL, +TRK, +SEC & +BUF] = modified for multi-sectr I/O
612 ; REGISTERS:
613 ; STACK USED:
614 ; *****
615 OPREAD: MOV AL, RQ_READ ; AL := Read sector function code *****
616 JMP short ptr DO_IO
617 OPWRITE: MOV AL, RQ_WRITE ; AL := Write sector function code *****
618 JMP short ptr DO_IO
```

```

00D1' B0 03      619      OPVERF: MOV      AL,RQ_VFC
00D3' EB 02      620      JMP
00D5' B0 04      621      OPVRFY: MOV      AL,RQ_VFD
00D7' A2 0000"   622      DD_IO:  MOV      D_REG+FUNC,AL
00DA' 75 95      623      JNZ      JCMDER
00DC' 72 7A      624      JVB      NRDYER
00DE' C6 06 0000" 00      625      D_REG+STAT,DKNORM
00E3' C6 06 0000" 00      626      D_REG+RSTAT,DKNORM
00E8' C4 1E 0000"   627      BX,dword ptr D_REG+DIT
00EC' A0 0000"   628      MOV      AL,D_REG+BECKNT
00EF' 08 C0      629      OR
00F1' 74 D4      630      JZ
00F3' 30 E4      631      XOR      AH,AH
00F5' 26F7 67 04   632      MUL      word ptr EB:DITSEC[BX]
00F9' 03 06 0000"   633      ADD      AX,word ptr D_REG+BUF
00FD' 83 D2 00      634      ADC      DX,0
0100' 4A          635      DEC
0101' 7F 31      636      JQ
0103' 7C 04      637      JL      ILOOP
0105' 09 C0      638      OR
0107' 75 48      639      JNZ
0109' EB 00EB     640      ILOOP: CALL
010C' 75 89      641      JNZ
010E' 80 3E 0000" 00      642      CMP      D_REG+BECKNT,0
0113' 74 82      643      JZ
0115' FE 0E 0000"   644      DEC
0119' 74 AC      645      JZ
0119' 74 AC      646
0119' 74 AC      647
0119' 74 AC      648
011B' C4 1E 00CJ"   649      BX,dword ptr D_REG+DIT
011F' 26BB 47 04   650      MOV      AX,ES:DITSEC[BX]
0123' 01 06 0000"   651      ADD      word ptr D_REG+BUF,AX
0127' BE 0000"     652      MOV      SI,offset D_REG+SEC
012A' FE 04      653      INC      byte ptr [SI]
012C' BA 04      654      MOV      AL,byte ptr [SI]
012E' 263A 47 06   655      CMP      AL,ES:DITTRK[BX]
0132' 76 D5      656      JBE      ILOOP
0132' 76 D5      657
0134' C6 04 01     658      MOV      byte ptr [SI],1
0137' 4E 04      659      DEC      SI
0138' FE 04      660      INC      byte ptr [SI]
013A' BA 04      661      MOV      AL,byte ptr [SI]
013C' 263A 47 07   662      CMP      AL,ES:DITCYL[BX]
0140' 72 C7      663      JBE      ILOOP
0140' 72 C7      664
0140' 72 C7      665
0140' 72 C7      666
0142' C6 04 00     667      MOV      byte ptr [SI],0
0145' 4E          668      DEC      SI
0146' FE 04      669      INC      byte ptr [SI]
0148' BA 04      670      MOV      AL,byte ptr [SI]

```

```

; AL := Verify CRC function code
; AL := Verify data function code
; Set function request to read or write
; Return command error if drive # bad
; Return not ready error if drive undef
; Initialize status to normal
; Initialize retry status to normal
; EB:BX := @ DIT for current drive
; AL := number of sectors to transfer
; Q: Is that value zero?
; Y: All right, don't xfer anything
; Convert to unsigned word value
; DX,AX := number of bytes to transfer
; Add buffer offset
; (double word)
; Q: Is that >64k?
; Y: Return 64k boundary error
; If ( < 64k, then perform the I/O
; Q: Is it = 64k?
; N: Return boundary error
; Execute the disk command
; Error exit if error occurred
; Q: device dep code after all the data?
; Y: Then exit
; Q: Have we I/O'd all the sectors?
; Y: Then exit
; N: Then increment to next sector:
; EB:BX := @ current DIT
; AX := Sector size
; Add to buffer offset
; SI := @ req sector field of D_REG
; Increment requested sector number
; AL := new sector number
; Q: Is there such a sector?
; Y: Perform the I/O
; N: Increment to the next track:
; Reset sector number to 1st sector
; SI := @ req track field of D_REG
; Increment requested track number
; AL := new track number
; Q: Is there such a track?
; Y: Perform the I/O
; N: Increment to the next cylinder:
; Zero track number
; SI := @ req cylinder field of D_REG
; Increment cylinder number
; AL := New cylinder number

```

```
014A' 263A 47 0B
014E' 72 B9
0150' 80 04
0152' EB 06
0154' 80 09
0156' EB 02
0158' 80 80
015A' E9 FE33
671 CMP AL,ES:DITDSK[BX] ; G: Is there such a cylinder?
672 JB IOLOOP ; Y: Perform the I/O
673 MOV AL,DKNRNF short ptr JEXITE ; N: Return a record not found error
674 JMP short ptr JEXITE ;
675
676 MOV AL,DKBOUN ; Exit with 64k boundary error
677 JMP short ptr JEXITE ;
678
679 MOV AL,DKNRDY ; Exit with disk not ready error
680 JEXITE: JMP EXITE ;
681
682
683
684
685
686
687
688
689
690
691
692
693
694
695
696
697
698
699
700
701
702
703
704
705
706
707
708
709
710
711
712
713
714
715
716
717
718
719
720
721
722
SUBTTL ROM disk driver front end: CHGDRV (CHANGE CURRENT DRIVE)
INTERNAL ROUTINE
NAME: CHGDRV
ABSTRACT: Check the requested drive number for validity and update the
disk driver data base, when appropriate, for accessing a new
drive (i.e., a different one from the last request).
INPUT: * DL = New drive number
* Check for valid drive number
PROCESSING: * Store current drive's position
* If no "indicators" are set, make new drive the current drive
by getting and storing:
- New drive number
- New drive's current position
- New drive's DIT address
OUTPUT: * BX = Same as input
* SI = Same as input
* DI = New drive number
* CF = NC & ZF = Z if no errors were found
* CF = C & ZF = Z if the drive is undefined
* CF = NC & ZF = NZ if drive # is invalid (# ( 0 or # ) NUMDRV)
REGISTERS: AX, CX, DI, ES
STACK USED: 2 bytes
NOTES: * Flag returns are called "indicators" rather than "errors" be-
cause the interpretation of the indicators as errors depends
on the command executed. For example, "drive undefined" isn't
an error for the "set DIT" functions, but it is for the I/O
functions. Therefore, the code for each function decides how
to interpret the indicators returned by this routine.
*****
CHGDRV PROC NEAR
DL,NUMDRV-1
JMP CHCRET
CMP D_REG+UNIT,DL
JNE CHCRET
MOV AL,D_REG+UNIT
MOV CBW
MOV DI,AX
INC AX
JZ DO_NEW
015D' 80 FA 00"
0160' 77 4A
0162' 3B 16 0000"
0166' 74 44
0168' A0 0000"
016B' 9B
016C' 8B FB
016E' 40
016F' 74 0B
```

```

0171' BA 0E 0000"
0175' BB 8D 0006"
0179'

0179' BA CA
017B' AD 000C"
017E' OC F0
0180' D2 EB
0182' AB 01
0184' F9
0185' 74 25
0187' 8A C2

0189' 97
018A' 8A AD 0006"
018E' D1 E7
0190' D1 E7
0192' C4 85 0004"
0196' 09 C0
0198' F9
0199' 74 11
019B' 8B 16 0000"
019F' 8B 2E 0000"
01A3' A3 0000"
01A6' BC 06 0000"
01AA' 31 C0
01AC' C3

723 MOV CL, D_REG+CURCYL
724 MOV D_POSIDIJ, CL
725 DO_NEW:
726
727 MOV CL, DL
728 AL, BYTE PTR SYBSCON
729 OR AL, 011110000B
730 SHR AL, CL
731 TEST AL, 1
732 BTC
733 JZ
734 MOV CHORET
735 AL, DL
736 CBW
737 XCHO
738 MOV CH, D_POSIDIJ
739 SHL DI, 1
740 SHL DI, 1
741 LEB AX, dword ptr D_DITIDIJ
742 OR AX, AX
743 BTC
744 JZ
745 MOV D_REG+UNIT, DL
746 MOV word ptr D_REG+CURCYL, CH
747 MOV word ptr D_REG+DIT+0, AX
748 MOV word ptr D_REG+DIT+2, EB
749 XOR AX, AX
749 CHORET: RET
750 *****
751
752 SUBITL ROM disk driver front end: DCALL (CALL ODD ROUTINE)
753 *****
754 PUBLIC ROUTINE *****
755 NAME: DCALL
756 ABSTRACT: Called by the interrupt level code to invoke device-dependent
757 General Disk Operation routines
758 PROCESSING: * Convert the NEAR call to this routine into a FAR call
759 * Set BP register to the offset of the disk data area in ROMDAT
760 * Invokes a device-dependent Generic Disk Operation subroutine
761 INPUT: * AL = Code for operation (passed on to interface routine)
762 * [D_REG+DIT] (dword) = seg:offset of the Disk Interface Table
763 none from this routine
764 REGISTERS: AX, BX, DX, DI, ES plus that used by the called routine
765 STACK USED: This routine does not use any stack, it converts the NEAR call
766 to this routine to a FAR call, placing 4 bytes of return addr
767 information on the stack. However, the four bytes are included
768 in the stack usage of the invoked routines. Invoking floppy
769 ODD routines will use up to 12 bytes of stack.
770 *****
771 DCALL PROC NEAR
772 POP DX
773 PUSH CB
774 PUSH DX
01AD' 5A
01AE' 0E
01AF' 52

```

```

; CL := Current drive's position
; Store current drive's position

; CL := Shift count for installed test
; AL := Installed bit for floppy drives
; Force all other drives to "installed"
; Move installed bit to bit 0
; G: Is drive installed?
; N: Return undefined (C & Z)
; AL := "New" current drive number
; Convert to word value
; DI := New drive index
; CH := New drive's position
; DI := Index to DIT table

; EB: AX := @ drive's DIT
; G: Is drive's DIT defined ( ) 0?
; Set CF (C) in case of error
; N: Return undefined (C & Z)
; Y: Set new unit number as current
; Set new position as current
; Set new DIT address as current

; Return OK (NC & Z)
; Insure carry clear
*****

```

```

*****
PUBLIC ROUTINE *****
NAME: DCALL
ABSTRACT: Called by the interrupt level code to invoke device-dependent
General Disk Operation routines
PROCESSING: * Convert the NEAR call to this routine into a FAR call
* Set BP register to the offset of the disk data area in ROMDAT
* Invokes a device-dependent Generic Disk Operation subroutine
INPUT: * AL = Code for operation (passed on to interface routine)
* [D_REG+DIT] (dword) = seg:offset of the Disk Interface Table
none from this routine
REGISTERS: AX, BX, DX, DI, ES plus that used by the called routine
STACK USED: This routine does not use any stack, it converts the NEAR call
to this routine to a FAR call, placing 4 bytes of return addr
information on the stack. However, the four bytes are included
in the stack usage of the invoked routines. Invoking floppy
ODD routines will use up to 12 bytes of stack.
*****
DCALL PROC NEAR
POP DX
PUSH CB
PUSH DX

```

```

775
776
777
778
779
780
781
782
783
784
785
786
787
788
789
790
791
792
793
794
795
796
797
798
799
800
801
802
803
804
805
806
807
808
809
810
811
812
813
814
815
816
817
818
819
820
821
822
823
824
825
826

01B0' C4 1E 0000"
01B4' BD 0003"
01B7' 26FF 2F

180'
184'
188'
192'
196'
200'
204'
208'
212'
216'
220'
224'
228'
232'
236'
240'
244'
248'
252'
256'
260'
264'
268'
272'
276'
280'
284'
288'
292'
296'
300'
304'
308'
312'
316'
320'
324'
328'
332'
336'
340'
344'
348'
352'
356'
360'
364'
368'
372'
376'
380'
384'
388'
392'
396'
400'
404'
408'
412'
416'
420'
424'
428'
432'
436'
440'
444'
448'
452'
456'
460'
464'
468'
472'
476'
480'
484'
488'
492'
496'
500'
504'
508'
512'
516'
520'
524'
528'
532'
536'
540'
544'
548'
552'
556'
560'
564'
568'
572'
576'
580'
584'
588'
592'
596'
600'
604'
608'
612'
616'
620'
624'
628'
632'
636'
640'
644'
648'
652'
656'
660'
664'
668'
672'
676'
680'
684'
688'
692'
696'
700'
704'
708'
712'
716'
720'
724'
728'
732'
736'
740'
744'
748'
752'
756'
760'
764'
768'
772'
776'
780'
784'
788'
792'
796'
800'
804'
808'
812'
816'
820'
824'
828'
832'
836'
840'
844'
848'
852'
856'
860'
864'
868'
872'
876'
880'
884'
888'
892'
896'
900'
904'
908'
912'
916'
920'
924'
928'
932'
936'
940'
944'
948'
952'
956'
960'
964'
968'
972'
976'
980'
984'
988'
992'
996'
1000'

; Call converted from NEAR to FAR
; ES,BX := @ Disk Interface Table
; BP := base address of disk data area
; Transfer to Disk Interface Routine
;*****
;*****
SUBTTL ROM disk driver front end: DFINISH (SIGNAL DISK DONE)
;*****
; PUBLIC ROUTINE
; NAME: DFINISH
; ABSTRACT: * Called by interrupt level code to indicate that it's done
; PROCESSING: * Set the disk driver wait flag to "READY"
; INPUT: none
; OUTPUT: * [D_WAIT] (byte) = "READY"
; REGISTERS: none
; STACK USED: 2 bytes
;*****
DFINISH PROC NEAR
MOV D_WAIT,READY ; Clear the D_WAIT flag
RET ; And return
;*****
SUBTTL ROM disk driver front end: DSKINI (DRIVER INITIALIZATION)
;*****
; PUBLIC ROUTINE DSKINI
; NAME: DSKINI
; ABSTRACT: Initialize the disk data area
; PROCESSING: * Initialize the disk driver data area in ROMDAT
; INPUT: none
; OUTPUT: none
; REGISTERS: AX, BX, CX, SI, DI, ES
; STACK USED: 2 bytes
;*****
DSKINI PROC NEAR
PUSH DS ; ES := DS
POP EB
MOV AX,offset D_DBGN ; AX := @ beginning of disk data area
DI,AX ; Copy that to DI
MOV CX,offset D_DEND+1 ; CX := just past end of disk data area
SUB CX,AX ; CX := Length of disk data area
XOR AL,AL ; AL := 0 for initialization
REP ; Repeat:
STOB EB ; Zero disk data area
; Initialize defaults:
MOV D_STAT,S_IDLE ; Initialize driver state to "idle"
; Note that B_IDLE = 0
CX,NUMFLP ; Initialize table of DIT pointers
BX,BX ; for floppy disk drives
D_DIT+00[BX],offset SSST
D_DIT+02[BX],CB
BX,4
INIDIT: MOV
ADD LOOP
INIDIT

```

```

827 ; *****
828 ; INTERNAL ROUTINE
829 ; NAME: INVPOB
830 ; ABSTRACT: Invalidate current disk positions and current drive. This
831 ;           resets the disk system. The invalid disk position tells the
832 ;           device specific code that a reset was requested.
833 ; PROCESSING: * Set all disk current positions to "INVALID" (-1)
834 ;           * Set current drive unit number to "INVALID" (-1)
835 ; INPUT: none
836 ; OUTPUT: * ID_POB] (byte array) = "INVALID"
837 ;           * [ID_REQ+UNIT] (byte) = "INVALID"
838 ; REGISTERS: BX, CX
839 ; STACK USED: 2 bytes
840 ; *****
841 INVP0B: MOV     CX,NUMDRV
842           XOR     BX,BX
843           MOV     D_POB[BX],INVALID
844           INC     BX
845           LOOP   INIPOB
846           MOV     D_REQ+UNIT,INVALID
847           RET
848
849
850
851 SUBTTL ROM disk driver front end: EXECMD (EXEC CMD & WAIT)
852 ; *****
853 ; INTERNAL ROUTINE
854 ; NAME: EXECMD
855 ; ABSTRACT: Invoke the disk Interrupt Service Routine to process a disk
856 ;           request and wait for it to finish.
857 ; PROCESSING: * Set the disk driver's wait flag to "BUSY"
858 ;           * Initiate interrupt processing using the SOFINT routine
859 ;           * Wait for wait flag to be set to "READY"
860 ;           * Check the status byte for any error indication
861 ; INPUT: none
862 ; OUTPUT: * ZF = NZ if an error occurred during interrupt processing
863 ; REGISTERS: none
864 ; STACK USED: 10 bytes
865 ; *****
866 EXECMD PROC NEAR
867           MOV     D_WAIT,BUSY
868           CALL   SOFINT
869           CMP     D_WAIT,READY
870           JNE     WAITER
871           CMP     D_REQ+STAT,DKNORM
872           RET
873           ; Set wait flag to "busy"
874           ; Initiate interrupt-driven code
875           ; Wait for it to finish
876
877
878
879
880
881
882
883
884
885
886
887
888
889
890
891
892
893
894
895
896
897
898
899
900
901
902
903
904
905
906
907
908
909
910
911
912
913
914
915
916
917
918
919
920
921
922
923
924
925
926
927
928
929
930
931
932
933
934
935
936
937
938
939
940
941
942
943
944
945
946
947
948
949
950
951
952
953
954
955
956
957
958
959
960
961
962
963
964
965
966
967
968
969
970
971
972
973
974
975
976
977
978
979
980
981
982
983
984
985
986
987
988
989
990
991
992
993
994
995
996
997
998
999
1000
    
```

```

879 ; * Invoke the disk Interrupt Service Routine via soft interrupt
880 ; INPUT: none
881 ; OUTPUT: * [D_REG+STAT] (byte) = "DKNRDY" (timeout error)
882 ; REGISTERS: none
883 ; STACK USED: 8 bytes, 6 for interrupt & 2 for storing the interrupted DB
884 ; *****
885 TIMEOUT PROC NEAR
886     MOV     D_REG+STAT,DKNRDY    ; Set status to timeout
887     JMP     short ptr SOFINT    ; Invoke a software interrupt
888 ; *****
889
890 SUBTTL ROM disk driver front end: SOFINT (SOFT INTERRUPT TO DSKISR)
891 ; *****
892 ; PUBLIC ROUTINE SOFINT
893 ; NAME:
894 ; ABSTRACT: Called by DSKIO routines and the FLTIME (motor-off) routine in
895 ;           FLPDIR to enter the interrupt-driven part of the disk driver.
896 ;           This particular subroutine may only be called by routines in
897 ;           the ROMCOD data segment, add-on ROMs will need their own copy.
898 ; PROCESSING: * Set the soft interrupt indicator. NOTE: This is ABSOLUTELY
899 ;             essential for the correct processing of a software interrupt.
900 ;             If we don't, the interrupt driven logic will tell the system
901 ;             interrupt controller that it has processed an interrupt which
902 ;             the controller knows nothing about (the soft interrupt). This
903 ;             would cause strange, but not wonderful things to happen.
904 ;             * Execute a software interrupt to the the disk driver.
905 ; INPUT: none
906 ; OUTPUT: * [D_SOFT] (byte) = 1 indicating a software interrupt issued
907 ; REGISTERS: none
908 ; STACK USED: 8 bytes, 6 for interrupt & 2 for storing the interrupted DS
909 ; *****
910 SOFINT PROC NEAR
911     INC     D_SOFT
912     INT     IR6INT
913     RET
914 ; *****
915
916 SUBTTL ROM disk driver front end: STANDARD DITS TABLE
917 ; *****
918 ; FIXED DATA TABLES
919 ; *****
920 DITBL LABEL WORD
921     DW     SSSD
922     DW     DSSD
923     DW     SSDID
924     DW     DSDID
925 ; *****
926 SSSD LABEL WORD
927     DD     FLPDIR
928     DW     XXSEC
929     DB     XXTRK
930     DB     SBCYL
931
932 ; *****
933 ; TABLE OF STANDARD DIT'S (POINTERS)
934 ; 0: Single-side, single track density
935 ; 1: Double-side, single track density
936 ; 2: Single-side, double track density
937 ; 3: Double-side, double track density
938 ; *****
939 SSSD LABEL WORD
940     DD     FLPDIR
941     DW     XXSEC
942     DB     XXTRK
943     DB     SBCYL
944
945 ; *****
946 ; SINGLE-BIDED, SINGLE TRACK DENSITY
947 ; Floppy disk Interface Routine vector
948 ; Sector size in bytes
949 ; Track size in sectors
950 ; Cylinder size in tracks

```

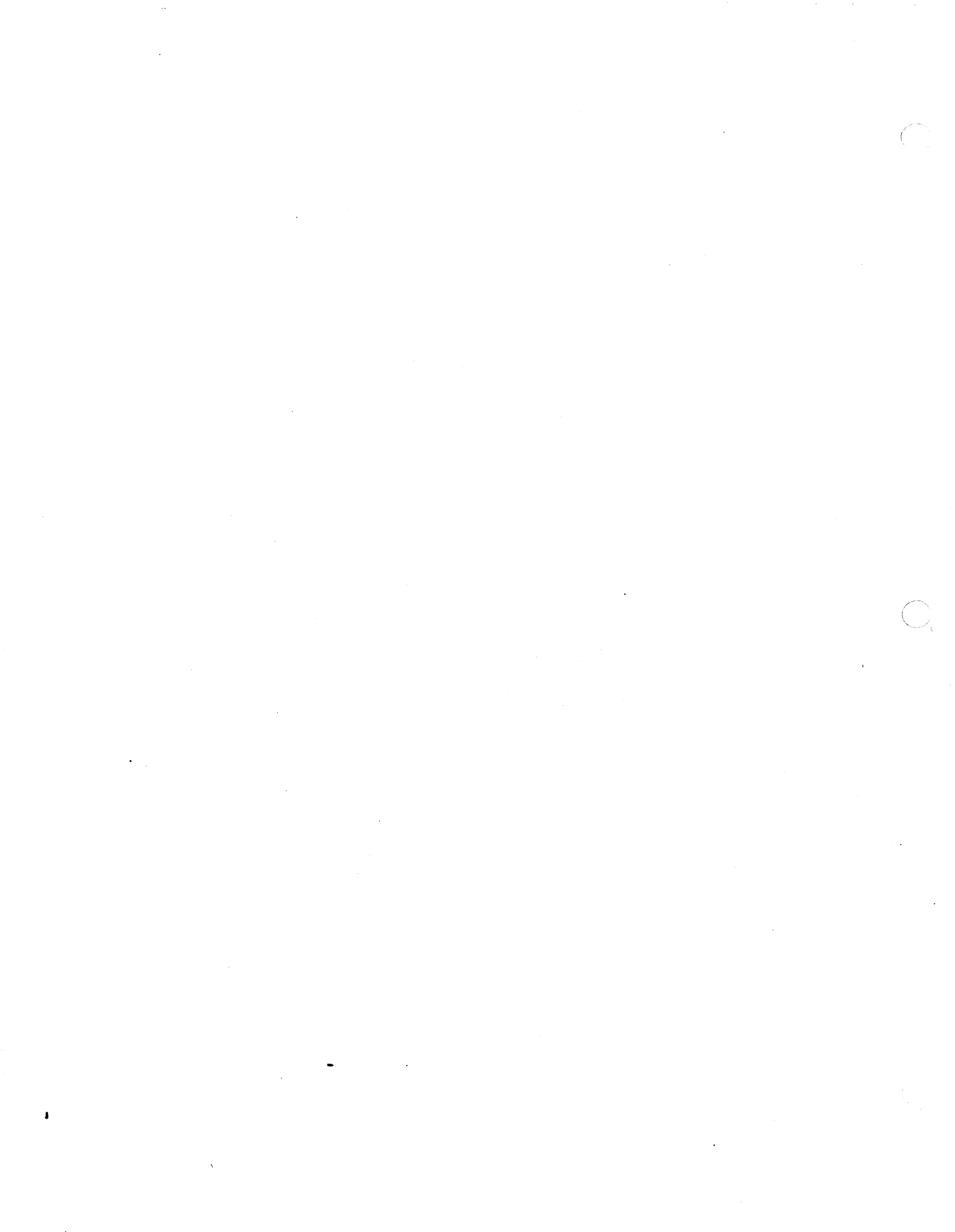
```

0228' 28          STDSK           ; Disk size in cylinders
0229' 02          FLPERR          ; Maximum number of retries
022A' 14          STPRCMP         ; Cylinder at which to change pre-comp
022B'           ; *****
022C'           ; DOUBLE-BIDED, SINGLE TRACK DENSITY *****
022D'           ; Floppy disk Interface Routine vector
022E'           ; Sector size in bytes
022F' 0200      DD              ; Track size in sectors
0230' 08          XXTRK          ; Cylinder size in tracks
0231' 02          DSCYL         ; Disk size in cylinders
0232' 28          STDSK           ; Maximum number of retries
0233' 02          FLPERR          ; Cylinder at which to change pre-comp
0234' 14          STPRCMP         ; *****
0235'           ; SINGLE-BIDED, DOUBLE TRACK DENSITY *****
0236'           ; Floppy disk Interface Routine vector
0237'           ; Sector size in bytes
0238' 08          XXTRK          ; Track size in sectors
0239' 01          SBCYL         ; Cylinder size in tracks
023A' 50          DTDSK         ; Disk size in cylinders
023B' 02          FLPERR          ; Maximum number of retries
023C' 28          DTPRCMP        ; Cylinder at which to change pre-comp
023D'           ; *****
023E'           ; DOUBLE-BIDED, DOUBLE TRACK DENSITY *****
023F'           ; Floppy disk Interface Routine vector
0240'           ; Sector size in bytes
0241' 08          XXTRK          ; Track size in sectors
0242' 02          DSCYL         ; Cylinder size in tracks
0243' 50          DTDSK         ; Disk size in cylinders
0244' 02          FLPERR          ; Maximum number of retries
0245' 28          DTPRCMP        ; Cylinder at which to change pre-comp
0246'           ; *****
0247'           ; SUBTTL
0248'           ; END
931    DB          STDSK
932    DB          FLPERR
933    DB          STPRCMP
934    ; *****
935    DSSTD LABEL WORD
936    DD          FLPDIR
937    DW          XXSEC
938    DB          XXTRK
939    DB          DSCYL
940    DB          STDSK
941    DB          FLPERR
942    DB          STPRCMP
943    ; *****
944    SBDTD LABEL WORD
945    DD          FLPDIR
946    DW          XXSEC
947    DB          XXTRK
948    DB          SBCYL
949    DB          DTDSK
950    DB          FLPERR
951    DB          DTPRCMP
952    ; *****
953    DSDTD LABEL WORD
954    DD          FLPDIR
955    DW          XXSEC
956    DB          XXTRK
957    DB          DSCYL
958    DB          DTDSK
959    DB          FLPERR
960    DB          DTPRCMP
961    ; *****
962    SUBTTL
963    END

```

No errors detected


```
1 //*****  
2 //** TITLE - DSKISR  
3 //** COMPUTER - BOBB C  
4 //** ABSTRACT - This is the drive independent, interrupt driven portion of  
5 //** the PEGASUS disk driver.  
6 //**  
7 //** All C code translated to assembly by the author. Some of the  
8 //** routines are expanded in line - in particular the interrupt  
9 //** overhead and entry/exit. This code probably only vaguely  
10 //** resembles what a real C compiler would generate since I  
11 //** could make use of special knowledge such as what registers  
12 //** were in use. The C code reflects the actual logic of the  
13 //** program but probably cannot translate directly into a working  
14 //** program without system-dependent re-writing.  
15 //**  
16 //** REGISTERS USED - All registers are restored intact  
17 //**  
18 //** STACK USED - at least 24 bytes, probably more.  
19 //**  
20 //*****  
21 NAME DSKISR  
22 #include "peg:dkerr.ceq" /*Include disk error codes*/  
60 #include "peg:dktop.ceq" /*Include disk operation codes*/  
80 #include "peg:dkreq.ceq" /*Include disk requests*/  
91 #include "peg:dksta.ceq" /*Include disk states*/  
103 , /*Miscellaneous*/  
104 #define RETRIES 2 /*Max retries on an operation*/  
105 RETRIES EQU 2  
106 ,  
107 #define NULL 0 /*The null value*/  
108 NULL EQU 0  
109 #define NO 0 /*Boolean value 'no' (false)*/  
110 NO EQU 0  
111 #define YES 1 /*Boolean value 'yes' (true)*/  
112 YES EQU 1  
113 ,  
114 #include "peg:intctlr.egu" /*Include disk error codes*/  
115 #include "peg:vector.egu" /*Include disk requests*/  
116 #include "peg:timevt.egu" /*Include disk states*/  
117 ,  
118 #include "peg:evtsta.egu" /*Include definition for RIB*/  
257 #include "peg:evtsta.egu" /*Include definition for RIB*/  
258 ACTEVT EQU 1 shl EVTSTA ; Mask for setting bit to activate evt  
259 TIMIV EQU 50 ; Timeout interval = 1.25 seconds  
260 //*****  
261 #define BYTE char  
262 #define BOOL char  
263 #define ADDR long  
264 ,  
265 #include "peg:dkrib.ceq"  
281 ,  
282 #define SECT ROMDAT  
-0002  
-0000  
-0000  
-0001  
-0080  
-0032  
-0000
```



```

283 ; BYTE D_EKNT ;
284 EXTRN D_EKNT: BYTE
285 ; BYTE D_NCMD ;
286 EXTRN D_NCMD: BYTE
287 ; BYTE D_NSTA ;
288 EXTRN D_NSTA: BYTE
289 ; RIB D_REG ;
290 EXTRN D_REG: BYTE
291 ; BOOL D_SOFT ;
292 EXTRN D_SOFT: BYTE
293 ; BYTE D_STAT ;
294 EXTRN D_STAT: BYTE
295 EXTRN DSKEVT: BYTE
296 EXTRN DSKIBP: WORD
297 EXTRN DKSSV: WORD
298 EXTRN DKSPSV: WORD
299
300 =0000
301 SECT ROMCOD
302 ASSUME CS: ROMCOD, DS: ROMDAT
303
304 EXTRN DCALL: NEAR
305 EXTRN DFINIS: NEAR
306
307 ; DSK1BR()
308 PUBLIC DSK1BR
309 PROC FAR
310 LOCAL
311 ;
312 ; Global BOOL D_BSY ;
313 ; Global RIB D_REG ;
314 ; Global BYTE D_EKNT ;
315 ; Global BYTE D_NCMD ;
316 ; Global BYTE D_NSTA ;
317 ; Global BYTE D_STAT ;
318 ; Global BOOL D_TIM ;
319 ;
320 ; INTERRUPT_ENTRY() ; /*Do system overhead things for interrupt processing*/
321 PUSH DS
322 INC byte ptr CB: INTCTR+CSWRAP
323 MOV DS, word ptr CS: DSADDR+CSWRAP
324 MOV DKSSV, SS
325 MOV DKSPSV, SP
326 MOV SP, DS
327 MOV SS, SP
328 MOV SP, offset DSK1SP
329 PUSH AX
330 PUSH BX
331 PUSH EB
332 PUSH CX
333 PUSH DX
334 PUSH SI
335
336 ;
337 ;
338 ;
339 ;
340 ;
341 ;
342 ;
343 ;
344 ;
345 ;
346 ;
347 ;
348 ;
349 ;
350 ;
351 ;
352 ;
353 ;
354 ;
355 ;
356 ;
357 ;
358 ;
359 ;
360 ;
361 ;
362 ;
363 ;
364 ;
365 ;
366 ;
367 ;
368 ;
369 ;
370 ;
371 ;
372 ;
373 ;
374 ;
375 ;
376 ;
377 ;
378 ;
379 ;
380 ;
381 ;
382 ;
383 ;
384 ;
385 ;
386 ;
387 ;
388 ;
389 ;
390 ;
391 ;
392 ;
393 ;
394 ;
395 ;
396 ;
397 ;
398 ;
399 ;
400 ;
401 ;
402 ;
403 ;
404 ;
405 ;
406 ;
407 ;
408 ;
409 ;
410 ;
411 ;
412 ;
413 ;
414 ;
415 ;
416 ;
417 ;
418 ;
419 ;
420 ;
421 ;
422 ;
423 ;
424 ;
425 ;
426 ;
427 ;
428 ;
429 ;
430 ;
431 ;
432 ;
433 ;
434 ;
435 ;
436 ;
437 ;
438 ;
439 ;
440 ;
441 ;
442 ;
443 ;
444 ;
445 ;
446 ;
447 ;
448 ;
449 ;
450 ;
451 ;
452 ;
453 ;
454 ;
455 ;
456 ;
457 ;
458 ;
459 ;
460 ;
461 ;
462 ;
463 ;
464 ;
465 ;
466 ;
467 ;
468 ;
469 ;
470 ;
471 ;
472 ;
473 ;
474 ;
475 ;
476 ;
477 ;
478 ;
479 ;
480 ;
481 ;
482 ;
483 ;
484 ;
485 ;
486 ;
487 ;
488 ;
489 ;
490 ;
491 ;
492 ;
493 ;
494 ;
495 ;
496 ;
497 ;
498 ;
499 ;
500 ;
501 ;
502 ;
503 ;
504 ;
505 ;
506 ;
507 ;
508 ;
509 ;
510 ;
511 ;
512 ;
513 ;
514 ;
515 ;
516 ;
517 ;
518 ;
519 ;
520 ;
521 ;
522 ;
523 ;
524 ;
525 ;
526 ;
527 ;
528 ;
529 ;
530 ;
531 ;
532 ;
533 ;
534 ;
535 ;
536 ;
537 ;
538 ;
539 ;
540 ;
541 ;
542 ;
543 ;
544 ;
545 ;
546 ;
547 ;
548 ;
549 ;
550 ;
551 ;
552 ;
553 ;
554 ;
555 ;
556 ;
557 ;
558 ;
559 ;
560 ;
561 ;
562 ;
563 ;
564 ;
565 ;
566 ;
567 ;
568 ;
569 ;
570 ;
571 ;
572 ;
573 ;
574 ;
575 ;
576 ;
577 ;
578 ;
579 ;
580 ;
581 ;
582 ;
583 ;
584 ;
585 ;
586 ;
587 ;
588 ;
589 ;
590 ;
591 ;
592 ;
593 ;
594 ;
595 ;
596 ;
597 ;
598 ;
599 ;
600 ;
601 ;
602 ;
603 ;
604 ;
605 ;
606 ;
607 ;
608 ;
609 ;
610 ;
611 ;
612 ;
613 ;
614 ;
615 ;
616 ;
617 ;
618 ;
619 ;
620 ;
621 ;
622 ;
623 ;
624 ;
625 ;
626 ;
627 ;
628 ;
629 ;
630 ;
631 ;
632 ;
633 ;
634 ;
635 ;
636 ;
637 ;
638 ;
639 ;
640 ;
641 ;
642 ;
643 ;
644 ;
645 ;
646 ;
647 ;
648 ;
649 ;
650 ;
651 ;
652 ;
653 ;
654 ;
655 ;
656 ;
657 ;
658 ;
659 ;
660 ;
661 ;
662 ;
663 ;
664 ;
665 ;
666 ;
667 ;
668 ;
669 ;
670 ;
671 ;
672 ;
673 ;
674 ;
675 ;
676 ;
677 ;
678 ;
679 ;
680 ;
681 ;
682 ;
683 ;
684 ;
685 ;
686 ;
687 ;
688 ;
689 ;
690 ;
691 ;
692 ;
693 ;
694 ;
695 ;
696 ;
697 ;
698 ;
699 ;
700 ;
701 ;
702 ;
703 ;
704 ;
705 ;
706 ;
707 ;
708 ;
709 ;
710 ;
711 ;
712 ;
713 ;
714 ;
715 ;
716 ;
717 ;
718 ;
719 ;
720 ;
721 ;
722 ;
723 ;
724 ;
725 ;
726 ;
727 ;
728 ;
729 ;
730 ;
731 ;
732 ;
733 ;
734 ;
735 ;
736 ;
737 ;
738 ;
739 ;
740 ;
741 ;
742 ;
743 ;
744 ;
745 ;
746 ;
747 ;
748 ;
749 ;
750 ;
751 ;
752 ;
753 ;
754 ;
755 ;
756 ;
757 ;
758 ;
759 ;
760 ;
761 ;
762 ;
763 ;
764 ;
765 ;
766 ;
767 ;
768 ;
769 ;
770 ;
771 ;
772 ;
773 ;
774 ;
775 ;
776 ;
777 ;
778 ;
779 ;
780 ;
781 ;
782 ;
783 ;
784 ;
785 ;
786 ;
787 ;
788 ;
789 ;
790 ;
791 ;
792 ;
793 ;
794 ;
795 ;
796 ;
797 ;
798 ;
799 ;
800 ;
801 ;
802 ;
803 ;
804 ;
805 ;
806 ;
807 ;
808 ;
809 ;
810 ;
811 ;
812 ;
813 ;
814 ;
815 ;
816 ;
817 ;
818 ;
819 ;
820 ;
821 ;
822 ;
823 ;
824 ;
825 ;
826 ;
827 ;
828 ;
829 ;
830 ;
831 ;
832 ;
833 ;
834 ;
835 ;
836 ;
837 ;
838 ;
839 ;
840 ;
841 ;
842 ;
843 ;
844 ;
845 ;
846 ;
847 ;
848 ;
849 ;
850 ;
851 ;
852 ;
853 ;
854 ;
855 ;
856 ;
857 ;
858 ;
859 ;
860 ;
861 ;
862 ;
863 ;
864 ;
865 ;
866 ;
867 ;
868 ;
869 ;
870 ;
871 ;
872 ;
873 ;
874 ;
875 ;
876 ;
877 ;
878 ;
879 ;
880 ;
881 ;
882 ;
883 ;
884 ;
885 ;
886 ;
887 ;
888 ;
889 ;
890 ;
891 ;
892 ;
893 ;
894 ;
895 ;
896 ;
897 ;
898 ;
899 ;
900 ;
901 ;
902 ;
903 ;
904 ;
905 ;
906 ;
907 ;
908 ;
909 ;
910 ;
911 ;
912 ;
913 ;
914 ;
915 ;
916 ;
917 ;
918 ;
919 ;
920 ;
921 ;
922 ;
923 ;
924 ;
925 ;
926 ;
927 ;
928 ;
929 ;
930 ;
931 ;
932 ;
933 ;
934 ;
935 ;
936 ;
937 ;
938 ;
939 ;
940 ;
941 ;
942 ;
943 ;
944 ;
945 ;
946 ;
947 ;
948 ;
949 ;
950 ;
951 ;
952 ;
953 ;
954 ;
955 ;
956 ;
957 ;
958 ;
959 ;
960 ;
961 ;
962 ;
963 ;
964 ;
965 ;
966 ;
967 ;
968 ;
969 ;
970 ;
971 ;
972 ;
973 ;
974 ;
975 ;
976 ;
977 ;
978 ;
979 ;
980 ;
981 ;
982 ;
983 ;
984 ;
985 ;
986 ;
987 ;
988 ;
989 ;
990 ;
991 ;
992 ;
993 ;
994 ;
995 ;
996 ;
997 ;
998 ;
999 ;
1000 ;

```

```

0020' 57          DI          ; ; ; Mask level & interrupts
0021' E4 19      AL, INTA01  ; ; ;
0023' 0C 40      AL, not ENAB6 ; ; ;
0023' E6 19      INTA01, AL  ; ; ;
0027' 80 26 0000" 7F      DSKEVT+IEVFLO, not ACTEVT ; ; ; Abort timeout request
002C' C7 06 0000" 0032   word ptr DSKEVT+IEVCTR, TIMITY ; ; ; Reset Timer count
0032' FB                ; Enable interrupts
0033' 80 3E 0000" 00      D_REG+STAT, E_NORM          ; Q; Timeout?
0038' 75 0C      20$       ; Y; Jump, don't get status.
003A' 80 3E 0006" 00      D_STAT, S_IDLE           ; Q; Beginning a request?
003F' 74 05      20$       ; Y; Jump, don't get status.
0041' 80 0B      AL, OP_RIB ; Qet status of last operation.
0043' EB 000B"      DCALL    ; ;
      -0046          $      ; ;
0046' C6 06 0003" 00      D_NSTA, NULL             ; Clear next state indicator
0048' C6 06 0002" FF      D_NCMD, OP_NULL          ; Clear next command indicator
0050' 8A 1E 0006"      BL, D_STAT             ; Dispatch state
0054' 30 FF      BH, BH        ; Qet state.
0056' 01 DB      BX, BX        ; Convert to a word table index.
0058' 80 3E 0006" 03      D_STAT, S_MAX           ; Q; Is this one defined?
005D' 77 40      LIDDEFI      ; N; Qo Handle as an aux state.
005F' 80 3E 0000" 00      D_REG+STAT, E_NORM       ; Ask this once for all states
      ; which need to know
0064' 2EFF A7 0069"      ; ;
0069' 0071"      CS: 40$(BX) ; ;
0068' 0076"      LIIDLE      ; ;
006D' 0078"      LIWAIT      ; ;
006F' 00BF"      LIIBEK      ; ;
      ; LIDONE      ; ;
0071' B0 09      $          ; Waiting for a request
0073' EB 000B"      AL, OP_BON ; Qo do any pre-request setup
      -0076          DCALL    ; ;
0076' EB 008D      $          ; Waiting to retry a request
0079' EB 29      REG PR      ; Then process the request
      short ptr LIEND1 ; and finish up
007B' B0 02      $          ; Implicit seek pending
007D' A0 0000"      D_REG+STAT, E_NORM       ; Q; Error from seek operation?
0080' A2 0000"      60$      ; Y; Jump and handle it.
0083' EB 0080      AL, D_REG+CYL           ; N; Bat new current cylinder
0086' EB 1C      REG PR      ; And process pending I/D request
      short ptr LIEND1 ; ;
008B' B0 02      $          ; Process error.
008A' EB 000B"      AL, OP_EP           ; ;
008D' EB 15      DCALL    ; And finish up
      short ptr LIEND1 ; ;
      -008F          $      ; General operation completion pending
    
```

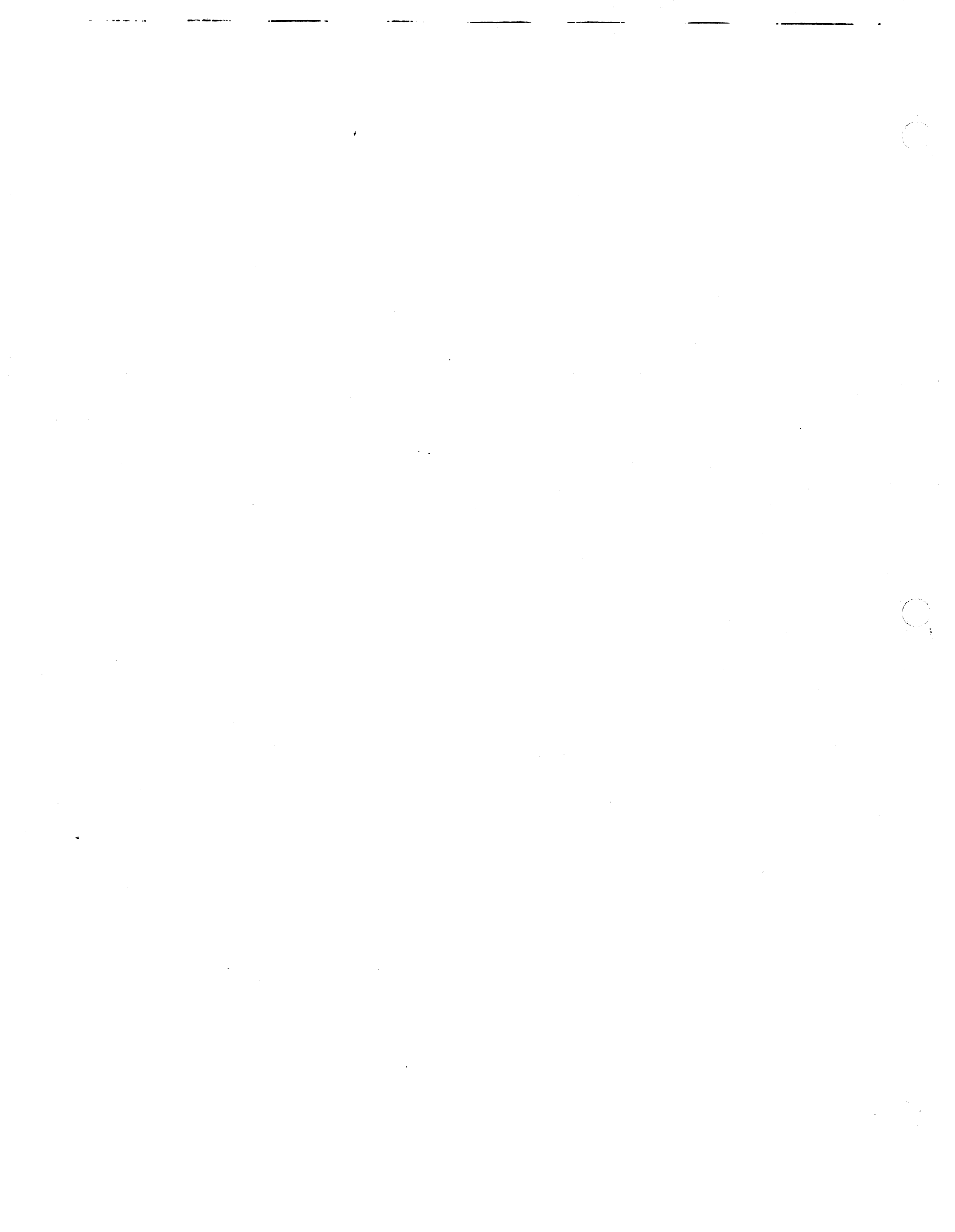
```

387      00BF' 75 07      D_REG+STAT,E_NORM      ; Q: Any errors from operation?
388      0091' C6 06 0003" 00      80$      ; Y: Jump and handle them.
389      0096' EB 0C      D_NSTA,B_IDLE          ; N: Then we're finished
390      0098' 80 02      short ptr LIEND1
391      009A' EB 0008"      AL,OP_EP
392      009D' EB 05      DCALL
393      ;                short ptr LIEND1
394
395      -009F      EQU      ; Driver is in a device-dependent state
396      009F' 80 01      AL,OP_ABP
397      00A1' EB 0008"      DCALL
398
399      -00A4      EQU      ;
400      00A4' 80 3E 0002" FF      D_NCMD,OP_NUL      ; Q: New command to execute?
401      00A9' 74 06      110$      ; N: Jump and continue
402      00AB' A0 0002"      AL,D_NCMD      ; Y: Go execute it
403      00AE' EB 0008"      DCALL
404      -00B1      EQU      ;
405
406      00B1' 80 3E 0003" 00      D_NSTA,B_IDLE
407      00B6' 75 0F      140$
408      00B8' C6 06 0001" 00      D_EKNT,NULL
409      00BD' 80 0A      AL,OP_TER
410      00BF' EB 0008"      DCALL
411      00C2' EB 000C"      DFINISH
412      00C5' EB 05      short ptr 150$
413      00C7'      JMP
414      00C7' 80 0E 0000" 80      140$:
415      -00CC      EQU
416
417      00CC' A0 0003"      DBKEVT+IEVFLG,ACTEVT
418      00CF' A2 0006"      EQU
419
420      00D2' FA      AL,D_NSTA
421
422      00D3' E4 19      D_STAT,AL
423      00D5' 24 BF      AL,INTA01
424      00D7' E6 19      AL,ENAB6
425      00DA' 5F      INTA01,AL
426      00DB' 5E      DI
427      00DC' 5A      SI
428      00DD' 59      DX
429      FE 0E 0005"      CX
430      00E1' 74 11      D_SOFT
431      00E3' FE 06 0003"      SOFOUT
432      00E7' 8E 06 0009"      D_SOFT
433      00EB' 88 1E 000A"      ES,DKSSB
434      00EF' 2EFF 2E C16$      BX,DKSPBV
435
436      00F4' 2EFE 0E C19A      dword ptr CS:XITVEC*4+CSWRAP
437      00F9' 07      byte ptr CS:INTCTR+CSWRAP
438      00FA' 5B      ES
      ; Restore interrupted BX
      ; Decrement interrupt counter
      ; Restore interrupted ES
      ; Restore interrupted BX

```


014F' C3
491 406: RET
492 1/*****
493 END

No errors detected




```
1 *****
2 ; TITLE - DSKTBT *****
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE *****
4 ; ABSTRACT - Quick test of floppy disk sector buffer and controller hardware *****
5 ; for power-up diagnostics. This version runs all the tests and *****
6 ; sets bits for each failed test *****
7 ; REGISTERS USED - AX, BL, CX, DX *****
8 ; STACK USED - 4 Bytes maximum *****
9 *****
10 NAME DSKTST *****
11 SUBTTL *****
12 DEBUO EQU OFFFH *****
13 SUBTTL EXTERNALS and PUBLICS *****
14 *****
15 PUBLIC DSKTST *****
16 EXTERN CRTTBT: NEAR ; The beginning of the CRT tests
17 EXTERN DECLED: NEAR ; Decrement diagnostic LED pattern
18 EXTERN DRVTBT: NEAR ; Checks for connected drives
19 EXTERN DSKINI: NEAR ; Disk driver initialization
20 EXTERN DSPDIE: NEAR ; Display and die routine
21 EXTERN E_CMD: ABS ; Command execution error
22 EXTERN E_EVEN: ABS ; Even-order bit in secbuf bad error
23 EXTERN E_ODD: ABS ; Odd-order bit in secbuf bad error
24 EXTERN E_REG: ABS ; FDC register bad error
25 EXTERN LED011: ABS ; Disk diagnostic LED pattern
26 EXTERN T6TPAT: BYTE ; Table of test patterns for regs.
27 SECT ROMDAT ;
28 EXTERN DSKC_M: BYTE ; RAM copy of disk control port
29 EXTERN DSKS_M: BYTE ; RAM copy of disk select port
30 SUBTTL Global equates *****
31 *****
32 ; INCLUDE PEG: PORTADDR.EQU *****
33 ; INCLUDE PEG: LATCHES.EQU *****
34 ; INCLUDE PEG: FLOPPY.EQU *****
35 *****
134 SUBTTL Local equates *****
135 *****
136 BSYMSK EQU 00000001B *****
137 C_STPN EQU 01000000B ; Busy bit mask for FDCSTA register
138 C_STOP EQU 11010000B ; Step-in/noupdate/unload/noverify/6
139 D0_324 EQU 10 ; Force interrupt/nointerrupts
140 D2_000 EQU 476 ; 32.4 usec delay count
141 D4_999 EQU 1191 ; 20.000 msec delay count
142 DSELECT EQU OFH ; 4.999B msec delay count
143 EVNMSK EQU 01010101B ; Deselect-all code
144 ODDMSK EQU 10101010B ; Even bits mask for secbuf errors
145 SBFSIZ EQU 1024 ; Odd bits mask for secbuf errors
146 SUBTTL Top level of power-up disk diagnostics ; Size of sector buffer in bytes
147 *****
148 SECT ROMCOD *****
149 ASSUME CS: ROMCOD, DS: ROMDAT *****
150 DSKTBT PROC NEAR *****
```



```

0000' EB 0015
0003' 08 D2
0005' 74 05
0007' 80 0A*
0009' E9 0003"
000C' E8 0003"
000F' E8 0004"
0012' E8 0002"
0015' E9 0001"

151 SBFTBT ; Perform floppy disk tests
152 DL,DL ; Q: Did any tests fail?
153 NOFAIL ; N: Continue DSKTBT
154 AL,LED011 ; AL := Disk diagnostic LED pattern
155 JMP DSPDIE ; Now, go display and die
156 NOFAIL: CALL DSKTBT ; See what drives we've got
157 CALL DSKINI ; Initialize disk driver
158 CALL DECLED ; Decrement diagnostic LED pattern
159 JMP CRTTST ; Continue with CRT test
160 ;*****
161 SUBTTL SBFTBT - Sector buffer test
162 ;*****
163 SBFTBT: MOV AL,DSKC_M ; AL := RAM copy of control port
164 AND AL,MMASK ; AL := buffer control bits only
165 OR AL,ACCBUF ; AL := set up for CPU (--) BUFFER
166 OUT DSKC_P,AL ; Hit control port
167 MOV DSKC_M,AL ; Update RAM copy
168
169
170 DX,SECBUF ; DX := Pointer to sector buffer
171 BP,010000000B ; BP := test pattern
172 BH,BH ; BH := Error bit accumulator
173
174 SBFT_LP: MOV AX,BP ; BP := Copy of test pattern
175 OUT RSTBUF,AL ; Reset secbuf counter
176 MOV CX,SBFSIZ-1 ; CX := secbuf size in bytes
177 ROR AX,1 ; Set the carry flag correctly
178 W_LOOP: OUT DX,AL ; write out the data
179 RCL AL,1 ; walk it
180 LOOP W_LOOP ; write to all of sector buffer
181 OUT RSTBUF,AL ; Reset sector buffer counter
182 MOV CX,SBFSIZ-1 ; CX := sector buffer size in bytes
183 ROR AX,BP ; Get starting test pattern
184 MOV AX,1 ; Set the carry flag correctly
185 MOV BL,AL ;
186 R_LOOP: IN AL,DX ; AL := data from sector buffer
187 LAHF ; Save carry flag
188 XOR AL,BL ; AL := bits in error (if any)
189 OR BH,AL ; BH := Accumulated bad bits.
190 SAHF ; Retrive carry flag.
191 RCL BL,1 ; Generate next test byte
192 LOOP R_LOOP ; continue till testing finished
193 XOR BP,OFFFHH ; test the complement
194 JS SBF_LP ; Q: Done with straight and comp?
195 ; N: Jump and do complement.
196 BP,1 ; Q: All nine loops?
197 JNB SBF_LP ; N: Jump and continue.
198 XOR DL,DL ; Clear error accumulator.
199 MOV AL,BH ; AH := copy of the bad bits
200 TEST AL,EVNMSK ; Q: Are any even-order bits bad?
201 JZ EVN_OK ; N: Go check the odd bits
202 OR DL,E_EVEN ; Y: Set even bit bad indicator
    
```

```

0065' AB AA      203      EVN_OK: TEST      AL,ODDMASK      ;
0067' 74 03      204      JZ              ODD_OK      ;
0069' 80 CA 0B#   205      OR              DL,E_ODD      ;
006C'             206      ODD_OK:             ;
;             207      JMP          SHORT REOTBT ;
;             208      ***** Then go test the disk controller *****
;             209      SUBTTL REOTBT - FDC register test *****
;             210      *****
;             211      REOTBT: *****
006C' A0 000C"    212      MOV          AL,DSKC_M      ;
006F' 2C 3F      213      AND          AL,MMASK      ;
0071' 0C 80      214      OR              AL,ACCFDC     ;
0073' E6 00      215      OUT          DSKC_P,AL     ;
0075' A2 000C"    216      MOV          DSKC_M,AL     ;
;             217      ;
007B' BE 000B"    218      MOV          SI,OFFSET TBTPAT ;
007B' B9 0004      219      MOV          CX,4          ;
;             220      ;
007E' 2E8A 04     221      MOV          AL,CB:BYTE PTR[B1] ;
0081' E6 21      222      OUT          FDCTRK,AL      ;
0083' 2E8A 44 01  223      MOV          AL,CB:BYTE PTR [B1] ;
0087' E6 22      224      OUT          FDCSEC,AL      ;
0089' 2E8A 44 02  225      MOV          AL,CB:BYTE PTR 2[B1] ;
008D' E6 23      226      OUT          FDCDAT,AL      ;
008F' E4 21      227      IN           AL,FDCTRK      ;
0091' 2E3A 04     228      CMP          AL,CS:BYTE PTR [B1] ;
0094' 75 15      229      JNE          REOBAD         ;
0096' E4 22      230      IN           AL,FDCSEC      ;
0098' 2E3A 44 01  231      CMP          AL,CB:BYTE PTR [B1] ;
009C' 75 0D      232      JNE          REOBAD         ;
009E' E4 23      233      IN           AL,FDCDAT      ;
00A0' 2E3A 44 02  234      CMP          AL,CB:BYTE PTR 2[B1] ;
00A4' 75 05      235      JNE          REOBAD         ;
00A6' 46          236      INC          SI              ;
00A7' E2 D5      237      LOOP         REG_LP         ;
00A9' EB 03      238      JMP          SHORT REO_OK     ;
;             239      ;
00AB' 80 CA 09#   240      REOBAD: OR          DL,E_REO     ;
;             241      ;
00AE'             242      REO_OK:             ;
;             243      JMP          CMDTBT      ;
;             244      ***** Then go test the disk controller *****
;             245      SUBTTL CMDTBT - FDC command test *****
;             246      *****
;             247      CMDTBT: *****
00AE'             248      ;
;             249      MOV          AL,DSELC      ;
;             250      OUT          DSKS_P,AL     ;
;             251      MOV          DSKS_M,AL     ;
;             252      MOV          AL,C_STOP     ;
00B0' 80 D0      253      MOV          AL,command to stop FDC
;             254      OUT          FDCCMD,AL     ;

```




```
1 ; *****
2 ; TITLE - FLPDIR
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE
4 ; ABSTRACT - Contains code which implements the Pegasus disk driver
5 ; Generic Disk Operations for the floppy disk controller:
6 ;
7 ; FLPMI - Force-interrupt (not used for floppy disk)
8 ; FLPIR - Initiate Read
9 ; FLPIW - Initiate Write
10 ; FLPVFC - Initiate Verify CRC
11 ; FLPVFD - Initiate Verify Data
12 ; FLPIB - Initiate Seek
13 ; FLPASP - Auxiliary State Processor
14 ; FLPEP - Error Processor
15 ; FLPRIS - Read and Interpret Status
16 ;
17 ; Also contains the following event entry points:
18 ;
19 ; FLTIME - General floppy disk timing event
20 ; MOTOFF - Floppy disk motor time-out handler
21 ;
22 ; And the following entry point called during power-up testing:
23 ;
24 ; FRCINT - Terminate FDC pending operation
25 ;
26 ; REGISTERS USED -
27 ;
28 ; STACK USED -
29 ;
30 ; *****
31 ; NAME FLPDIR
32 ; SUBTTL Floppy disk Generic Disk Operations
33 ; *****
34 ; The following fixes were made in version 1.01:
35 ;
36 ;
37 ; * Problem: MSDOS 0001 - "disk-RDM no error reported on DRQ low".
38 ; Solution: The "no data" error is now comprehended by FLPRB
39 ; * Problem: MSDOS 0003 - "disk-RDM Seek error reporting too global".
40 ; Solution: The error processor, FLPEP, now does a "read address" operation
41 ; when a "record not found" error occurs and uses the information gained
42 ; thereby to better define the actual problem/error.
43 ; * Problem: the "force-interrupt-on-next-index-mark" trick to determine if
44 ; media is present in the drive is not needed as the floppy disk controller
45 ; counts index marks during I/O and times out after 5 have passed (1 second
46 ; and returns "record not found" if the operation has not completed. That
47 ; logic doesn't work anyway.
48 ; Solution: That logic was removed to make room for the fix for MSDOS 0003
49 ; which correctly returns error codes for the original situation as well.
50 ; * disk driver not waiting for disk motors to stabilize.
51 ; The select logic, SELECT, has been modified to correctly determine the
52 ; motor's current status and initiate a delay if needed.
53 ; * Need the 30 msec head-settling delay after seek.
```




```
54 ; The seek logic, FLPIB, stores a mask to be used by the initiate I/O logic,  
55 ; INITIO, when issuing I/O commands to the floppy disk controller. INITIO  
56 ; clears this mask after using it so that the delay is induced only immedi-  
57 ; ately following seek operations.  
58 ; The following changes were made in version 1.22:  
59 ; * The motor off time has been reduced to 10 seconds and the select logic  
60 ; modified so that when an external drive is selected, the internal motors  
61 ; are not turned off. Motors are turned on as needed and turned off only  
62 ; when no activity on any drive has been detected for 10 seconds.  
63 ; * Some debug code was removed to make room for the changes.  
64 ; *****  
65 ; PUBLIC DEFINITIONS  
66 ; *****  
67 SECT ROMCOD *****  
68 PUBLIC FLPDIR ; Floppy disk Disk Interface Routine  
69 PUBLIC FLTIME ; Floppy timeout entry point  
70 PUBLIC FRCINT ; Terminate FDC function in progress  
71 PUBLIC MOTOFF ; Turn off all floppy drive motors  
72 ; *****  
73 ; EXTERNAL REFERENCES  
74 ; *****  
75 SECT ROMCOD *****  
76 EXTRN BOFINT:NEAR ; Software interrupt routine  
77 ; substituted for FLTIME  
78 EXTRN TIMOUT:NEAR ; The timeout subroutine  
79 ; *****  
80 SECT ROMDAT *****  
81 EXTRN D_EKNT:BYTE ; Disk driver error counter  
82 EXTRN D_NCMD:BYTE ; Disk driver next generic opcode  
83 EXTRN D_NSTA:BYTE ; Disk driver next state  
84 EXTRN D_REQ:BYTE ; Request Information Block  
85 EXTRN D_SOFT:BYTE ; Software interrupt flag for DSKISR  
86 EXTRN D_STAT:BYTE ; Disk driver current state  
87 EXTRN D_WKBP:BYTE ; Disk driver general workspace  
88 EXTRN DSKG_M:BYTE ; Memory image of floppy control port  
89 EXTRN DSKS_M:BYTE ; Memory image of floppy select port  
90 EXTRN FL1EVT:BYTE ; Floppy disk motor-off timing event  
91 EXTRN FL2EVT:BYTE ; Floppy disk general usage timing evt  
92 EXTRN INVALID:ABS ; Invalid position value  
93 ; *****  
94 ; PEGASUS INTERRUPT VECTORS  
95 ; *****  
96 INCLUDE PEG:VECTOR.EGU  
97 ; *****  
98 ; PEGASUS DISK HARDWARE CONSTANT DEFINITIONS  
99 ; *****  
100 INCLUDE PEG:FLPADR.EGU  
101 ; *****  
102 ; PEGASUS DISK HARDWARE CONSTANT DEFINITIONS  
103 ; *****  
104 ; I/O and interface control port mask used by INITIO  
105 ; IO_MSK EQU (not PRECMP) and (not SIDE_0) and MMASK  
106 ; *****  
107 ; FLOPPY DISK CONTROLLER DEFINITIONS  
108 ; *****  
224 ;
```

-0000

-0000

-0000

-000F

```
225 , INCLUDE PEO: FLPDMD.EGU  
226 ,  
227 ,  
228 FII EGU C_FOR or IMMEDI ; Force-Interrupt-Immediate command  
229 ,  
230 INTIDX EGU C_FOR or INDEXP ; Force-Interrupt on index pulse  
231 ,  
232 READ EGU C_RDB or T2SCHK ; Read with side-select check  
233 ,  
234 REBTOR EGU C_REB or T1BT06 ; Restore command  
235 ,  
236 , Seek + update + load + 6 msec  
237 , track head step  
238 , reg rate  
239 SEEK EGU C_SEE or T1UPTD or T1MLDD or T1BT06  
300 ,  
301 BTEPIN EGU C_BTI or T1BT06 ; Step-in command  
302 ,  
303 WRITE EGU C_WRB or T2SCHK ; Write with side-select check  
304 ,  
305 , STATE DEFINITIONS (GENERAL & FLOPPY)  
306 ,  
307 INCLUDE PEO: DSKSTA.EGU ; Include disk driver state equates  
308 ,  
309 B_DEP EGU 255 ; B_max =) Driver states  
310 B_IDLE EGU 0 ; Device-dependent code must set state  
311 B_WAIT EGU 1 ; Waiting for a request  
312 B_IBEEK EGU 2 ; Waiting to retry a request  
313 B_DONE EGU 3 ; Implicit seek pending  
314 B_MAX EGU 3 ; Operation completed  
315 , ; The highest "standard" state number  
316 , ; Other values are device-dependent  
317 B_FRX EGU B_MAX+1 ; Floppy read transfer state  
318 B_VFD EGU B_MAX+2 ; Verify data state  
319 B_REC1 EGU B_MAX+3 ; Attempting error recovery state  
320 B_RDA EGU B_MAX+4 ; Processing read address  
321 B_FMAX EGU B_MAX+4 ; The highest valued floppy disk state  
322 ,  
323 , DISK ERROR CODE DEFINITIONS  
324 ,  
325 , INCLUDE PEO: DSKERR.EGU  
326 ,  
327 ,  
328 ,  
329 ,  
330 ,  
331 ,  
332 ,  
333 ,  
334 , DIT STRUCTURE DEFINITION  
335 ,  
336 , INCLUDE PEO: DSKDIT.EGU  
337 ,  
338 ,  
339 ,  
340 ,  
341 ,  
342 ,  
343 ,  
344 ,  
345 ,  
346 ,  
347 ,  
348 ,  
349 ,  
350 ,  
351 ,  
352 ,  
353 ,  
354 ,  
355 ,  
356 ,  
357 ,  
358 ,  
359 ,  
360 ,  
361 ,  
362 ,  
363 ,  
364 ,  
365 ,  
366 ,  
367 ,  
368 ,  
369 ,  
370 ,  
371 ,  
372 ,  
373 ,  
374 ,  
375 ,  
376 ,  
377 ,  
378 ,  
379 ,  
380 ,  
381 ,  
382 ,  
383 ,  
384 ,  
385 ,  
386 ,  
387 ,  
388 ,  
389 ,  
390 ,  
391 ,  
392 ,  
393 ,  
394 ,  
395 ,  
396 ,  
397 ,  
398 ,  
399 ,  
400 ,  
401 ,  
402 ,  
403 ,  
404 ,  
405 ,  
406 ,  
407 ,  
408 ,  
409 ,  
410 ,  
411 ,  
412 ,  
413 ,  
414 ,  
415 ,  
416 ,  
417 ,  
418 ,  
419 ,  
420 ,  
421 ,  
422 ,  
423 ,  
424 ,  
425 ,  
426 ,  
427 ,  
428 ,  
429 ,  
430 ,  
431 ,  
432 ,  
433 ,  
434 ,  
435 ,  
436 ,  
437 ,  
438 ,  
439 ,  
440 ,  
441 ,  
442 ,  
443 ,  
444 ,  
445 ,  
446 ,  
447 ,  
448 ,  
449 ,  
450 ,  
451 ,  
452 ,  
453 ,  
454 ,  
455 ,  
456 ,  
457 ,  
458 ,  
459 ,  
460 ,  
461 ,  
462 ,  
463 ,  
464 ,  
465 ,  
466 ,  
467 ,  
468 ,  
469 ,  
470 ,  
471 ,  
472 ,  
473 ,  
474 ,  
475 ,  
476 ,  
477 ,  
478 ,  
479 ,  
480 ,  
481 ,  
482 ,  
483 ,  
484 ,  
485 ,  
486 ,  
487 ,  
488 ,  
489 ,  
490 ,  
491 ,  
492 ,  
493 ,  
494 ,  
495 ,  
496 ,  
497 ,  
498 ,  
499 ,  
500 ,  
501 ,  
502 ,  
503 ,  
504 ,  
505 ,  
506 ,  
507 ,  
508 ,  
509 ,  
510 ,  
511 ,  
512 ,  
513 ,  
514 ,  
515 ,  
516 ,  
517 ,  
518 ,  
519 ,  
520 ,  
521 ,  
522 ,  
523 ,  
524 ,  
525 ,  
526 ,  
527 ,  
528 ,  
529 ,  
530 ,  
531 ,  
532 ,  
533 ,  
534 ,  
535 ,  
536 ,  
537 ,  
538 ,  
539 ,  
540 ,  
541 ,  
542 ,  
543 ,  
544 ,  
545 ,  
546 ,  
547 ,  
548 ,  
549 ,  
550 ,  
551 ,  
552 ,  
553 ,  
554 ,  
555 ,  
556 ,  
557 ,  
558 ,  
559 ,  
560 ,  
561 ,  
562 ,  
563 ,  
564 ,  
565 ,  
566 ,  
567 ,  
568 ,  
569 ,  
570 ,  
571 ,  
572 ,  
573 ,  
574 ,  
575 ,  
576 ,  
577 ,  
578 ,  
579 ,  
580 ,  
581 ,  
582 ,  
583 ,  
584 ,  
585 ,  
586 ,  
587 ,  
588 ,  
589 ,  
590 ,  
591 ,  
592 ,  
593 ,  
594 ,  
595 ,  
596 ,  
597 ,  
598 ,  
599 ,  
600 ,  
601 ,  
602 ,  
603 ,  
604 ,  
605 ,  
606 ,  
607 ,  
608 ,  
609 ,  
610 ,  
611 ,  
612 ,  
613 ,  
614 ,  
615 ,  
616 ,  
617 ,  
618 ,  
619 ,  
620 ,  
621 ,  
622 ,  
623 ,  
624 ,  
625 ,  
626 ,  
627 ,  
628 ,  
629 ,  
630 ,  
631 ,  
632 ,  
633 ,  
634 ,  
635 ,  
636 ,  
637 ,  
638 ,  
639 ,  
640 ,  
641 ,  
642 ,  
643 ,  
644 ,  
645 ,  
646 ,  
647 ,  
648 ,  
649 ,  
650 ,  
651 ,  
652 ,  
653 ,  
654 ,  
655 ,  
656 ,  
657 ,  
658 ,  
659 ,  
660 ,  
661 ,  
662 ,  
663 ,  
664 ,  
665 ,  
666 ,  
667 ,  
668 ,  
669 ,  
670 ,  
671 ,  
672 ,  
673 ,  
674 ,  
675 ,  
676 ,  
677 ,  
678 ,  
679 ,  
680 ,  
681 ,  
682 ,  
683 ,  
684 ,  
685 ,  
686 ,  
687 ,  
688 ,  
689 ,  
690 ,  
691 ,  
692 ,  
693 ,  
694 ,  
695 ,  
696 ,  
697 ,  
698 ,  
699 ,  
700 ,  
701 ,  
702 ,  
703 ,  
704 ,  
705 ,  
706 ,  
707 ,  
708 ,  
709 ,  
710 ,  
711 ,  
712 ,  
713 ,  
714 ,  
715 ,  
716 ,  
717 ,  
718 ,  
719 ,  
720 ,  
721 ,  
722 ,  
723 ,  
724 ,  
725 ,  
726 ,  
727 ,  
728 ,  
729 ,  
730 ,  
731 ,  
732 ,  
733 ,  
734 ,  
735 ,  
736 ,  
737 ,  
738 ,  
739 ,  
740 ,  
741 ,  
742 ,  
743 ,  
744 ,  
745 ,  
746 ,  
747 ,  
748 ,  
749 ,  
750 ,  
751 ,  
752 ,  
753 ,  
754 ,  
755 ,  
756 ,  
757 ,  
758 ,  
759 ,  
760 ,  
761 ,  
762 ,  
763 ,  
764 ,  
765 ,  
766 ,  
767 ,  
768 ,  
769 ,  
770 ,  
771 ,  
772 ,  
773 ,  
774 ,  
775 ,  
776 ,  
777 ,  
778 ,  
779 ,  
780 ,  
781 ,  
782 ,  
783 ,  
784 ,  
785 ,  
786 ,  
787 ,  
788 ,  
789 ,  
790 ,  
791 ,  
792 ,  
793 ,  
794 ,  
795 ,  
796 ,  
797 ,  
798 ,  
799 ,  
800 ,  
801 ,  
802 ,  
803 ,  
804 ,  
805 ,  
806 ,  
807 ,  
808 ,  
809 ,  
810 ,  
811 ,  
812 ,  
813 ,  
814 ,  
815 ,  
816 ,  
817 ,  
818 ,  
819 ,  
820 ,  
821 ,  
822 ,  
823 ,  
824 ,  
825 ,  
826 ,  
827 ,  
828 ,  
829 ,  
830 ,  
831 ,  
832 ,  
833 ,  
834 ,  
835 ,  
836 ,  
837 ,  
838 ,  
839 ,  
840 ,  
841 ,  
842 ,  
843 ,  
844 ,  
845 ,  
846 ,  
847 ,  
848 ,  
849 ,  
850 ,  
851 ,  
852 ,  
853 ,  
854 ,  
855 ,  
856 ,  
857 ,  
858 ,  
859 ,  
860 ,  
861 ,  
862 ,  
863 ,  
864 ,  
865 ,  
866 ,  
867 ,  
868 ,  
869 ,  
870 ,  
871 ,  
872 ,  
873 ,  
874 ,  
875 ,  
876 ,  
877 ,  
878 ,  
879 ,  
880 ,  
881 ,  
882 ,  
883 ,  
884 ,  
885 ,  
886 ,  
887 ,  
888 ,  
889 ,  
890 ,  
891 ,  
892 ,  
893 ,  
894 ,  
895 ,  
896 ,  
897 ,  
898 ,  
899 ,  
900 ,  
901 ,  
902 ,  
903 ,  
904 ,  
905 ,  
906 ,  
907 ,  
908 ,  
909 ,  
910 ,  
911 ,  
912 ,  
913 ,  
914 ,  
915 ,  
916 ,  
917 ,  
918 ,  
919 ,  
920 ,  
921 ,  
922 ,  
923 ,  
924 ,  
925 ,  
926 ,  
927 ,  
928 ,  
929 ,  
930 ,  
931 ,  
932 ,  
933 ,  
934 ,  
935 ,  
936 ,  
937 ,  
938 ,  
939 ,  
940 ,  
941 ,  
942 ,  
943 ,  
944 ,  
945 ,  
946 ,  
947 ,  
948 ,  
949 ,  
950 ,  
951 ,  
952 ,  
953 ,  
954 ,  
955 ,  
956 ,  
957 ,  
958 ,  
959 ,  
960 ,  
961 ,  
962 ,  
963 ,  
964 ,  
965 ,  
966 ,  
967 ,  
968 ,  
969 ,  
970 ,  
971 ,  
972 ,  
973 ,  
974 ,  
975 ,  
976 ,  
977 ,  
978 ,  
979 ,  
980 ,  
981 ,  
982 ,  
983 ,  
984 ,  
985 ,  
986 ,  
987 ,  
988 ,  
989 ,  
990 ,  
991 ,  
992 ,  
993 ,  
994 ,  
995 ,  
996 ,  
997 ,  
998 ,  
999 ,  
1000 ,
```

```

407 ; The REPFAC constant is used for generating code in the loops which transfer
408 ; data to and from the sector buffer. "Loop packing" means that the contents of
409 ; the loop are repeated so that the loop count may be reduced. Loop overhead is
410 ; reduced, speeding up the operation overall.
411 ;
412 ; Packing factors are here expressed as the number of bits to adjust the sector
413 ; size (IN WORDS) for use as the loop counter in the data transfer loop.
414 ;
415 ; The number of times the data move operations is repeated within the data
416 ; transfer loop is called the repeat count. This quantity doubles for each bit
417 ; position the sector size count is shifted left. Therefore, the relationship
418 ; is: repeat count = 2 ^ packing factor.
419 ;
420 ; NOTE: sector size is stored in bytes, but data transfers are all in words.
421 ; Therefore, all figures are adjusted to work with word operations.
422 ;
423 ; The following table shows relative performance of various packing factors.
424 ; The single-byte data transfer loop figures are included as the baseline
425 ; performance reference.
426 ;
427 ; PACKING DATA CLOCK RELATIVE RELATIVE LOOP
428 ; FACTOR TRANSFERED CYCLES TIME PERFORMANCE SIZE
429 ;
430 ; 0.5 1 BYTE 26112 100.0% 1.00 7 BYTES
431 ; 1 1 WORD 11520 44.1% 2.26 4 BYTES
432 ; 2 2 WORDS 9344 35.8% 2.79 6 BYTES
433 ; 3 4 WORDS 8256 31.6% 3.16 10 BYTES
434 ; 4 8 WORDS 7712 29.5% 3.39 18 BYTES
435 ; 5 16 WORDS 7440 28.5% 3.51 34 BYTES
436 ; 6 32 WORDS 7304 27.8% 3.59 66 BYTES
437 ; 7 64 WORDS 7236 27.7% 3.61 133 BYTES *
438 ;
439 ; * - loop must have has different terminating instruction sequence & requires
440 ; a slightly different initialization (one more register) but does not
441 ; incur any more time overhead in the loop than with the other examples.
442 ;
443 ; *****
444 REPFAC EQU 4 ; Packing factor
445 REPNT EQU 1 shl (REPFAC-1) ; Repeat count
446 ; *****
447 ; RIB STRUCTURE DEFINITION
448 ; INCLUDE PEO:DSKRIB.EQU
449 ; *****
450 ; TIMING EVENT DEFINITIONS
451 ; INCLUDE PEO:TIMEVT.EQU
452 ; *****
453 ; TIMING EVENT OFFSETS
454 ; IEVLNK EQU 00H ; Link pointer
455 ; IEVTID EQU 02H ; Task id

```

-0004
 -0008

-0000
 -0002

```

-0003 474 IEVFLO EQU 03H ; Flags
-0004 475 IEVCTR EQU 04H ; Counter
-0006 476 IEVSUB EQU 06H ; Subroutine address
477 ;
478 ; Event flags
479 ;
480 EVTTYP EQU 4 ; Event type
-0004 481 EVTACK EQU 5 ; Event acknowledged 0/1 = Yes/No
-0006 482 EVTEXP EQU 6 ; Event expired 0/1 = No/Yes
-0007 483 EVTSTA EQU 7 ; Event status 0/1 = Inactive/Active
484
-0080 *****
-000C 485 ACTEVT EQU 1 shl EVTSTA ; Mask for setting bit to activate evnt
-0014 486 INDXTI EQU 12 ; Index pulse wait interval = 300 msec
-0190 487 MONTIM EQU 20 ; Disk motor-on wait = 1/2 sec
488 MOFTIM EQU 400 ; Motor-off timeout interval = 10 sec
489 *****
490 ; WORKSPACE OFFSETS
491 ;
492 *****
493 F_LCMD EQU 0 ; For saving last FDC command issued
494 F_DELAY EQU 1 ; For saving delay mask after seek
495 *****
496 ; MACROB
497 *****
498 EVENT MACRO ZEVT,XTIM,ZWHERE ; Start an event
499 IFNB
500 MOV word ptr ZEVT+IEVSUB,offset ZWHERE
501 ENDF
502 MOV word ptr ZEVT+IEVCTR,XTIM
503 OR ZEVT+IEVFLO,ACTEVT
504 ENDM
505 *****
506 MILEVT MACRO ZEVT ; Cancel an active event
507 AND ZEVT+IEVFLO,not ACTEVT
508 ENDM
509 *****
510 ; BEGINNING OF THE CODE AREA
511 *****
512 SECT ROMCOD
513 ASSUME CS:ROMCOD,DS:ROMDAT,ES:NOTHING
514 SUBTTL Floppy disk GDD routine: FLPDIR
515 *****
516 ; NAME: FLPDIR - Floppy Disk Interface Routine
517 ; ABSTRACT: This is the call interface to the floppy disk General Disk
518 ; Operation routines.
519 ; INPUTB: * AL = function code (always valid)
520 ; * SB: BP = @ the disk driver data area
521 ; PROCESSING: * Transfer control to the appropriate Generic Disk Operation
522 ; REGISTERS: AX plus whatever transferred routine uses
523 ; STACK USED: Whatever the called routine uses (up to about 12 bytes)
524 *****
525 FLPDIR PROC FAR

```

```
0000' 98          CBW          , Convert generic opcode to word
0001' 01 C0      AX,AX        , Convert generic opcode to index
0003' 88 F0      MOV SI,AX    , SI := jump table index
0005' 2EFF A4 000A" JMP CB:CMDBTBL[SI]    , Branch to routine
*****
000A'          CMTDBL LABEL WORD , Generic disk operations jump table
000A' 0138"     DW FLPFI      , Vector: Force interrupt
000C' 01E8"     DW FLPASP     , Vector: Auxiliary state processor
000E' 025D"     DW FLPEP     , Vector: Error processor
0010' 01D1"     DW FLPIR     , Vector: Initiate read
0012' 013C"     DW FLPIS     , Vector: Initiate seek
0014' 015D"     DW FLPIM     , Vector: Initiate write
0016' 01D6"     DW FLPVFC    , Vector: Verify CRC
0018' 01DC"     DW FLPVFD    , Vector: Verify data
001A' 01BF"     DW FLPRS     , Vector: Read status
001C' 029E"     DW FLPBGN    , Vector: Request termination
001E' 0287"     DW FLPTER    , Vector: Terminate floppy request
*****
0020' 80 80      SUBTTL Floppy disk internal GDD routine: CPUxxx (MODIFY INTERFACE CTL)
0022' EB 02      *****
0024' 80 00      *****
*****
0026' B4 3F      CPUFDC PROC NEAR
0028' EB 0E      MOV AL,ACCFDC , AH := bit pattern for CPU (--) FDC
*****
0024' 80 00      JMP short ptr CHGMOD , Go change interface mode
*****
0024' 80 00      CPUBUF PROC NEAR
*****
0024' 80 00      MOV AL,ACCBUF , AH := bit pattern for CPU (--) BUF
*****
0024' 80 00      JMP short ptr CHGMOD , Go change interface mode
*****
0026' B4 3F      CHGMOD: MOV AH,MMASK , AH := interface mode bit mask
0028' EB 0E      JMP short ptr HITC_P , Go change interface control port
*****
0024' 80 00      *****
0024' 80 00      SUBTTL Floppy disk internal GDD routine: FLTIME
*****
0026' B4 3F      *****
0028' EB 0E      FLTIME is called by the system timing event routine (driven by the
system clock) when a general floppy timing event (associated with the event
data structure labeled: FL2EVT) expires. The two floppy timing events handled
are: the wait for a motor to come up to speed & the wait for an interrupt on
an index pulse when processing a timeout error.
*****
0024' 80 00      PROCESSING: * Interrupt into the disk interrupt service routine
*****
0026' B4 3F      REGISTERS USED: none
0028' EB 0E      STACK USED: 6 bytes
*****
0024' 80 00      FLTIME PROC NEAR
0026' B4 3F      INC D_SOFT , Make software interrupt flag non-0
0028' EB 0E      *****
*****
```

```

578 ; INT IR6INT ; Invoke DSKISR
579 ; RET ; Then return
580 ; NOTE: these timing events are now vectored directly to the routine 80FINT
581 ; found in the module, DSKID which does exactly what you see here.
582 ; *****
583 SUBTTL Floppy disk internal ODD routine: FRCINT
584 ; *****
585 PUBLIC/INTERNAL ROUTINE
586 ; NAME: FRCINT - Force interrupt
587 ; ABSTRACT: Clear the floppy disk controller to an idle condition.
588 ; PROCESSING: * Send a "force interrupt" with no options to the FDC
589 ; * Impose a short delay for the command to take effect
590 ; * Interface made set to CPU (--) FDC
591 ; OUTPUT: AX
592 ; REGISTERS USED: AX
593 ; STACK USED: 6 bytes
594 ; NOTES: This routine is misnamed because it is not intended to generate
595 ; an interrupt even though it does use a "force interrupt" com-
596 ; mand. In fact, "force interrupt" with no options does not cause
597 ; the FDC to interrupt.
598 ; *****
599 FRCINT PROC NEAR
600 CALL CPUFDC ; Set CPU (--) FDC
601 MOV AL,C_FOR ; AL := force interrupt command
602 OUT FDCCMD,AL ; Tell FDC to do that
603 MOV AL,10 ; Delay after forced interrupt
604 DEC AL ; Does not affect carry
605 JNZ INTDLY
606 RET ; And return
607 ; *****
608 SUBTTL Floppy disk internal ODD routine: HIRC_P
609 ; *****
610 ; NAME: HIRC_P - Change interface control port
611 ; ABSTRACT: Change floppy disk interface control port to new settings
612 ; * Disable interrupts for exclusive access (required because
613 ; the port is shared - i.e., some bits are used for other
614 ; hardware than the floppy disk interface)
615 ; * Create new pattern for interface control port using inputs
616 ; * Duput new pattern to the control port
617 ; * Update RAM copy of the port
618 ; * AL = new bit fields for interface control port
619 ; * AH = bit mask for masking out fields to be changed
620 ; * [DSKC_M] = Updated control port image
621 ; * Interface control port updated as requested
622 ; REGISTERS USED: AX
623 ; STACK USED: 4 bytes
624 ; NOTE: "177" comment delimiters indicate interrupts may be either on or off
625 ; *****
626 HIRC_P PROC NEAR
627 PUSHF
628 CLI
629 AND AH,DSKC_M
630 OR AL,AH
631 ; 177 Save flags (esp. interrupt flag)
632 ; 177 Critical code, disable interrupts
633 ; 177 AH := control port less new bits
634 ; 177 AL := updated port image

```

002A' EB FFF3
 002D' B0 D0
 002F' E6 20
 0031' B0 0A
 0033' FE C8
 0035' 75 FC
 0037' C3

003B' 9C
 0039' FA
 003A' 22 26 000A"
 003E' 08 E0

```

0040' A2 000A"
0043' E6 00
0045' 9D
0046' C3
630 DBKC_M,AL
631 OUT
632 POPF
633 RET
634
635
636
637
638
639
640
641
642
643
644
645
646
647
648
649
650
651
652
653
654
655
656
657
658
659
660
661
662
663
664
665
666
667
668
669
670
671
672
673
674
675
676
677
678
679
680
681

; ; ; Update control port image
; ; ; Hit control port
; ; ; Restore flags (incl. interrupt)
; ; ; And return
; ; ;
; ; ; Floppy disk internal ODD routine: HITB_P
; ; ;
; ; ; NAME: HITB_P - Program select and motor control port
; ; ; ABSTRACT: Write new pattern to select and motor control port
; ; ; PROCESSING: * Output supplied pattern to the select and motor control port
; ; ; * Save a copy of that pattern in the memory image copy of port
; ; ; * [DBKC_M] = Updated control and motor control control port
; ; ; * Interface control port updated as requested
; ; ; none
; ; ; REGISTERS: 2 bytes
; ; ; STACK USED:
; ; ; NOTES: Unlike HITC_P, the select and motor control port is not shared.
; ; ; Therefore, there is no need to protect accesses to it by dis-
; ; ; abling interrupts.
; ; ;
; ; ; NAME: NEAR
; ; ; ABSTRACT: MOV DBKC_M,AL ; Update select port image
; ; ; PROCESSING: OUT DBKC_P,AL ; Hit select port
; ; ; * ; And return
; ; ;
; ; ; NAME: SUBTTL Floppy disk internal ODD routine: HITFDC (ISSUE FDC COMMAND)
; ; ; ABSTRACT: HITFDC - Issue FDC (Floppy Disk Controller) command
; ; ; PROCESSING: Send a command to the floppy disk controller
; ; ; * Output command to FDC command register
; ; ; * Store that command as last one issued
; ; ; * AL = FDC command to issue
; ; ; * Interface mode MUST BE: CPU (--) FDC
; ; ; * [D_WKSP+F_LCMD] (byte) = last FDC command issued
; ; ; none
; ; ; REGISTERS: 2 bytes
; ; ; STACK USED:
; ; ; NAME: NEAR
; ; ; ABSTRACT: MOV D_WKSP+F_LCMD,AL
; ; ; PROCESSING:
; ; ; *
; ; ; NAME: SUBTTL Floppy disk internal ODD routine: INITIO
; ; ; ABSTRACT: INITIO - Initiate I/O
; ; ; PROCESSING: Performs processing to initiate an I/O operation
; ; ; * Select disk drive
; ; ; * Program interface and floppy disk controller for the request-
; ; ; ed I/O
; ; ; INPUTS: * CH = I/O opcode (read or write) to execute
; ; ; * CL = Next state to enter
; ; ; * DH = Appropriate interface mode control bits:

```

```

682 ,
683 ,
684 ,
685 ,
686 ,
687 ,
688 ,
689 ,
690 ,
691 ,
692 ,
693 ,
694 ,
695 ,
696 ,
697 ,
698 ,
699 ,
700 ,
701 ,
702 ,
703 ,
704 ,
705 ,
706 ,
707 ,
708 ,
709 ,
710 ,
711 ,
712 ,
713 ,
714 ,
715 ,
716 ,
717 ,
718 ,
719 ,
720 ,
721 ,
722 ,
723 ,
724 ,
725 ,
726 ,
727 ,
728 ,
729 ,
730 ,
731 ,
732 ,
733 ,

FDC --) BUF for READ
BUF --) FDC for WRITE
* EB:BX = @ DIT for the current disk drive
* [D_REG+CYL] (byte) = desired cylinder for the I/O
* [D_REG+TRK] (byte) = desired track for the I/O
* [D_REG+SEC] (byte) = desired sector for the I/O
* [D_NSTA] (byte) = Disk driver's next state
AX, BX, CX, DX, SI
8 bytes
*****
INITRD PROC
MOV CH, READ
MOV DH, FDCBUF
*****
INITIO:
CALL CPUBUF
OUT RBIBUF, AL
CALL SELECT
*****
INIRET
AL, D_REG+CYL
FDC, TRK, AL
AL, EB: DITPRC[BX]
NOPRCM
DH, PRECMP
DH, BIDE_0
AL, D_REG+TRK
AL, 1
NOPRDI
AND DH, not SIDE_0
OR CH, T2CHK1
NOBIDI: MOV AL, D_REG+SEC
OUT FDCSEC, AL
MOV AL, CH
OR AL, D_WKSP+F_DELAY
CLI
CALL HITFDC
MOV AL, DH
MOV AH, ID_MSBK
CALL HITC_P
STI
MOV D_NSTA, CL
MOV D_WKSP+F_DELAY, 0
INIRET: RET
*****
SUBTTL Floppy disk internal ODD routine: MOTOFF
*****
PUBLIC ROUTINE
*****
NAME: MOTOFF - Floppy motor off
ABSTRACT: Called by the system timing event routine (driven by the timer)
to turn off running drive motors after a period of non-use.
PROCESSING: * Turn off all floppy disk drive motors and deselect all drives

```



```

734 ; INPUT: none
735 ; OUTPUT: none
736 ; REGISTERS: none
737 ; STACK USED: 6 bytes
738 ;*****
739 MOTOFF PROC NEAR
740     AX
741     MOV     AL,DRVMSK
742     CALL   HITS_P
743+    WILEVT FLIEVT
744     POP     AX
745     RET
746
747 ;*****
748 SUBTTL Floppy disk internal GDO routine: SELECT
749 ;*****
750 BELTBL DB     DRIV_0 or MOTR_0
751         DB     DRIV_1 or MOTR_1
752         DB     DRIV_2 or MOTR_2
753         DB     DRIV_3 or MOTR_3
754 ;*****
755 ; NAME:
756 ; ABSTRACT:
757 ; PROCESSING:
758 ;
759 ;
760 ;
761 ;
762 ;
763 ; INPUT:
764 ;
765 ; OUTPUT:
766 ;
767 ;
768 ;
769 ;
770 ;
771 ; REGISTERS: AX
772 ; STACK USED: 6 bytes
773 ;*****
774 SELECT PROC NEAR
775     CALL   CPUFDC
776     PUSH  BX
777     MOV     BL,D_REG+UNIT
778     XOR     BH,BH
779     MOV     AL,DSKS_M
780     AND     AL,not MOTMSK
781     MOV     DL,AL
782     OR      AL,SELTBL[BX]
783     CALL   HITS_P
784     AND     AL,not MOTMSK
785     XOR     AL,DL
786     JZ      SELRET
787
788 ;*****
789 ;*****
790 ;*****
791 ;*****
792 ;*****
793 ;*****
794 ;*****
795 ;*****
796 ;*****
797 ;*****
798 ;*****
799 ;*****
800 ;*****
801 ;*****
802 ;*****
803 ;*****
804 ;*****
805 ;*****
806 ;*****
807 ;*****
808 ;*****
809 ;*****
810 ;*****
811 ;*****
812 ;*****
813 ;*****
814 ;*****
815 ;*****
816 ;*****
817 ;*****
818 ;*****
819 ;*****
820 ;*****
821 ;*****
822 ;*****
823 ;*****
824 ;*****
825 ;*****
826 ;*****
827 ;*****
828 ;*****
829 ;*****
830 ;*****
831 ;*****
832 ;*****
833 ;*****
834 ;*****
835 ;*****
836 ;*****
837 ;*****
838 ;*****
839 ;*****
840 ;*****
841 ;*****
842 ;*****
843 ;*****
844 ;*****
845 ;*****
846 ;*****
847 ;*****
848 ;*****
849 ;*****
850 ;*****
851 ;*****
852 ;*****
853 ;*****
854 ;*****
855 ;*****
856 ;*****
857 ;*****
858 ;*****
859 ;*****
860 ;*****
861 ;*****
862 ;*****
863 ;*****
864 ;*****
865 ;*****
866 ;*****
867 ;*****
868 ;*****
869 ;*****
870 ;*****
871 ;*****
872 ;*****
873 ;*****
874 ;*****
875 ;*****
876 ;*****
877 ;*****
878 ;*****
879 ;*****
880 ;*****
881 ;*****
882 ;*****
883 ;*****
884 ;*****
885 ;*****
886 ;*****
887 ;*****
888 ;*****
889 ;*****
890 ;*****
891 ;*****
892 ;*****
893 ;*****
894 ;*****
895 ;*****
896 ;*****
897 ;*****
898 ;*****
899 ;*****
900 ;*****
901 ;*****
902 ;*****
903 ;*****
904 ;*****
905 ;*****
906 ;*****
907 ;*****
908 ;*****
909 ;*****
910 ;*****
911 ;*****
912 ;*****
913 ;*****
914 ;*****
915 ;*****
916 ;*****
917 ;*****
918 ;*****
919 ;*****
920 ;*****
921 ;*****
922 ;*****
923 ;*****
924 ;*****
925 ;*****
926 ;*****
927 ;*****
928 ;*****
929 ;*****
930 ;*****
931 ;*****
932 ;*****
933 ;*****
934 ;*****
935 ;*****
936 ;*****
937 ;*****
938 ;*****
939 ;*****
940 ;*****
941 ;*****
942 ;*****
943 ;*****
944 ;*****
945 ;*****
946 ;*****
947 ;*****
948 ;*****
949 ;*****
950 ;*****
951 ;*****
952 ;*****
953 ;*****
954 ;*****
955 ;*****
956 ;*****
957 ;*****
958 ;*****
959 ;*****
960 ;*****
961 ;*****
962 ;*****
963 ;*****
964 ;*****
965 ;*****
966 ;*****
967 ;*****
968 ;*****
969 ;*****
970 ;*****
971 ;*****
972 ;*****
973 ;*****
974 ;*****
975 ;*****
976 ;*****
977 ;*****
978 ;*****
979 ;*****
980 ;*****
981 ;*****
982 ;*****
983 ;*****
984 ;*****
985 ;*****
986 ;*****
987 ;*****
988 ;*****
989 ;*****
990 ;*****
991 ;*****
992 ;*****
993 ;*****
994 ;*****
995 ;*****
996 ;*****
997 ;*****
998 ;*****
999 ;*****
1000 ;*****
    
```

Save AX register
 AL := all motors off bit pattern
 Change select & motor control port
 Cancel motor-off event
 Restore AX
 Floppy disk internal GDO routine: SELECT
 Unit number select bit table
 Select disk drive
 Prepare floppy disk interface for accessing a particular drive
 Select disk drive and turn on its motor
 If the motor was not previously running:
 - Initiates a wait for the motor to come up to speed
 - Set driver state to B_WAIT which will cause the request to be re-issued when the "motor-on wait" timing event expires
 - Return an indication that the request must wait
 [D_REG+UNIT] (byte) = Pegasus unit number of unit to select
 CH,CL,DH,EB:BX - Registers which must be preserved
 CF = 0 =) motor was running, 1 =) motor was not running
 If the motor was not running:
 [D_NCMDJ] = OP_NUL (do nothing command)
 [D_NSTAJ] = B_MOT (motor wait)
 Interface mode is set to CPU (--) FDC
 AX
 6 bytes
 NEAR
 CPUFDC
 BX
 BL,D_REG+UNIT
 BH,BH
 AL,DSKS_M
 AL,not MOTMSK
 DL,AL
 AL,SELTBL[BX]
 HITS_P
 AL,not MOTMSK
 AL,DL
 SELRET
 JZ
 Set interface mode to CPU (--) FDC
 Save BX
 BL := unit number
 Convert it to word for use as index
 Mask out all but the motor data
 DL := image of select & motor port
 AL := New select and motor bits
 Change select & motor control port
 Mask out all but the motor data
 Q: Has the motor already on?
 Y: Then we are ready to go

```

00D0' C6 06 0009" 01
00D5' C6 06 0004" FF
00E5' F9
00E6' 9B
00E7' C3

787 MOV D_NSTA,B_WAIT ; N: Next state := general wait
788 MOV D_NCMD,OP_NUL ; Change next operation to null
789+ FL2EVT,MONTIM ; Start event for motor-on wait
795 BELRET: POP BX ; Restore BX
797 RET ; And return
798
799 SUBTTL Floppy disk internal ODD routine: VERIFY
800
801 ; NAME: VERIFY - Verify data
802 ; ABSTRACT: Compares data in the sector buffer with memory data
803 ; PROCESSING: * Compare the user's data with the disk data in sector buffer
804 ; INPUT: * [D_REG+BUF] (dword) = segment:offset for user's buffer
805 ; * EB: BX = & DIT for the current disk drive
806 ; OUTPUT: * If a verify failure occurs:
807 ; - [D_REG+STAT] (byte) = "DKVFE" (Verify error)
808 ; - [D_REG+BUF] (dword) = address of WORD which doesn't match
809 ; REGISTER: AX, BX, CX, DX, SI
810 ; STACK USED: 6 bytes
811 ; NOTES: Unlike the read and write data transfers, VERIFY #does not# use
812 ; the loop packing method to speed up sector buffer accesses.
813 ; This is for the sake of simplicity. Especially since the CMPB
814 ; (compare string) instruction cannot be used in the same simple
815 ; manner as STOS or LODS and, therefore, was not used.
816
817 VERIFY PROC NEAR
818 CALL CPUBUF ; Set interface mode to CPU (--) BUF
819 OUT RSTBUF.AL ; Reset sector buffer counter
820 MOV CX,EB: DITBEC[BX] ; CX := Sector length in bytes
821 SHR CX,1 ; Adjust for word access to sechuf
822 MOV DX,SECBUF ; DX := Address of sector buffer port
823 PUSH DB ; Save current DS
824 LDB SI,dword ptr D_REG+BUF ; DS:SI := @ user buffer
825
826 AX,DX ; AX := 1 word from sector buffer
827 CMP AX,word ptr DS:[B1] ; Q: Does the data match?
828 JNE NO_VFY ; N: Go fail
829 INC SI ; Y: Keep checking until done
830 INC SI ;
831 LOOP VRL00P ; Restore DS
832 POP DB ; And return
833 RET ;
834
835 NO_VFY: POP DB ; Restore DS
836 MOV word ptr D_REG+BUF+0,B1 ; Store address of bad word
837 MOV D_REG+STAT,DKVFE ; Set status to failure
838 RET ; And return
839
840 SUBTTL Floppy disk internal ODD routine: XFRRDD
841
842 ; NAME: XFRRDD - Transfer read data
843 ; ABSTRACT: Called to unload the contents of the sector buffer into memory.

```

```

844 ; PROCESSING:
845 ; INPUT:
846 ;
847 ; OUTPUT:
848 ; REGISTERS: AX, BX, CX, DX, DI
849 ; STACK USED: 8 bytes
850 ; NOTES: This routine employs the loop packing technique discussed
851 ; earlier to speed up transferring data from the sector buffer
852 ; to memory.
853 ;*****
854 XFRRDD PROC
855 CALL CPUBUF
856 OUT RSTBUF,AL
857 MOV AX,ES:DITSEC[BX]
858 MOV CL,REFFAC
859 SHR AX,CL
860 MOV CX,AX
861 MOV DX,SECBUF
862 PUSH ES
863 LES DI,dword ptr D_REG+BUF
864
865 RDLOOP: REPT
866 IN AX,DX
867 STOB word ptr ES:[DI]
868 ENDR
869 LOOP RDLOOP
870
871 POP ES
872 RET
873
874 ;*****
875 SUBTTL Floppy disk QDD routine: FLPMI
876 ; NAME: FLPMI - Force Interrupt
877 ; ABSTRACT: Performs the processing needed to cause the disk controller to
878 ; send an interrupt request.
879 ; PROCESSING: * Send a "force interrupt immediate" to the disk controller
880 ; INPUT: none
881 ; OUTPUT: * [D_WKSP+F_LCMD] (byte) = Last command sent to FDC
882 ; REGISTERS: AX
883 ; STACK USED: 6 bytes
884 ; NOTES: I found that the "force interrupt" function was not needed for
885 ; driving the floppy disk, so this code has been omitted to save
886 ; space in the system ROM, which is CRITICALLY short of space.
887 ; If this function is somehow called inadvertently, it will
888 ; return immediately.
889 ;*****
890 FLPMI EQU
891 CALL CPUFDC
892 MOV AL,FII
893 CALL HITFDC
894 RET
895
896 ;*****
897 RETINS
898 EQUATE to location of a return
899 ; Set interface mode to CPU (--) FDC
900 ; AL := Force-interrupt-immediate
901 ; Hit FDC command register with that
902 ; And return
903 ;*****
904
905
906
907
908
909
910
911
-013B
```

```

912 SUBTTL Floppy disk GDO routine: FLP1B
913 *****
914 ; NAME: FLP1B - Initiate Beek
915 ; ABSTRACT: Performs the processing needed to seek a particular cylinder on
916 ; a disk. Also processes the "disk system reset" condition indi-
917 ; cated by the "INVALID" (-1) current position for the disk.
918 ; PROCESBINO: * Call SELECT to:
919 ; - Select the desired disk drive
920 ; - If the drive motor isn't on:
921 ; - Turn it on
922 ; - Initiate a wait for it to come up to speed
923 ; * If the current position is "INVALID", initiate a recalibrate
924 ; operation before allowing the seek to begin
925 ; * Initiate a seek to the desired cylinder
926 ; * [D_REG+CYL] (byte) = desired cylinder for the seek
927 ; * [D_NSTAJ] (byte) = Next state for disk driver (B_WAIT, B_REC1,
928 ; or "unchanged" (B_IBEEK))
929 ; * [D_WKBP+F_LCMD] (byte) = Last command sent to FDC
930 ; * [D_WKBP+F_DELAY] (byte) = 30 msec delay mask for next I/O
931 ; AX, BX
932 ; REGISTERS:
933 ; STACK USED: 8 bytes
934 *****
935 FLP1B PROC
936 CALL BELECT
937 ; Select disk & insure motor on
938 ; Interface mode is CPU (--) FDC
939 ; If the motor wasn't on, wait for it
940 ; AL := Current cylinder
941 ; G: Was disk system reset?
942 ; Y: Go reset & recalibrate
943 ; Set track register to that
944 ; AL := Desired cylinder
945 ; Select cylinder (FDC "track" )
946 ; AL := Beek command with options
947 ; Hit command port with that
948 ; Set 30 msec delay mask for next I/O
949 ; And return
950 ;
951 ;
952 ; Go recalibrate
953 *****
954 SUBTTL Floppy disk GDO routine: FLP1B
955 *****
956 ; NAME: FLP1B - Initiate Write
957 ; ABSTRACT: Perform the processing needed to write data to the disk
958 ; PROCESBINO: * Transfer the data to the sector buffer
959 ; * Set parameters and call INITIO to initiate a write operation
960 ; * ES: BX = @ DIT for the current disk drive
961 ; * [D_REG+BUF] (dword) = segment:offset for user's buffer
962 ; * [D_REG+CYL] (byte) = desired cylinder for the I/O
963 ; * [D_REG+TRK] (byte) = desired track for the I/O
964 ; * [D_REG+SEC] (byte) = desired sector for the I/O
965 ; * [D_NSTAJ] (byte) = next state for driver (B_WAIT or B_DONE)
966 ; AX, BX, CX, DX, SI
967 ; REGISTERS:
968 ;
969 ;
970 ;
971 ;
972 ;
973 ;
974 ;
975 ;
976 ;
977 ;
978 ;
979 ;
980 ;
981 ;
982 ;
983 ;
984 ;
985 ;
986 ;
987 ;
988 ;
989 ;
990 ;
991 ;
992 ;
993 ;
994 ;
995 ;
996 ;
997 ;
998 ;
999 ;
1000 ;
    
```

```

964 ; STACK USED: 10 bytes
965 ; NOTES: This routine employs the loop packing discussed earlier to
966 ; speed up transferring data between memory & the sector buffer.
967 ; *****
968 FLPIN PROC FAR
969 CALL CPUBUF ; Set interface mode to CPU (--) BUF
970 OUT RSTBUF,AL ; Reset sector buffer counter
971 MOV AX,ES:DITSEC(BX) ; AX := Sector length in bytes
972 MOV CL,REPFAC ; Adjust for loop packing and word...
973 SHR AX,CL ; access to the sector buffer
974 MOV CX,AX ; CX := Repeat count for output loop
975 MOV DX,SECBUF ; DX := Address of sector buffer port
976 PUSH DS ; Save current data segment register
977 LDS SI,dword ptr D_REG+BUF ; DS:SI := @ user buffer
978
979 IMLOOP: REPT -000B ; Pack the output loop
980 LODS word ptr DS:[SI] ; AX := Word from user's buffer
981 OUT DX,AX ; Output that word to sector buffer
982 ENDR ; End of repeated section
983 LOOP IMLOOP ; Loop until the buffer is written
984
985 POP DS ; Restore data segment register
986
987 MOV CH,WRITE ; CH := Write command with options
988 MOV CL,B_DONE ; CL := next state (done)
989 MOV CX,B_DONE or (WRITE shl 8) ; This replaces the above 2 instr's
990 MOV DH,BUFFDC ; DH := bit pattern for BUF (--) FDC
991 CALL INITIO ; Initiate the requested I/O
992 RET ; And return
993 JMP short INITIO ; This replaces the above 2 instr's
994
995 *****
996 SUBTTL Floppy disk GDD routine: FLPRS
997 *****
998 NAME: FLPRS -- Read Status
999 ABSTRACT: Read the floppy disk controller status and interpret it into a
1000 ; standard (IBM defined) error code.
1001 PROCESSING: * Read the FDC status
1002 ; * Synthesize and return a standard (i.e. IBM) error code
1003 ; * [D_WKSP+F_LCMD] (byte) = Last command sent to FDC
1004 ; * [D_REG+STAT] (byte) status code (unchanged if no error)
1005
1006 INPUT: AX
1007 OUTPUT:
1008 REGISTERS:
1009 STACK USED: 6 bytes
1010 NOTES:
1011 * Status on type 1 commands is not checked. We don't expect any
1012 ; meaningful status from them as no verify options are ever set
1013 ; for any type 1 command (restore, seek, step).
1014 * The code for checking to see if a force interrupt command was
1015 ; issued has been commented out because this code never issues
1016 ; a force interrupt command which will actually cause an inter-
1017 ; rupt (see FRCINT elsewhere in this module).
1018 *****
1019 FLPRS PROC FAR
1020 CALL CPUFDC ; Set CPU (--) FDC
1021
1022
1023
1024
1025
1026
1027
1028
1029
1030
1031

```

```

0192' E4 20      1032      IN      AL,FDCBTA
0194' BA E0      1033      MOV     AH,AL
0196' A0 0000"  1034      MOV     AL,D_WKSP+F_LCMD
0199' C6 06 0000" 00      1035      MOV     D_WKSP+F_LCMD,0
019E' B4 C0      1036      TEST    AL,AL
01A0' 74 2E      1037      JZ      RIBRET
1038'           1038      CMP     AL,C_FOR
1039'           1039      JZ      RIBRET
01A2' AB 80      1040      TEST    AL,1000000B
01A4' 74 2A      1041      JZ      RIBRET
01A6' 24 E0      1042      AND     AL,11100000B
1043'           1043
01A8' 3C A0      1044      CMP     AL,C_MRB
01AA' BA C4      1045      MOV     AL,AH
01AC' 75 0C      1046      JNE     T2READ
1047'           1047
01AE' B4 03      1048      MOV     AH,DKWPRT
01B0' AB 40      1049      TEST    AL,M_WPR
01B2' 75 18      1050      JNZ     RIBDON
01B4' B4 20      1051      MOV     AH,DKFAIL
01B6' AB 20      1052      TEST    AL,M_FAU
01B8' 75 12      1053      JNZ     RIBDON
01BA' B4 08      1054      MOV     AH,DKDMAE
01BC' AB 04      1055      MOV     AL,M_LOB
01BE' 75 0C      1056      JNE     RIBDON
01C0' B4 10      1057      MOV     AH,DKCRCE
01C2' AB 08      1058      TEST    AL,M_CRC
01C4' 75 06      1059      JNZ     RIBDON
01C6' B4 04      1060      MOV     AH,DKRNFE
01C8' AB 10      1061      TEST    AL,M_RNF
01CA' 74 04      1062      JZ      RIBRET
01CC' BB 26 0000" 1063      RIBDON: MOV     D_REG+STAT,AH
01D0' CB         1064      RISRET: RET
1065'           1065
1066'           1066
1067'           1067
1068'           1068
1069'           1069
1070'           1070
1071'           1071
1072'           1072
1073'           1073
1074'           1074
1075'           1075
1076'           1076
1077'           1077
1078'           1078
1079'           1079
01D1'           1080
1081'           1081
01D1' B1 04      1082      MOV     CH,READ
1083'           1083      MOV     CL,S_FRX
1084'           1084      MOV     DH,FDCBUF
1085'           1085      CALL    INITIO
1086'           1086
1087'           1087
1088'           1088
1089'           1089
1090'           1090
1091'           1091
1092'           1092
1093'           1093
1094'           1094
1095'           1095
1096'           1096
1097'           1097
1098'           1098
1099'           1099
1100'           1100
1101'           1101
1102'           1102
1103'           1103
1104'           1104
1105'           1105
1106'           1106
1107'           1107
1108'           1108
1109'           1109
1110'           1110
1111'           1111
1112'           1112
1113'           1113
1114'           1114
1115'           1115
1116'           1116
1117'           1117
1118'           1118
1119'           1119
1120'           1120
1121'           1121
1122'           1122
1123'           1123
1124'           1124
1125'           1125
1126'           1126
1127'           1127
1128'           1128
1129'           1129
1130'           1130
1131'           1131
1132'           1132
1133'           1133
1134'           1134
1135'           1135
1136'           1136
1137'           1137
1138'           1138
1139'           1139
1140'           1140
1141'           1141
1142'           1142
1143'           1143
1144'           1144
1145'           1145
1146'           1146
1147'           1147
1148'           1148
1149'           1149
1150'           1150
1151'           1151
1152'           1152
1153'           1153
1154'           1154
1155'           1155
1156'           1156
1157'           1157
1158'           1158
1159'           1159
1160'           1160
1161'           1161
1162'           1162
1163'           1163
1164'           1164
1165'           1165
1166'           1166
1167'           1167
1168'           1168
1169'           1169
1170'           1170
1171'           1171
1172'           1172
1173'           1173
1174'           1174
1175'           1175
1176'           1176
1177'           1177
1178'           1178
1179'           1179
1180'           1180
1181'           1181
1182'           1182
1183'           1183
1184'           1184
1185'           1185
1186'           1186
1187'           1187
1188'           1188
1189'           1189
1190'           1190
1191'           1191
1192'           1192
1193'           1193
1194'           1194
1195'           1195
1196'           1196
1197'           1197
1198'           1198
1199'           1199
1200'           1200
1201'           1201
1202'           1202
1203'           1203
1204'           1204
1205'           1205
1206'           1206
1207'           1207
1208'           1208
1209'           1209
1210'           1210
1211'           1211
1212'           1212
1213'           1213
1214'           1214
1215'           1215
1216'           1216
1217'           1217
1218'           1218
1219'           1219
1220'           1220
1221'           1221
1222'           1222
1223'           1223
1224'           1224
1225'           1225
1226'           1226
1227'           1227
1228'           1228
1229'           1229
1230'           1230
1231'           1231
1232'           1232
1233'           1233
1234'           1234
1235'           1235
1236'           1236
1237'           1237
1238'           1238
1239'           1239
1240'           1240
1241'           1241
1242'           1242
1243'           1243
1244'           1244
1245'           1245
1246'           1246
1247'           1247
1248'           1248
1249'           1249
1250'           1250
1251'           1251
1252'           1252
1253'           1253
1254'           1254
1255'           1255
1256'           1256
1257'           1257
1258'           1258
1259'           1259
1260'           1260
1261'           1261
1262'           1262
1263'           1263
1264'           1264
1265'           1265
1266'           1266
1267'           1267
1268'           1268
1269'           1269
1270'           1270
1271'           1271
1272'           1272
1273'           1273
1274'           1274
1275'           1275
1276'           1276
1277'           1277
1278'           1278
1279'           1279
1280'           1280
1281'           1281
1282'           1282
1283'           1283
1284'           1284
1285'           1285
1286'           1286
1287'           1287
1288'           1288
1289'           1289
1290'           1290
1291'           1291
1292'           1292
1293'           1293
1294'           1294
1295'           1295
1296'           1296
1297'           1297
1298'           1298
1299'           1299
1300'           1300
1301'           1301
1302'           1302
1303'           1303
1304'           1304
1305'           1305
1306'           1306
1307'           1307
1308'           1308
1309'           1309
1310'           1310
1311'           1311
1312'           1312
1313'           1313
1314'           1314
1315'           1315
1316'           1316
1317'           1317
1318'           1318
1319'           1319
1320'           1320
1321'           1321
1322'           1322
1323'           1323
1324'           1324
1325'           1325
1326'           1326
1327'           1327
1328'           1328
1329'           1329
1330'           1330
1331'           1331
1332'           1332
1333'           1333
1334'           1334
1335'           1335
1336'           1336
1337'           1337
1338'           1338
1339'           1339
1340'           1340
1341'           1341
1342'           1342
1343'           1343
1344'           1344
1345'           1345
1346'           1346
1347'           1347
1348'           1348
1349'           1349
1350'           1350
1351'           1351
1352'           1352
1353'           1353
1354'           1354
1355'           1355
1356'           1356
1357'           1357
1358'           1358
1359'           1359
1360'           1360
1361'           1361
1362'           1362
1363'           1363
1364'           1364
1365'           1365
1366'           1366
1367'           1367
1368'           1368
1369'           1369
1370'           1370
1371'           1371
1372'           1372
1373'           1373
1374'           1374
1375'           1375
1376'           1376
1377'           1377
1378'           1378
1379'           1379
1380'           1380
1381'           1381
1382'           1382
1383'           1383
1384'           1384
1385'           1385
1386'           1386
1387'           1387
1388'           1388
1389'           1389
1390'           1390
1391'           1391
1392'           1392
1393'           1393
1394'           1394
1395'           1395
1396'           1396
1397'           1397
1398'           1398
1399'           1399
1400'           1400
1401'           1401
1402'           1402
1403'           1403
1404'           1404
1405'           1405
1406'           1406
1407'           1407
1408'           1408
1409'           1409
1410'           1410
1411'           1411
1412'           1412
1413'           1413
1414'           1414
1415'           1415
1416'           1416
1417'           1417
1418'           1418
1419'           1419
1420'           1420
1421'           1421
1422'           1422
1423'           1423
1424'           1424
1425'           1425
1426'           1426
1427'           1427
1428'           1428
1429'           1429
1430'           1430
1431'           1431
1432'           1432
1433'           1433
1434'           1434
1435'           1435
1436'           1436
1437'           1437
1438'           1438
1439'           1439
1440'           1440
1441'           1441
1442'           1442
1443'           1443
1444'           1444
1445'           1445
1446'           1446
1447'           1447
1448'           1448
1449'           1449
1450'           1450
1451'           1451
1452'           1452
1453'           1453
1454'           1454
1455'           1455
1456'           1456
1457'           1457
1458'           1458
1459'           1459
1460'           1460
1461'           1461
1462'           1462
1463'           1463
1464'           1464
1465'           1465
1466'           1466
1467'           1467
1468'           1468
1469'           1469
1470'           1470
1471'           1471
1472'           1472
1473'           1473
1474'           1474
1475'           1475
1476'           1476
1477'           1477
1478'           1478
1479'           1479
1480'           1480
1481'           1481
1482'           1482
1483'           1483
1484'           1484
1485'           1485
1486'           1486
1487'           1487
1488'           1488
1489'           1489
1490'           1490
1491'           1491
1492'           1492
1493'           1493
1494'           1494
1495'           1495
1496'           1496
1497'           1497
1498'           1498
1499'           1499
1500'           1500
1501'           1501
1502'           1502
1503'           1503
1504'           1504
1505'           1505
1506'           1506
1507'           1507
1508'           1508
1509'           1509
1510'           1510
1511'           1511
1512'           1512
1513'           1513
1514'           1514
1515'           1515
1516'           1516
1517'           1517
1518'           1518
1519'           1519
1520'           1520
1521'           1521
1522'           1522
1523'           1523
1524'           1524
1525'           1525
1526'           1526
1527'           1527
1528'           1528
1529'           1529
1530'           1530
1531'           1531
1532'           1532
1533'           1533
1534'           1534
1535'           1535
1536'           1536
1537'           1537
1538'           1538
1539'           1539
1540'           1540
1541'           1541
1542'           1542
1543'           1543
1544'           1544
1545'           1545
1546'           1546
1547'           1547
1548'           1548
1549'           1549
1550'           1550
1551'           1551
1552'           1552
1553'           1553
1554'           1554
1555'           1555
1556'           1556
1557'           1557
1558'           1558
1559'           1559
1560'           1560
1561'           1561
1562'           1562
1563'           1563
1564'           1564
1565'           1565
1566'           1566
1567'           1567
1568'           1568
1569'           1569
1570'           1570
1571'           1571
1572'           1572
1573'           1573
1574'           1574
1575'           1575
1576'           1576
1577'           1577
1578'           1578
1579'           1579
1580'           1580
1581'           1581
1582'           1582
1583'           1583
1584'           1584
1585'           1585
1586'           1586
1587'           1587
1588'           1588
1589'           1589
1590'           1590
1591'           1591
1592'           1592
1593'           1593
1594'           1594
1595'           1595
1596'           1596
1597'           1597
1598'           1598
1599'           1599
1600'           1600
1601'           1601
1602'           1602
1603'           1603
1604'           1604
1605'           1605
1606'           1606
1607'           1607
1608'           1608
1609'           1609
1610'           1610
1611'           1611
1612'           1612
1613'           1613
1614'           1614
1615'           1615
1616'           1616
1617'           1617
1618'           1618
1619'           1619
1620'           1620
1621'           1621
1622'           1622
1623'           1623
1624'           1624
1625'           1625
1626'           1626
1627'           1627
1628'           1628
1629'           1629
1630'           1630
1631'           1631
1632'           1632
1633'           1633
1634'           1634
1635'           1635
1636'           1636
1637'           1637
1638'           1638
1639'           1639
1640'           1640
1641'           1641
1642'           1642
1643'           1643
1644'           1644
1645'           1645
1646'           1646
1647'           1647
1648'           1648
1649'           1649
1650'           1650
1651'           1651
1652'           1652
1653'           1653
1654'           1654
1655'           1655
1656'           1656
1657'           1657
1658'           1658
1659'           1659
1660'           1660
1661'           1661
1662'           1662
1663'           1663
1664'           1664
1665'           1665
1666'           1666
1667'           1667
1668'           1668
1669'           1669
1670'           1670
1671'           1671
1672'           1672
1673'           1673
1674'           1674
1675'           1675
1676'           1676
1677'           1677
1678'           1678
1679'           1679
1680'           1680
1681'           1681
1682'           1682
1683'           1683
1684'           1684
1685'           1685
1686'           1686
1687'           1687
1688'           1688
1689'           1689
1690'           1690
1691'           1691
1692'           1692
1693'           1693
1694'           1694
1695'           1695
1696'           1696
1697'           1697
1698'           1698
1699'           1699
1700'           1700
1701'           1701
1702'           1702
1703'           1703
1704'           1704
1705'           1705
1706'           1706
1707'           1707
1708'           1708
1709'           1709
1710'           1710
1711'           1711
1712'           1712
1713'           1713
1714'           1714
1715'           1715
1716'           1716
1717'           1717
1718'           1718
1719'           1719
1720'           1720
1721'           1721
1722'           1722
1723'           1723
1724'           1724
1725'           1725
1726'           1726
1727'           1727
1728'           1728
1729'           1729
1730'           1730
1731'           1731
1732'           1732
1733'           1733
1734'           1734
1735'           1735
1736'           1736
1737'           1737
1738'           1738
1739'           1739
1740'           1740
1741'           1741
1742'           1742
1743'           1743
1744'           1744
1745'           1745
1746'           1746
1747'           1747
1748'           1748
1749'           1749
1750'           1750
1751'           1751
1752'           1752
1753'           1753
1754'           1754
1755'           1755
1756'           1756
1757'           1757
1758'           1758
1759'           1759
1760'           1760
1761'           1761
1762'           1762
1763'           1763
1764'           1764
1765'           1765
1766'           1766
1767'           1767
1768'           1768
1769'           1769
1770'           1770
1771'           1771
1772'           1772
1773'           1773
1774'           1774
1775'           1775
1776'           1776
1777'           1777
1778'           1778
1779'           1779
1780'           1780
1781'           1781
1782'           1782
1783'           1783
1784'           1784
1785'           1785
1786'           1786
1787'           1787
1788'           1788
1789'           1789
1790'           1790
1791'           1791
1792'           1792
1793'           1793
1794'           1794
1795'           1795
1796'           1796
1797'           1797
1798'           1798
1799'           1799
1800'           1800
1801'           1801
1802'           1802
1803'           1803
1804'           1804
1805'           1805
1806'           1806
1807'           1807
1808'           1808
1809'           1809
1810'           1810
1811'           1811
1812'           1812
1813'           1813
1814'           1814
1815'           1815
1816'           1816
1817'           1817
1818'           1818
1819'           1819
1820'           1820
1821'           1821
1822'           1822
1823'           1823
1824'           1824
1825'           1825
1826'           1826
1827'           1827
1828'           1828
1829'           1829
1830'           1830
1831'           1831
1832'           1832
1833'           1833
1834'           1834
1835'           1835
1836'           1836
1837'           1837
1838'           1838
1839'           1839
1840'           1840
1841'           1841
1842'           1842
1843'           1843
1844'           1844
1845'           1845
1846'           1846
1847'           1847
1848'           1848
1849'           1849
1850'           1850
1851'           1851
1852'           1852
1853'           1853
1854'           1854
1855'           1855
1856'           1856
1857'           1857
1858'           1858
1859'           1859
1860'           1860
1861'           1861
1862'           1862
1863'           1863
1864'           1864
1865'           1865
1866'           1866
1867'           1867
1868'           1868
1869'           1869
1870'           1870
1871'           1871
1872'           1872
1873'           1873
1874'           1874
1875'           1875
1876'           1876
1877'           1877
1878'           1878
1879'           1879
1880'           1880
1881'           1881
1882'           1882
1883'           1883
1884'           1884
1885'           1885
1886'           1886
1887'           1887
1888'           1888
1889'           1889
1890'           1890
1891'           1891
1892'           1892
1893'           1893
1894'           1894
1895'           1895
1896'           1896
1897'           1897
1898'           1898
1899'           1899
1900'           1900
1901'           1901
1902'           1902
1903'           1903
1904'           1904
1905'           1905
1906'           1906
1907'           1907
1908'           1908
1909'           1909
1910'           1910
1911'           1911
1912'           1912
1913'           1913
1914'           1914
1915'           1915
1916'           1916
1917'           1917
1918'           1918
1919'           1919
1920'           1920
1921'           1921
1922'           1922
1923'           1923
1924'           1924
1925'           1925
1926'           1926
1927'           1927
1928'           1928
1929'           1929
1930'           1930
1931'           1931
1932'           1932
1933'           1933
1934'           1934
1935'           1935
1936'           1936
1937'           1937
1938'           1938
1939'           1939
1940'           1940
1941'           1941
1942'           1942
1943'           1943
1944'           1944
1945'           1945
1946'           1946
1947'           1947
1948'           1948
1949'           1949
1950'           1950
1951'           1951
1952'           1952
1953'           1953
1954'           1954
1955'           1955
1956'           1956
1957'           1957
1958'           1958
1959'           1959
1960'           1960
1961'           1961
1962'           1962
1963'           1963
1964'           1964
1965'           1965
1966'           1966
1967'           1967
1968'           1968
1969'           1969
1970'           1970
1971'           1971
1972'           1972
1973'           1973
1974'           1974
1975'           1975
1976'           1976
1977'           1977
1978'           1978
1979'           1979
1980'           1980
1981'           1981
1982'           1982
1983'           1983
1984'           1984
1985'           1985
1986'           1986
1987'           1987
1988'           1988
1989'           1989
1990'           1990
1991'           1991
1992'           1992
1993'           1993
1994'           1994
1995'           1995
1996'           1996
1997'           1997
1998'           1998
1999'           1999
2000'           2000
2001'           2001
2002'           2002
2003'           2003
2004'           2004
2005'           2005
2006'           2006
2007'           2007
2008'           2008
2009'           2009
2010'           2010
2011'           2011
2012'           2012
2013'           2013
2014'           2014
2015'           2015
2016'           2016
2017'           2017

```

```

01D3' E9 FE7D
1084 JNITRD: JMP near JNITRD
1085
1086 ;
1087 ; ***** This jump accomplishes everything
1088 ; SUBRTL Floppy disk GDD routine: FLPVFC ; which is commented out here
1089 ; ***** Return
1090 ; NAME: FLPVFC - Verify Crc
1091 ; ABSTRACT: Perform the necessary processing to verify a sector CRC
1092 ; PROCESSING: * Set parameters and call INITIO to initiate the reading of the
1093 ; data (during which the controller will check the CRC).
1094 ; INPUT: * ES: BX = @ DIT for the current disk drive
1095 ; * [D_REG+CYL] (byte) = desired cylinder for the I/O
1096 ; * [D_REG+TRK] (byte) = desired track for the I/O
1097 ; * [D_REG+SEC] (byte) = desired sector for the I/O
1098 ; * [D_NSTAJ] (byte) = Next state for driver (S_WAIT or S_DONE)
1099 ; REGISTERS: AX, BX, DX, SI
1100 ; STACK USED: 10 bytes
1101 ; *****
1102 FLPVFC PROC FAR
1103 ; CH, READ ; CH := Read command with options
1104 ; MOV CL, D_NSTA ; CL := next state
1105 ; MOV DH, FDCBUF ; DH := bit pattern for FDC --> BUF
1106 ; CALL INITIO ; Initiate the requested I/O
1107 ; JMP short JNITRD ; This jump accomplishes everything
1108 ; ; which is commented out here
1109 ; RET ; Return
1110 ; *****
1111 SUBRTL Floppy disk GDD routine: FLPVFD
1112 ; *****
1113 ; NAME: FLPVFD - Verify Data
1114 ; ABSTRACT: Perform the processing needed to compare data in memory with
1115 ; data on disk.
1116 ; PROCESSING: * Set parameters & call INITIO to initiate the read & verify
1117 ; of the data
1118 ; INPUT: * ES: BX = @ DIT for the current disk drive
1119 ; * [D_REG+CYL] (byte) = desired cylinder for the I/O
1120 ; * [D_REG+TRK] (byte) = desired track for the I/O
1121 ; * [D_REG+SEC] (byte) = desired sector for the I/O
1122 ; * [D_NSTAJ] (byte) = next state for driver (S_WAIT or S_VFD)
1123 ; REGISTERS: AX, DX, SI
1124 ; STACK USED: 10 bytes
1125 ; *****
1126 FLPVFD PROC FAR
1127 ; CH, READ ; CH := Read command with options
1128 ; MOV CL, S_VFD ; CL := next state (verify data)
1129 ; MOV DH, FDCBUF ; DH := bit pattern for FDC --> BUF
1130 ; CALL INITIO ; Initiate the requested I/O
1131 ; JMP short JNITRD ; This jump accomplishes everything
1132 ; ; which is commented out here
1133 ; RET ; Return
1134 ; *****
1135 SUBRTL Floppy disk GDD routine: FLPASP

```

```

1136 ; *****
1137 ; NAME: FLPASP - Auxiliary State Processor
1138 ; ABSTRACT: Process disk driver states specific to the floppy disk inter-
1139 ; face. This routine is an extension of the general disk inter-
1140 ; rupt level code which processes the generic driver states.
1141 ; INPUT: * EB: BX = @ DIT for current disk drive
1142 ; * ID_STAT] (byte) = Current state of the disk driver
1143 ; PROCESBING: * Process disk driver states specific to the floppy disks
1144 ; * Hits the floppy disk controller port directly rather than
1145 ; * through the General Disk Operations "next command" interface.
1146 ; *****
1147 ASP_JMP DW L_FRX ; VECTOR: Floppy read transfer
1148 DW L_VFD ; VECTOR: Verify data
1149 DW L_REC1 ; VECTOR: Processing error recovery
1150 DW L_RDA ; VECTOR: Process read address
1151 FAR
1152 FLPASP AL, D_STAT ; AL := current state of driver
1153 SUB AL, S_MAX+1 ; Adjust for fixed states
1154 CBW ; Convert to a word value
1155 ADD AX, AX ; Convert to a jump table index
1156 CMP D_STAT, S_FMAX ; Q: Is current state a valid state?
1157 JLE ASPSEL ; Y: Go process the state
1158 INT FATINT ; N: Fatal error
1159 MOV DI, AX ; DI := jump table index
1160 MOV AL, D_REC+STAT ; AL := Status code
1161 CMP AL, DKNDRM ; Q: Did an error occur?
1162 JMP ASP_JMP[DI] ; Branch to state handler routine
1163
1164 L_FRX: PUSHF ; FRX - Floppy read transfer
1165 CALL XFRRDD ; Save comparison status
1166 POPF ; Transfer the read data
1167 CMP AL, DKNDRM ; Restore comparison status
1168 JNE FLPEP ; Q: Did an error occur?
1169 JMP short ptr FINISH ; Y: Go do something about it
1170 ; N: Then we're done
1171 CMP AL, DKNDRM ; VFD - Verify data
1172 JNE FLPEP ; Q: Did an error occur?
1173 CALL VERIFY ; Y: Go do something about it
1174 MOV D_NBTA, S_IDLE ; N: Verify the data
1175 RET ; Finished, Set next state to "idle"
1176
1177 L_REC1: MOV DX, RESTOR or (S_WAIT shl 8) ; RECI - Error recovery, step in
1178 MOV D_REG+CURCYL, 0 ; icmd: restore, state: retry wait
1179 MOV D_REG+STAT, DKNDRM ; Set current cylinder position to 0
1180 CALL CPUFDC ; Reset operation status
1181 MOV AL, DL ; Set CPU (--) FDC
1182 CALL HITFDC ; Put command in AL
1183 MOV AX, FDCBUF or (MMASK shl 8) ; Issue command to FDC
1184 ; AL := code for FDC (--) BUF
1185 CALL HITC_P ; AH := interface made bit mask
1186 MOV D_NBTA, DH ; Set FDC (--) BUF
1187 RET ; Store next state
1188 ; And return
  
```



```

1 *****
2 ; TITLE - HEADER - THIS MODULE MUST BE THE FIRST IN ROM.
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE
4 ; ABSTRACT - This module contains the System ROM header information. It
5 ; must be the first module linked, so that it is at offset 0000
6 ; relative to the beginning of the ROM. Each ROM in the system
7 ; must contain a header with this structure:
8 ;
9 ; DW xxxx ; Size of the ROM (OFFFH for System)
10 ; DW xxxx ; Entry point of the ROM
11 ; DB xx ; Length of ID message
12 ; CR,LF ; First characters of message
13 ; DB 'Vx.xx' ; 5-byte ROM version in ASCII
14 ; DB xx,xx,... ; Remainder of ID message
15 ;
16 ; REGISTERS USED - NONE
17 ;
18 ; STACK USED - NONE
19 ;
20 *****
21 NAME HEADER
22 SUBTTL SYSTEM ROM HEADER
23 *****
24 ; PUBLIC DEFINITIONS
25 ;
26 ;
27 ;
28 PUBLIC ROMVER
29 ;
30 *****
31 ; EXTERNAL REFERENCES
32 ;
33 ; EXTERN PUPTST:NEAR ; ENTRY POINT OF ROM
34 ; EXTERN ROMSIZ:ABB ; ROM SIZE
35 ;
36 ; LOCAL CONSTANTS
37 ;
38 ;
39 CR EQU ODH
40 LF EQU OAH
41 ;
42 ; MODULE ENTRY POINT
43 ;
44 SECT ROMCOD
45 ASSUME CS:ROMCOD
46 ;
47 ;
48 ;
49 ;
50 ROMMSQ DB 32
51 ROMVER DB 2E
52 DB 53
53 DB 59
54 DB 53
55 DB 4F
56 DB 4D
57 DB 20
58 DB 63
59 DB 29
60 DB 20

```

0000' FFFF ; Size of this ROM (Special for system)
0002' 0001" ; Entry point of this ROM
0004' 3A ; Size of the sign on message
0005' 0D 0A
0007' 56 31 2E 32 33
000C' 20 53 59 53 52 4F
0012' 4D 20 2B 63 29 20

0018' 43 6F 70 79 72 69
001E' 67 6B 74 20 34 65
0024' 7B 61 73 20 49 6E
002A' 73 74 72 75 6D 65
0030' 6E 74 73 20 49 6E
0036' 63 2E 20 31 39 3B
003C' 33
003D' 0D 0A
-003A

53 MS081Z DB CR,LF
54 MS081Z EGU \$-ROMMSO
55 ,
56 END

No errors detected

```

1 *****
2
3 ; TITLE - INTTST - Interrupt controller power up diagnostics.
4 ; COMPUTER - BOBB ASSEMBLY LANGUAGE
5 ; ABSTRACT - This module contains the power up diagnostics that test the
6 ; interrupt controller in the PEGASUS main board. Only those interrupts
7 ; that are generated by basic system components are tested.
8 *****
9 NAME INTTST - PEGASUS power up interrupt controller test
10 SUBTTL
11 *****
12 *****
13 *****
14 *****
15 *****
16
17 SECT ROMDAT
18 ASSUME CS:ROMCOD
19
20 EXTERN DSKC_M:BYTE ; DISK SELECT LATCH MEMORY IMAGE. (ROMDAT)
21 EXTERN PRCD_M:BYTE ; PRINTER OUTPUT LATCH MEMORY IMAGE. (ROMDAT)
22 *****
23 *****
24 *****
25 *****
26 *****
27 SECT ROMCOD
28 EXTERN DSPDIE:NEAR ; DISPLAY FATAL ERROR and DIE. (OUTPUT)
29 EXTERN FILVEC:NEAR ; ENTRY TO VECTOR INITIALIZATION. (VECTINI)
30 EXTERN DECLED:NEAR ; DECREMENT LED SUBROUTINE. (DECLED)
31 EXTERN KEYRST:NEAR ; KEYBOARD INTERRUPT RESET. (KEYDSR)
32 EXTERN FRCINT:NEAR ; FLOPPY CONTROLLER RESET. (DSKDSR)
33 EXTERN TIMTST:NEAR ; TIMER DIAGNOSTICS ERROR. (TIMTST)
34 EXTERN ICNERR:ABS ; INTERRUPT CONTROLLER ERROR CODE. (TSERR)
35 EXTERN IVIERR:ABS ; INVALID INTERRUPT ERROR. (TSERR)
36 EXTERN LED100:ABS ; INTERRUPT LED ERROR PATTERN. (ROMERR)
37 EXTERN NMERR:ABS ; NMI INTERRUPT FAILURE CODE. (TSERR)
38 EXTERN TIMERR:ABS ; TIMER INT FAILURE CODE. (TSERR)
39 EXTERN KEYERR:ABS ; KEYBOARD INT FAILURE CODE. (TSERR)
40 EXTERN FLIERR:ABS ; FLOPPY INT FAILURE CODE. (TSERR)
41 *****
42 *****
43 *****
44 *****
45 *****
46 *****
47 *****
48 *****
49 *****
50 *****
51 *****
52 *****
53 *****
54 *****
55 *****
56 *****
57 *****
58 *****
59 *****
60 *****
61 *****
62 *****
63 *****
64 *****
65 *****
66 *****
67 *****
68 *****
69 *****
70 *****
71 *****
72 *****
73 *****
74 *****
75 *****
76 *****
77 *****
78 *****
79 *****
80 *****
81 *****
82 *****
83 *****
84 *****
85 *****
86 *****
87 *****
88 *****
89 *****
90 *****
91 *****
92 *****
93 *****
94 *****
95 *****
96 *****
97 *****
98 *****
99 *****
100 *****
101 *****
102 *****
103 *****
104 *****
105 *****
106 *****
107 *****
108 *****
109 *****
110 *****
111 *****
112 *****
113 *****
114 *****
115 *****
116 *****
117 *****
118 *****
119 *****
120 *****
121 *****
122 *****
123 *****
124 *****
125 *****
126 *****
127 *****
128 *****
129 *****
130 *****
131 *****
132 *****
133 *****
134 *****
135 *****
136 *****
137 *****
138 *****
139 *****
140 *****
141 *****
142 *****
143 *****
144 *****
145 *****
146 *****
147 *****
148 *****
149 *****
150 *****
151 *****
152 *****
153 *****
154 *****
155 *****
156 *****
157 *****
158 *****
159 *****
160 *****
161 *****
162 *****
163 *****
164 *****
165 *****
166 *****
167 *****
168 *****
169 *****
170 *****
171 *****
172 *****
173 *****
174 *****
175 *****
176 *****
177 *****
178 *****
179 *****
180 *****
181 *****
182 *****
183 *****
184 *****
185 *****
186 *****
187 *****
188 *****
189 *****
190 *****
191 *****
192 *****
193 *****
194 *****
195 *****
196 *****
197 *****
198 *****
199 *****
200 *****
201 *****
202 *****
203 *****
204 *****
205 *****
206 *****
207 *****
208 *****
209 *****
210 *****
211 *****
212 *****
213 *****
214 *****
215 *****
216 *****
217 *****
218 *****
219 *****
220 *****
221 *****
222 *****
223 *****
224 *****
225 *****
226 *****
227 *****
228 *****
229 *****
230 *****
231 *****
232 *****
233 *****
234 *****
235 *****
236 *****
237 *****
238 *****
239 *****
240 *****
241 *****
242 *****
243 *****
244 *****
245 *****
246 *****
247 *****
248 *****
249 *****
250 *****
251 *****
252 *****
253 *****
254 *****
255 *****
256 *****
257 *****
258 *****
259 *****
260 *****
261 *****
262 *****
263 *****
264 *****
265 *****
266 *****
267 *****
268 *****
269 *****
270 *****
271 *****
272 *****
273 *****
274 *****
275 *****
276 *****
277 *****
278 *****
279 *****
280 *****
281 *****
282 *****
283 *****
284 *****
285 *****
286 *****
287 *****
288 *****
289 *****
290 *****
291 *****
292 *****
293 *****
294 *****
295 *****
296 *****
297 *****
298 *****
299 *****
300 *****
301 *****
302 *****
303 *****
304 *****
305 *****
306 *****
307 *****
308 *****
309 *****
310 *****
311 *****
312 *****
313 *****
314 *****
315 *****
316 *****
317 *****
318 *****
319 *****
320 *****
321 *****
322 *****
323 *****
324 *****
325 *****
326 *****
327 *****
328 *****
329 *****
330 *****
331 *****
332 *****
333 *****
334 *****
335 *****
336 *****
337 *****
338 *****
339 *****
340 *****
341 *****
342 *****
343 *****
344 *****
345 *****
346 *****
347 *****
348 *****
349 *****
350 *****
351 *****
352 *****
353 *****
354 *****
355 *****
356 *****
357 *****
358 *****
359 *****
360 *****
361 *****
362 *****
363 *****
364 *****
365 *****
366 *****
367 *****
368 *****
369 *****
370 *****
371 *****
372 *****
373 *****
374 *****
375 *****
376 *****
377 *****
378 *****
379 *****
380 *****
381 *****
382 *****
383 *****
384 *****
385 *****
386 *****
387 *****
388 *****
389 *****
390 *****
391 *****
392 *****
393 *****
394 *****
395 *****
396 *****
397 *****
398 *****
399 *****
400 *****
401 *****
402 *****
403 *****
404 *****
405 *****
406 *****
407 *****
408 *****
409 *****
410 *****
411 *****
412 *****
413 *****
414 *****
415 *****
416 *****
417 *****
418 *****
419 *****
420 *****
421 *****
422 *****
423 *****
424 *****
425 *****
426 *****
427 *****
428 *****
429 *****
430 *****
431 *****
432 *****
433 *****
434 *****
435 *****
436 *****
437 *****
438 *****
439 *****
440 *****
441 *****
442 *****
443 *****
444 *****
445 *****
446 *****
447 *****
448 *****
449 *****
450 *****
451 *****
452 *****
453 *****
454 *****
455 *****
456 *****
457 *****
458 *****
459 *****
460 *****
461 *****
462 *****
463 *****
464 *****
465 *****
466 *****
467 *****
468 *****
469 *****
470 *****
471 *****
472 *****
473 *****
474 *****
475 *****
476 *****
477 *****
478 *****
479 *****
480 *****
481 *****
482 *****
483 *****
484 *****
485 *****
486 *****
487 *****
488 *****
489 *****
490 *****
491 *****
492 *****
493 *****
494 *****
495 *****
496 *****
497 *****
498 *****
499 *****
500 *****
501 *****
502 *****
503 *****
504 *****
505 *****
506 *****
507 *****
508 *****
509 *****
510 *****
511 *****
512 *****
513 *****
514 *****
515 *****
516 *****
517 *****
518 *****
519 *****
520 *****
521 *****
522 *****
523 *****
524 *****
525 *****
526 *****
527 *****
528 *****
529 *****
530 *****
531 *****
532 *****
533 *****
534 *****
535 *****
536 *****
537 *****
538 *****
539 *****
540 *****
541 *****
542 *****
543 *****
544 *****
545 *****
546 *****
547 *****
548 *****
549 *****
550 *****
551 *****
552 *****
553 *****
554 *****
555 *****
556 *****
557 *****
558 *****
559 *****
560 *****
561 *****
562 *****
563 *****
564 *****
565 *****
566 *****
567 *****
568 *****
569 *****
570 *****
571 *****
572 *****
573 *****
574 *****
575 *****
576 *****
577 *****
578 *****
579 *****
580 *****
581 *****
582 *****
583 *****
584 *****
585 *****
586 *****
587 *****
588 *****
589 *****
590 *****
591 *****
592 *****
593 *****
594 *****
595 *****
596 *****
597 *****
598 *****
599 *****
600 *****
601 *****
602 *****
603 *****
604 *****
605 *****
606 *****
607 *****
608 *****
609 *****
610 *****
611 *****
612 *****
613 *****
614 *****
615 *****
616 *****
617 *****
618 *****
619 *****
620 *****
621 *****
622 *****
623 *****
624 *****
625 *****
626 *****
627 *****
628 *****
629 *****
630 *****
631 *****
632 *****
633 *****
634 *****
635 *****
636 *****
637 *****
638 *****
639 *****
640 *****
641 *****
642 *****
643 *****
644 *****
645 *****
646 *****
647 *****
648 *****
649 *****
650 *****
651 *****
652 *****
653 *****
654 *****
655 *****
656 *****
657 *****
658 *****
659 *****
660 *****
661 *****
662 *****
663 *****
664 *****
665 *****
666 *****
667 *****
668 *****
669 *****
670 *****
671 *****
672 *****
673 *****
674 *****
675 *****
676 *****
677 *****
678 *****
679 *****
680 *****
681 *****
682 *****
683 *****
684 *****
685 *****
686 *****
687 *****
688 *****
689 *****
690 *****
691 *****
692 *****
693 *****
694 *****
695 *****
696 *****
697 *****
698 *****
699 *****
700 *****
701 *****
702 *****
703 *****
704 *****
705 *****
706 *****
707 *****
708 *****
709 *****
710 *****
711 *****
712 *****
713 *****
714 *****
715 *****
716 *****
717 *****
718 *****
719 *****
720 *****
721 *****
722 *****
723 *****
724 *****
725 *****
726 *****
727 *****
728 *****
729 *****
730 *****
731 *****
732 *****
733 *****
734 *****
735 *****
736 *****
737 *****
738 *****
739 *****
740 *****
741 *****
742 *****
743 *****
744 *****
745 *****
746 *****
747 *****
748 *****
749 *****
750 *****
751 *****
752 *****
753 *****
754 *****
755 *****
756 *****
757 *****
758 *****
759 *****
760 *****
761 *****
762 *****
763 *****
764 *****
765 *****
766 *****
767 *****
768 *****
769 *****
770 *****
771 *****
772 *****
773 *****
774 *****
775 *****
776 *****
777 *****
778 *****
779 *****
780 *****
781 *****
782 *****
783 *****
784 *****
785 *****
786 *****
787 *****
788 *****
789 *****
790 *****
791 *****
792 *****
793 *****
794 *****
795 *****
796 *****
797 *****
798 *****
799 *****
800 *****
801 *****
802 *****
803 *****
804 *****
805 *****
806 *****
807 *****
808 *****
809 *****
810 *****
811 *****
812 *****
813 *****
814 *****
815 *****
816 *****
817 *****
818 *****
819 *****
820 *****
821 *****
822 *****
823 *****
824 *****
825 *****
826 *****
827 *****
828 *****
829 *****
830 *****
831 *****
832 *****
833 *****
834 *****
835 *****
836 *****
837 *****
838 *****
839 *****
840 *****
841 *****
842 *****
843 *****
844 *****
845 *****
846 *****
847 *****
848 *****
849 *****
850 *****
851 *****
852 *****
853 *****
854 *****
855 *****
856 *****
857 *****
858 *****
859 *****
860 *****
861 *****
862 *****
863 *****
864 *****
865 *****
866 *****
867 *****
868 *****
869 *****
870 *****
871 *****
872 *****
873 *****
874 *****
875 *****
876 *****
877 *****
878 *****
879 *****
880 *****
881 *****
882 *****
883 *****
884 *****
885 *****
886 *****
887 *****
888 *****
889 *****
890 *****
891 *****
892 *****
893 *****
894 *****
895 *****
896 *****
897 *****
898 *****
899 *****
900 *****
901 *****
902 *****
903 *****
904 *****
905 *****
906 *****
907 *****
908 *****
909 *****
910 *****
911 *****
912 *****
913 *****
914 *****
915 *****
916 *****
917 *****
918 *****
919 *****
920 *****
921 *****
922 *****
923 *****
924 *****
925 *****
926 *****
927 *****
928 *****
929 *****
930 *****
931 *****
932 *****
933 *****
934 *****
935 *****
936 *****
937 *****
938 *****
939 *****
940 *****
941 *****
942 *****
943 *****
944 *****
945 *****
946 *****
947 *****
948 *****
949 *****
950 *****
951 *****
952 *****
953 *****
954 *****
955 *****
956 *****
957 *****
958 *****
959 *****
960 *****
961 *****
962 *****
963 *****
964 *****
965 *****
966 *****
967 *****
968 *****
969 *****
970 *****
971 *****
972 *****
973 *****
974 *****
975 *****
976 *****
977 *****
978 *****
979 *****
980 *****
981 *****
982 *****
983 *****
984 *****
985 *****
986 *****
987 *****
988 *****
989 *****
990 *****
991 *****
992 *****
993 *****
994 *****
995 *****
996 *****
997 *****
998 *****
999 *****
1000 *****

```

-0000
332 ,
333 BECT ROMCOD
334 ASSUME CS:ROMCOD, DS:ROMDAT, EB:ABS0

```

0000' B2 09*
0002' E9 00F2
336 IMRER: MOV DL,ICNERR
337 JMP FATAL
338
339 ; This is the entry to the interrupt controller diagnostics. First, the
340 ; controller is initialized along with all the interrupt vectors in memory.
341 ; The IMR register is tested with interrupts disabled. With all vectors
342 ; set to "Invalid interrupt" the basic interrupts are unmasked. If no
343 ; invalid interrupts occur, the test proceeds.
344 ;
345 ; Disable speaker and timer interrupts.
346 ;
347 INTTBT: MOV AL,(ICW1 + IC_IW4 + IC_SNO + IC_LTM) ; WRITE ICW1.
348 OUT INTA00,AL
349 MOV AL,IROINT
350 MOV DX,INTA01
351 OUT DX,AL
352 MOV AL,(ICW4 + IC_MB6)
353 OUT DX,AL
354 MOV AL,OF0H
355 OUT DX,AL
356 IN AL,DX
357 CMP AL,OF0H
358 JNE IMRER
359 MOV AL,OCCH
360 OUT DX,AL
361 IN AL,DX
362 CMP AL,OCCH
363 JNE IMRER
364 MOV AL,055H
365 OUT DX,AL
366 IN AL,DX
367 CMP AL,055H
368 JNE IMRER
369 MOV AL,0AAH
370 OUT DX,AL
371 IN AL,DX
372 CMP AL,0AAH
373 JNE IMRER
374 MOV AL,OFFH
375 OUT DX,AL
376 CALL PRTRST
377 CALL TIMRST
378 CALL FLPRST
379 CALL KEYRST
380 MOV CX,OFFSET INVINT
381 CALL FILVEC
382 XOR DL,DL
383 BTI
384 NOP
385 NOP
386 CLI
387
0005' B0 1B
0007' E6 1B
0009' B0 40
000B' BA 0019
000E' EE
000F' B0 01
0011' EE
0012' B0 F0
0014' EE
0015' EC
0016' 3C F0
0018' 75 E6
001A' B0 CC
001C' EE
001D' EC
001E' 3C CC
0020' 75 DE
0022' B0 55
0024' EE
0025' EC
0026' 3C 55
0028' 75 D6
002A' B0 AA
002C' EE
002D' EC
002E' 3C AA
0030' 75 CE
0032' B0 FF
0034' EE
0035' EB 00CA
0038' EB 00D2
003B' EB 00BE
003E' EB 0006"
0041' B9 0162"
0044' EB 0004"
0047' 30 D2
0049' FB
004A' 90
004B' 90
004C' FA
336 DL,ICNERR
337 AND REPORT ERROR
338
339 ; This is the entry to the interrupt controller diagnostics. First, the
340 ; controller is initialized along with all the interrupt vectors in memory.
341 ; The IMR register is tested with interrupts disabled. With all vectors
342 ; set to "Invalid interrupt" the basic interrupts are unmasked. If no
343 ; invalid interrupts occur, the test proceeds.
344 ;
345 ; Disable speaker and timer interrupts.
346 ;
347 INTTBT: MOV AL,(ICW1 + IC_IW4 + IC_SNO + IC_LTM) ; WRITE ICW1.
348 OUT INTA00,AL
349 MOV AL,IROINT
350 MOV DX,INTA01
351 OUT DX,AL
352 MOV AL,(ICW4 + IC_MB6)
353 OUT DX,AL
354 MOV AL,OF0H
355 OUT DX,AL
356 IN AL,DX
357 CMP AL,OF0H
358 JNE IMRER
359 MOV AL,OCCH
360 OUT DX,AL
361 IN AL,DX
362 CMP AL,OCCH
363 JNE IMRER
364 MOV AL,055H
365 OUT DX,AL
366 IN AL,DX
367 CMP AL,055H
368 JNE IMRER
369 MOV AL,0AAH
370 OUT DX,AL
371 IN AL,DX
372 CMP AL,0AAH
373 JNE IMRER
374 MOV AL,OFFH
375 OUT DX,AL
376 CALL PRTRST
377 CALL TIMRST
378 CALL FLPRST
379 CALL KEYRST
380 MOV CX,OFFSET INVINT
381 CALL FILVEC
382 XOR DL,DL
383 BTI
384 NOP
385 NOP
386 CLI
387
    ; ; SET ERROR CODE INTO DL
    ; ; AND REPORT ERROR
    ; ;
    ; ; Hardware interrupts 40h-147h
    ; ; Point to controller mask reg.
    ; ; Select 8086 mode.
    ; ; Try an interrupt mask.
    ; ; Read interrupt mask.
    ; ; G: Is mask correct ?
    ; ; No, fatal error.
    ; ; Yes, try another mask.
    ; ; Read mask.
    ; ; G: IS MASK CORRECT ?
    ; ; NO, FATAL ERROR.
    ; ; Yes, try another mask.
    ; ; Read mask.
    ; ; G: IS MASK CORRECT ?
    ; ; NO, FATAL ERROR.
    ; ; MASK ALL INTERRUPTS.
    ; ; RESET PRINTER
    ; ; RESET TIMER DEVICES.
    ; ; RESET FLOPPY CONTROLLER.
    ; ; RESET KEYBOARD VART INTERFACE.
    ; ; SET ADDRESS INVALID INTR VECTOR.
    ; ; SET INTERRUPT VECTORS TO INVALID.
    ; ; SET INITIAL ERROR FLAG
    ; ; ENABLE INTERRUPTS.
    ; ; WINDOW
    ; ; WINDOW
    ; ; DISABLE INTERRUPTS.

```




```

388 ;
389 ;
390 ;
391 ;
004D' 268B 0E 0008
0052' 26C7 06 0008 0134"
0059' B0 22
005B' E6 11
005D' B0 20
005F' E6 11
0061' 2689 0E 0008
0066' B0 0E 0002" 08
0068' A0 0002"
006E' E6 03

0070' B0 F7
0072' E6 19
0074' FB
0075' B0 0E 0001" 02
007A' A0 0001"
007D' 26C7 06 010C 013C"
0084' E6 00
0086' B0 76
008B' E6 17
008A' B0 02
008C' E6 15
008E' B0 00
0090' E6 15
0092' B3 03
0094' FE CB
0096' 75 FC
0098' FA
0099' 26C7 06 010C 0162"
00A0' B0 26 0001" FD
00A5' B0 02
00A7' E6 00

00A9' B0 BF
00AB' E6 19
00AD' B9 4B1E
00B0' FB
00B1' E2 FE
00B3' FA
00B4'
00B4' 26C7 06 0118 014E"

392 ;
393 ;
394 ;
395 ;
396 ;
397 ;
398 ;
399 ;
400 ;
401 ;
402 ;
403 ;
404 ;
405 ;
406 ;
407 ;
408 ;
409 ;
410 ;
411 ;
412 ;
413 ;
414 ;
415 ;
416 ;
417 ;
418 ;
419 ;
420 ;
421 ;
422 ;
423 ;
424 ;
425 ;
426 ;
427 ;
428 ;
429 ;
430 ;
431 ;
432 ;
433 ;
434 ;
435 ;
436 ;
437 ;
438 ;
439 ;

NOW TO CAUSE INDIVIDUAL INTERRUPTS IN THE FOLLOWING ORDER
NMI, TIMERS, FLOPPY, KEYBOARD. THE NMI IS TESTED WITH THE
CRT AND PARITY INTERRUPTS DISABLED.

MOV CX,EB:WORD PTR (NMIINT*4) ;;; SAVE CURRENT NMI VECTOR.
MOV ES:WORD PTR (NMIINT*4),OFFSET NMIVCT ;;; NMI INTERRUPT VECTOR.
MOV AL,(UC_RTB + UC_DTR) ;;; RAISE DTR TO CAUSE NMI.
OUT KBCMD,AL ;;; DO IT.
MOV AL,UC_RTB ;;; RESTORE NMI DISABLE, JUST IN CASE.
OUT KBCMD,AL ;;;
MOV EB:WORD PTR (NMIINT*4),CX ;;; RESTORE NMI VECTOR.
OR PRCD_M,PARIEN ;;; RE-ENABLE PARITY NMI INTERRUPT.
MOV AL,PRCD_M ;;;
OUT PRCD_P,AL ;;;

NOW FOR TIMER INTERRUPT. ONLY TIMER 1 IS TESTED SINCE TIMER2'S
INTERRUPT IS SHARED WITH FOREIGN DEVICES ON THE BUS.

MOV AL,ENAB3 ;;; ENABLE SYSTEM TIMER INTERRUPT.
OUT INTA01,AL ;;;
STI ;;; ALLOW INVALID INTERRUPT TO OCCUR
OR DBKC_M,TMR1EN ; ; SET TIMER INT ENABLE LATCH.
MOV AL,DSKC_M ; ;
MOV EB:WORD PTR (IR3INT*4),OFFSET TIMRIN ; SET UP CORRECT VECTOR.
OUT DBKC_P,AL ; ; ENABLE TIMER 1.
MOV AL,(TC_SCI+TC_MRD+TC_MD3) ; ; SET UP SYSTEM TIMER.
OUT TIMCMD,AL ; ; WRITE TO TIMER COMMAND REG.
MOV AL,02 ; ; LOW BYTE OF COUNT.
OUT TIMER1,AL ; ;
MOV AL,00 ; ; HIGH BYTE OF COUNT.
OUT TIMER1,AL ; ;
MOV BL,03 ; ; LOOP FOR AT LEAST 15 MICROSECS
BL
DEC BL ; ;
JNE DECLOP ; ;
CLI ; ;
GOTIM: MOV EB:WORD PTR (IR3INT*4),OFFSET INVINT ;;; RESTORE INVALID VECTOR
AND DSKC_M,NOT TMR1EN ;;; DISABLE TIMER INTERRUPT.
MOV AL,TMR1EN ;;;
OUT DSKC_P,AL ;;;

NOW FOR DISK CONTROLLER INTERRUPT. FIRST, THE INTERRUPT IS
ENABLED TO DETERMINE IF AN INTERRUPT FROM THE BUS IS PENDING.
IF SO, IT IS INVALID.

MOV AL,ENAB6 ;;; SET ENABLE OF DISK INTERRUPT.
OUT INTA01,AL ;;; WRITE TO INT CONTROLLER.
MOV CX,4B1EH ;;; DELAY 100 MS.
STI ; ;
LOOP $ ; ; WAIT JUST IN CASE.
CLI ; ;

OUTBT: MOV ES:WORD PTR (IR6INT*4),OFFSET FLPINT

```

```
00BB' FB  
00BC' EB 0059  
00BF' FA  
00C0' 26C7 06 011B 0162"  
  
00C7'  
00C7' 80 7F  
00C9' E6 19  
00CB' FB  
00CC' 90  
00CD' 90  
00CE' FA  
00CF' 26C7 06 011C 0159"  
00D6' B0 21  
00D8' E6 11  
00DA' FB  
00DB' B9 0014  
00DE' E2 FE  
00E0' FA  
00E1' B0 20  
00E3' E6 11  
00E5' B0 FF  
00E7' E6 19  
00E9' 26C7 06 011C 0162"  
  
00F0' B0 1E  
00F2' 30 C2  
00F4' E9 0008"  
  
00F7'  
00F7' B0 08*  
00F9' E9 0003"  
  
00FC'  
00FC' E8 0007"  
00FF' E4 20  
0101' C3  
  
0102'  
0102' B0 26 0002" 77  
  
440  
441  
442  
443  
444  
445  
446  
447  
448  
449  
450  
451  
452  
453  
454  
455  
456  
457  
458  
459  
460  
461  
462  
463  
464  
465  
466  
467  
468  
469  
470  
471  
472  
473  
474  
475  
476  
477  
478  
479  
480  
481  
482  
483  
484  
485  
486  
487  
488  
489  
490  
491  
  
BTI  
CALL  
CLI  
MOV  
  
FORCIT  
  
EB: WORD PTR (IR6INT*4), OFFSET INVRT ; ; RESTORE VECTOR.  
  
NOW TO TRY KEYBOARD INTERRUPT.  
  
AL, ENAB7  
INTA01, AL  
  
ENABLE FOR KEYBOARD INT.  
  
CAUSE INVALID INTERRUPT  
  
; ; IF IT OCCURS.  
ES: WORD PTR (IR7INT*4), OFFSET KBDINT ; ;  
AL, (UC_RT8 + UC_TXE) ; ; CAUSE KEYBOARD INTERRUPT.  
KBCMD, AL  
  
CX, 20  
; ; LOOP FOR A WHILE.  
  
; ;  
AL, UC_RT8  
KBCMD, AL  
AL, OFFH  
INTA01, AL  
ES: WORD PTR (IR7INT*4), OFFSET INVRT ; ;  
  
REBSTORE KEYBOARD UART.  
DIBABLE ALL INT IN MASK.  
  
GET AND REPORT ON ANY ERRORS  
  
AL, 00011110B  
  
AND OF NM1, TIMER, KEY AND FLOPPY  
INTERRUPT OCCURRENCES  
; ; SETS CORRECT BIT IN ERROR IF NO  
; ; INTERRUPT OCCURRED.  
TIMTST  
  
JUMP AND CONTINUE TESTING  
; ; (TIMER TEST REPORTS ERRORS)  
COMMON FATAL EXIT FOR ALL INTERRUPT ERRORS.  
  
AL, LED100  
DSPDIE  
  
ROUTINE TO DISABLE FLOPPY CONTROLLER.  
  
CALL FRCINT  
IN AL, FDCSTA  
RET  
  
ROUTINES TO DISABLE PRINTER, PARITY AND TIMER INTERRUPTS.  
  
AND PRCD_M, NOT (PARIEN+PRINEN) ; ; DISABLE PARITY INTERRUPT.
```

```

0107' A0 0002"
010A' E6 03
010C' C3
,
010D'
010D' 80 26 0001" F9
0112' A0 0001"
0115' E6 00
0117' C3
,
0118'
0118' 9C
0119' FA
011A' A0 0001"
011D' 24 3F
011F' 0C 80
0121' E6 00
0123' A2 0001"
0126' 9D
0127' 80 DB
0129' E6 20
012B' 80 0A
012D'
012D' FE CB
012F' 75 FC
0131' E4 20
0133' C3
,
0134' 80 20
0136' E6 11
013B' 80 CA 0C*
013B' CF
,
013C' 80 26 0001" FD
0141' A0 0001"
0144' E6 00
0146' 80 CA 0D*
0149' 80 20
014B' E6 1B
014D' CF
,
014E' E4 20
0150' 80 D0
0152' E6 20
0154' 80 CA 0F*
0157' EB F0
,
492 AL,PRCO_M ; DISABLE PARALLEL PORT INTERRUPT.
493 OUT PRCO_P,AL ;
494 RET ;
495 ,
496 TIMRST:
497 AND DSKC_M,NOT (TMR1EN+TMR2EN) ; ; Disable timer 1 and 2.
498 MOV AL,DSKC_M ; ;
499 OUT DSKC_P,AL ; ;
500 RET ;
501 ,
502 FORCIT:
503 PUSHF ; Save carry and interrupt status.
504 CLI ;
505 MOV AL,DSKC_M ; ; READ CURRENT MEMORY STATE.
506 AND AL,MMASK ; ; MASK OFF CONTROL BITS.
507 OR AL,ACCFDC ; ; SET UP FOR CPU (---) FDC.
508 OUT DSKC_P,AL ; ; SET THE LATCH.
509 MOV DSKC_M,AL ; ; UPDATE MEMORY STATE.
510 POPF ; ; RESTORE INTERRUPT STATUS.
511 MOV AL,IMMINT ; ; CAUSE IMMEDIATE INTERRUPT.
512 OUT FDCCMD,AL ; ;
513 MOV AL,IO ; ; WAIT FOR CMD TO EXECUTE.
514 INTDLY:
515 AL ; ;
516 INTDLY ; ;
517 IN AL,FDCSTA ; ; READ STATUS TO CLEAR INTERRUPT.
518 RET ;
519 ,
520 ,
521 ,
522 NMIVCT: MOV AL,UC_RTB ; ; DISABLE FURTHER NMI'S.
523 OUT KBCMD,AL ; ;
524 OR DL,NMIERR ; ; INDICATE CORRECT OCCURRENCE.
525 IRET ;
526 ,
527 ,
528 ,
529 TIMRIN: AND DSKC_M,NOT TMR1EN ; ; DISABLE TIMER INTERRUPT.
530 MOV AL,DSKC_M ; ;
531 OUT DSKC_P,AL ; ;
532 OR DL,TIMERR ; ; INDICATE CORRECT OCCURRENCE
533 MOV AL,EOI ; ;
534 OUT INTA00,AL ; ;
535 IRET ;
536 ,
537 ,
538 ,
539 FLPINT: IN AL,FDCSTA ; ; READ FLOPPY STATUS TO RESET INT.
540 MOV AL,FORCED ; ; RESET FLOPPY INTERRUPT.
541 OUT FDCCMD,AL ; ;
542 OR DL,FLTIERR ; ; INDICATE CORRECT OCCURRENCE
543 JMP COMNI ; ;

```

```
544 ;  
545 ;  
546 ;  
547 KBDINT: MOV AL,(UC_RTB) ; ; ; DISABLE KEY INT.  
548 OUT KBCMD,AL ; ; ;  
549 OR DL,KEYERR ; ; ; INDICATE CORRECT OCCURRENCE  
550 JMP COMNI ; ; ; COMMON INTERRUPT EXIT  
551 ;  
552 ;  
553 ;  
554 INVINT: NOT DL ; ; ; BETS ANY UNTESTED BITS IN ERROR.  
555 ; ; ; NUMBER OF GOOD BITS INDICATES  
556 ; ; ; PROGRESS THROUGH THE TEST  
557 ; OR DL,IVERR ; ; ; SIGNAL ERROR  
558 JMP FATAL ; ; ; JUMP AND REPORT ERROR  
559 END
```

No errors detected

```
1 ; *****  
2 ; TITLE - INTXIT (COMMON INTERRUPT EXIT LOGIC)  
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE  
4 ; ABSTRACT - This module contains the common interrupt exit logic executed  
5 ; by all interrupt processors. The address of this routine is stored  
6 ; in an interrupt vector location (interrupt XITVEC) in segment ABSO  
7 ; (Absolute Zero) so that it is accessible to application programs.  
8 ; This logic decrements the interrupt counter and saves the location  
9 ; of the stack of the interrupted logic. The interrupt exit logic  
10 ; then restores commonly used registers from the interrupt stack,  
11 ; switches back to the stack of the interrupted logic, and restores  
12 ; register DS from the user stack.  
13 ;  
14 ; Also contained in this module is the dummy entry point used by  
15 ; soft interrupts SLCINT and TIMINT when ZEX is not present in the  
16 ; system. This logic simply does an IRET.  
17 ;  
18 ; INPUTS - EB, BX = SS, SP of interrupted logic  
19 ; Interrupt stack contains saved ES, BX, AX (EB at top of stack).  
20 ; Stack of interrupted logic contains saved DS.  
21 ;  
22 ; OUTPUTS -  
23 ;  
24 ; REGISTERS USED -  
25 ;  
26 ; STACK USED -  
27 ;  
28 ; *****  
29 ; NAME INTXIT  
30 ; SUBTTL  
31 ; *****  
32 ; PUBLIC DEFINITIONS  
33 ;  
34 ; *****  
35 ;  
36 ; PUBLIC INTRET  
37 ; PUBLIC INTXIT  
38 ;  
39 ; *****  
40 ; EXTERNAL REFERENCES  
41 ;  
42 ; *****  
43 ;  
44 ; SECT ROMDAT ; Where Interrupt Exit saves BP (ROMDAT)  
45 ; EXTERN IXSPSV: WORD ;  
46 ; EXTERN IXSSV: WORD ; Where Interrupt Exit saves SS (ROMDAT)
```

```
47 ; ***** LOCAL CONSTANTS *****
48 ;
49 ; INCLUDE PEG:VECTOR.EGU
50 ; INCLUDE PEG:INTCLR.EGU
51 ; *****
173 SUBTTL MAIN
174 ; *****
175 ; MODULE ENTRY POINT
176 ; *****
177 ;
178 SECT ROMCOD
179 ASSUME CS:ROMCOD
180 ASSUME DS:NOTHING
181
0000' INTXIT PROC FAR , EB, BX = ORIGINAL SS, SP
0000' CLI , ; DISABLE INTERRUPTS
0001' MOV AL, EDI , ; RESET 8259
0003' OUT INTA00, AL , ;
0005' DEC BYTE PTR CB: INTCTR+CBWRAP, , ; DECREMENT INTERRUPT COUNTER
187 ;
188 ABBUME DS:ROMDAT
189 MOV DS, WORD PTR CB: DSADDR+CBWRAP, , ; DS = ROM DATA SEGMENT
190 MOV IXBSSV, ES , ; SAVE SS, BP OF ORIGINAL LOGIC
191 MOV IXSPSV, BX , ;
192 POP ES , ; RESTORE COMMONLY USED REGISTERS
193 POP BX , ; FROM INTERRUPT STACK
194 POP AX , ;
195 MOV SS, IXBSSV , ; RESTORE ORIGINAL SS, BP
196 MOV SP, IXSPSV , ; RESTORE DS FROM ORIGINAL STACK
197 POP DS , ; ** INTERRUPT RETURN **
198 ;
199 ;
200 ;
201 ; *****
202 ; INTRET is the logic that is executed by a soft interrupts to BLCINT
203 ; and TIMINT
204 ; *****
205 ;
206 ;
207 INTRET PROC FAR , Dummy soft interrupt
208 IRET , ; ** INTERRUPT RETURN **
209 ;
210 SUBTTL
211 END
212
```

No errors detected

```

1 ;*****
2 ; TITLE - KBTABL - Standard keyboard encoding tables
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE (BSD Assembler)
4 ; ABSTRACT - This module contains the data tables used by the Pegasus
5 ; keyboard routines for standard driver module, the keyboard sends
6 ; As explained in the keyboard driver module, the keyboard sends
7 ; one or two bytes per key depression. The first contains the
8 ; mode key information (CTRL, SHIFT, etc.). The second is the
9 ; 'scan code' corresponding to the key's physical position on the
10 ; keyboard. These tables are arranged in order of ascending
11 ; scan codes. The mode determination is handled in the keyboard
12 ; DBR code and the correct table accessed for encoding. The
13 ; keyboard layout charts below map the physical locations of
14 ; the keys to the scan codes. If the byte looked up is -1 (OFFH),
15 ; the key is to be ignored. If the byte is greater than 7FH, but
16 ; less than OFCH (i.e. the high bit is set), then the low order 7
17 ; bits are used as an index into the KBFUNC table to retrieve
18 ; the function key code. The other possible values are defined
19 ; as follows: OFEH is the Program Pause function, OFDH is the
20 ; Program Break function, and OFCH is the Print Screen function.
21 ;
22 ;*****
23 ; NAME KBTABL - Standard keyboard encoding tables
24 ; SUBTTL
25 ;*****
26 ;*****
27 ; PUBLIC DEFINITIONS
28 ;*****
29 PUBLIC KB_ALT
30 PUBLIC KB_CTRL
31 PUBLIC KB_FUNC
32 PUBLIC KB_NUMK
33 PUBLIC KB_SHIFT
34 PUBLIC KB_UNSH
35 ;
36 ;*****
37 ; LOCAL CONSTANTS
38 ;*****
39 INCLUDE PEG:ASC11.EGU
40 INCLUDE PEG:KBSPLC.EGU
41 INCLUDE PEG:KBMISC.EGU
42 ;
246 ;
247 ;
248 ;
249 ;
250 ;
251 ;
252 ;
253 ;
254 ;
255 ;
256 ;
  
```

GENERAL KEYBOARD POSITION NUMBERING:

(left half)

1	1	1	1	1	1	1	1	1	1			
1101102110311041	1	1	2	3	4	1	1	5	6	7	8	1




```

309 I E S C I 1 1 2 1 3 1 4 1 5 1 6 1 7 1 8 1 9 1 0 1 - 1 = 1 B P C E I ' |
310 |
311 | I | I | I | I | I | I | I | I | I | I | I | I | I | I | I | L |
312 | I T A B I Q I W I E I R I T I Y I U I I O I P I C I J I I F I
313 |
314 | I C A P I I I I I I I I I I I I I I I I I I I I I I I I I I I I I I
315 | I C T L I O K I A I B I D I F I Q I H I J I K I L I I I I I I I I I I I I
316 |
317 | I I I I I I I I I I I I I I I I I I I I I I I I I I I I I I I I I I I
318 | I A L T I B I H I F T I Z I X I C I V I B I N I M I . I . I / I B I S H I F T I
319 |
320 |
321 | I I ( SPACE BAR ) I I
322 |
323 |
324 |
325 |
326 |
327 |
328 |
329 |
330 |
331 |
332 |
333 |
334 |
335 |
336 |
337 |
338 |
339 |
340 |
341 |
342 |
343 |
344 |
345 |
346 |
347 |
  
```

(right half)

```

I I I B R K I I
I I N S I D E L I P A U I P R T I
I I I I I I I
I - I + I B P C I T A B I
I I I I I I I
I 7 I 8 I 9 I - I
I I I I I I I
I 4 I 5 I 6 I . I
I I I I I E I
I I 2 I 3 I N I
I I I I I T I
I 0 I I I R I
  
```

THE TABLES:

SECT RDMCOD

UNSHIFTed (normal) table - Note that the value 'xx' in keys defined as 'xx+80H' is to be used as an index into KBFUNC for the actual value.

KBUNSH	LABEL	BYTE	Key #	Lengend:
0000'	A0	32+80H	01	F5 key
0001'	A1	33+80H	02	F6 key
0002'	A2	34+80H	03	F7 key
0003'	A3	35+80H	04	F8 key

-0000

0004'	A4	361	DB	36+80H	05	F9 key
0005'	A5	362	DB	37+80H	06	F10 key
0006'	F3	363	DB	115+80H	07	F11 key
0007'	F4	364	DB	116+80H	08	F12 key
0008'	31	365	DB	'1'	09	1 / !
0009'	32	366	DB	'2'	10	2 / @
000A'	33	367	DB	'3'	11	3 / #
000B'	34	368	DB	'4'	12	4 / \$
000C'	35	369	DB	'5'	13	5 / %
000D'	36	370	DB	'6'	14	6 / ^
000E'	37	371	DB	'7'	15	7 / &
000F'	38	372	DB	'8'	16	8 / *
0010'	39	373	DB	'9'	17	9 / (
0011'	30	374	DB	'0'	18	0 /)
0012'	2D	375	DB	'-'	19	- / +
0013'	3D	376	DB	'='	20	= / +
0014'	08	377	DB	88	21	BACK SPACE key
0015'	60	378	DB	'\'	22	\ / ~
0016'	3D	379	DB	'='	23	NUM = (numeric pad)
0017'	28	380	DB	'+'	24	NUM +
0018'	20	381	DB	SPACE	25	NUM SPACE key
0019'	09	382	DB	HT	26	NUM TAB key
001A'	31	383	DB	'1'	27	NUM 1
001B'	FF	384	DB	-1	28	---- (not implemented)
001C'	30	385	DB	'0'	29	NUM 0
001D'	0D	386	DB	CR	30	NUM ENTER key
001E'	34	387	DB	'4'	31	NUM 4
001F'	35	388	DB	'5'	32	NUM 5
0020'	39	389	DB	'9'	33	NUM 9
0021'	2D	390	DB	'-'	34	NUM -
0022'	32	391	DB	'2'	35	NUM 2
0023'	FF	392	DB	-1	36	---- (not implemented)
0024'	FF	393	DB	-1	37	---- (not implemented)
0025'	FF	394	DB	-1	38	---- (not implemented)
0026'	37	395	DB	'7'	39	NUM 7
0027'	38	396	DB	'8'	40	NUM 8
0028'	36	397	DB	'6'	41	NUM 6
0029'	2C	398	DB	'.'	42	NUM .
002A'	33	399	DB	'3'	43	NUM 3
002B'	2E	400	DB	'.'	44	NUM .
002C'	CE	401	DB	7B+80H	45	PRINT key (duplicate of 90)
002D'	AA	402	DB	42+80H	46	Cursor RIGHT key
002E'	AE	403	DB	46+80H	47	Insert key
002F'	AF	404	DB	47+80H	48	DEleta key
0030'	0F	405	DB	HT	49	TAB key
0031'	71	406	DB	'q'	50	Q
0032'	77	407	DB	'w'	51	W
0033'	65	408	DB	'e'	52	E
0034'	72	409	DB	'r'	53	R
0035'	74	410	DB	't'	54	T
0036'	79	411	DB	'y'	55	Y
0037'	75	412	DB	'u'	56	U

Index	Character	DB	Index	Character	DB	Index	Character	DB
0038'	69	DB	103	F3 key	DB			
0039'	6F	DB	104	F4 key	DB			
003A'	70	DB	105	F5 key	DB			
003B'	5B	DB	106	F6 key	DB			
003C'	5D	DB	107	F7 key	DB			
003D'	0A	DB	108	F8 key	DB			
003E'	FE	DB	109	F9 key	DB			
003F'	A7	DB	110	F10 key	DB			
0040'	1B	DB	111	F11 key	DB			
0041'	61	DB	112	F12 key	DB			
0042'	73	DB	113	Cursor UP key	DB			
0043'	64	DB	114	Cursor DOWN key	DB			
0044'	66	DB	115	Cursor LEFT key	DB			
0045'	67	DB	116	Cursor RIGHT key	DB			
0046'	68	DB	117	HOME key	DB			
0047'	6A	DB	118	SPACE bar	DB			
0048'	6B	DB	119	RETURN key	DB			
0049'	6C	DB	120	Cursor LEFT key	DB			
004A'	3B	DB	121	Cursor LEFT key	DB			
004B'	27	DB	122	HOME key	DB			
004C'	0D	DB	123	SPACE bar	DB			
004D'	5C	DB	124	RETURN key	DB			
004E'	A9	DB	125	Cursor LEFT key	DB			
004F'	A6	DB	126	Cursor LEFT key	DB			
0050'	20	DB	127	HOME key	DB			
0051'	7A	DB	128	SPACE bar	DB			
0052'	7B	DB	129	RETURN key	DB			
0053'	63	DB	130	Cursor LEFT key	DB			
0054'	76	DB	131	Cursor LEFT key	DB			
0055'	62	DB	132	HOME key	DB			
0056'	6E	DB	133	SPACE bar	DB			
0057'	6D	DB	134	RETURN key	DB			
0058'	2C	DB	135	Cursor LEFT key	DB			
0059'	CE	DB	136	Cursor LEFT key	DB			
005A'	2E	DB	137	HOME key	DB			
005B'	2F	DB	138	SPACE bar	DB			
005C'	FF	DB	139	RETURN key	DB			
005D'	AF	DB	140	Cursor LEFT key	DB			
005E'	AE	DB	141	Cursor LEFT key	DB			
005F'	AC	DB	142	HOME key	DB			
0060'	FC	DB	143	SPACE bar	DB			
0061'	FF	DB	144	RETURN key	DB			
0062'	FF	DB	145	Cursor LEFT key	DB			
0063'	FE	DB	146	Cursor LEFT key	DB			
0064'	9C	DB	147	HOME key	DB			
0065'	9D	DB	148	SPACE bar	DB			
0066'	9E	DB	149	RETURN key	DB			
0067'	9F	DB	150	Cursor LEFT key	DB			

SHIFTed Mode table - Note that the value 'xx' in keys defined as 'xx+BOH' is to be used as an index into KBFUNC for the actual value.

009B'	45	DB	'E'	52	E
009C'	52	DB	'R'	53	R
009D'	54	DB	'T'	54	T
009E'	59	DB	'Y'	55	Y
009F'	55	DB	'U'	56	U
00A0'	49	DB	'I'	57	I
00A1'	4F	DB	'O'	58	O
00A2'	50	DB	'P'	59	P
00A3'	78	DB	'{'	60	[/ {
00A4'	7D	DB	'}'	61] / }
00A5'	0A	DB	LF	62	LINE FEED key
00A6'	FD	DB	PBKFNC	63	BREAK/PAUSE key (duplicate of 100)
00A7'	EF	DB	111+80H	64	Cursor UP key
00A8'	1B	DB	EBC_	65	EBC key
00A9'	41	DB	'A'	66	A
00AA'	53	DB	'B'	67	B
00AB'	44	DB	'D'	68	D
00AC'	46	DB	'F'	69	F
00AD'	47	DB	'O'	70	O
00AE'	48	DB	'H'	71	H
00AF'	4A	DB	'J'	72	J
00B0'	4B	DB	'K'	73	K
00B1'	4C	DB	'L'	74	L
00B2'	3A	DB	'.'	75	. / :
00B3'	22	DB	'/'	76	' / "
00B4'	0D	DB	CR	77	RETURN key
00B5'	7C	DB	'I'	78	\ /
00B6'	F2	DB	114+80H	79	Cursor LEFT key
00B7'	EE	DB	110+80H	80	HOME key
00B8'	20	DB	SPACE	81	SPACE bar
00B9'	5A	DB	'Z'	82	Z
00BA'	58	DB	'X'	83	X
00BB'	43	DB	'C'	84	C
00BC'	56	DB	'V'	85	V
00BD'	42	DB	'B'	86	B
00BE'	4E	DB	'N'	87	N
00BF'	4D	DB	'M'	88	M
00C0'	3C	DB	'('	89	, / (
00C1'	FC	DB	PBCFNC	90	PRINT key
00C2'	3E	DB	'\'	91	. /)
00C3'	3F	DB	'/'	92	/ / ?
00C4'	FF	DB	-1	93	---- (not implemented)
00C5'	E8	DB	104+80H	94	Delete key (duplicate of 48)
00C6'	E5	DB	101+80H	95	Insert key (duplicate of 47)
00C7'	F0	DB	112+80H	96	Cursor DOWN key
00C8'	FF	DB	-1	97	---- (not implemented)
00C9'	FF	DB	-1	98	---- (not implemented)
00CA'	FF	DB	-1	99	---- (not implemented)
00CB'	FD	DB	PBKFNC	100	BREAK/PAUSE key
00CC'	B0	DB	4B+80H	101	F1 key
00CD'	B1	DB	49+80H	102	F2 key
00CE'	B2	DB	50+80H	103	F3 key

00CF' B3	569	DB	51+80H	104	F4 key
00D0'	570	DB	62+80H	01	F5 key
00D1'	571	DB	63+80H	02	F6 key
00D2'	572	DB	64+80H	03	F7 key
00D3'	573	DB	65+80H	04	F8 key
00D4'	574	DB	66+80H	05	F9 key
00D5'	575	DB	67+80H	06	F10 key
00D6'	576	DB	119+80H	07	F11 key
00D7'	577	DB	120+80H	08	F12 key
00D8'	578	DB	00+80H	09	1 / !
00D9'	579	DB	-1	10	2 / @
00DA'	580	DB	-1	11	3 / #
00DB'	581	DB	-1	12	4 / \$
00DC'	582	DB	RS	13	5 / %
00DD'	583	DB	-1	14	6 / ^
00DE'	584	DB	-1	15	7 / &
00DF'	585	DB	-1	16	8 / *
00E0'	586	DB	-1	17	9 / (
00E1'	587	DB	US	18	0 /)
00E2'	588	DB	-1	19	- / -
00E3'	589	DB	-1	20	= / +
00E4'	590	DB	DEL	21	BACK SPACE key
00E5'	591	DB	'='	22	NUM = (numeric pad)
00E6'	592	DB	'+'	23	NUM +
00E7'	593	DB	SPACE	24	NUM SPACE key
00E8'	594	DB	HT	25	NUM TAB key
00E9'	595	DB	'1'	26	NUM 1
00EA'	596	DB	-1	27	----- (not implemented)
00EB'	597	DB	'0'	28	NUM 0
00EC'	598	DB	CR	29	NUM ENTER key
00ED'	599	DB	'4'	30	NUM 4
00EE'	600	DB	'5'	31	NUM 5
00EF'	601	DB	'9'	32	NUM 9
00F0'	602	DB	'-'	33	NUM -
00F1'	603	DB	'2'	34	NUM 2
00F2'	604	DB	-1	35	----- (not implemented)
00F3'	605	DB	-1	36	----- (not implemented)
00F4'	606	DB	-1	37	----- (not implemented)
00F5'	607	DB	-1	38	----- (not implemented)
00F6'	608	DB	'7'	39	NUM 7
00F7'	609	DB	'8'	40	NUM 8
00F8'	610	DB	'6'	41	NUM 6
00F9'	611	DB	'.'	42	NUM .
00FA'	612	DB	'3'	43	NUM 3
00FB'	613	DB	'.'	44	NUM -
00FC'	614	DB	-1	45	PRINT key (duplicate of 90)
00FD'	615	DB	80+80H	46	Cursor RIGHT key

0132'	FF	673	DB	-1	99	(not implemented)
0133'	FF	674	DB	-1	100	BREAK/PAUSE key
0134'	BA	675	DB	5B+80H	101	F1 key
0135'	BB	676	DB	59+80H	102	F2 key
0136'	BC	677	DB	60+80H	103	F3 key
0137'	BD	678	DB	61+80H	104	F4 key
679						
680						

ALTERNATE Mode table - Note that the value 'xx' in keys defined as 'xx+80H' is to be used as an index into KBFUNC for the actual value.

KB_ALT	LABEL	BYTE	Key #	Legend
0138'	CB	72+80H	01	F5 key
0138'	CB	73+80H	02	F6 key
0139'	C9	74+80H	03	F7 key
013A'	CA	75+80H	04	F8 key
013B'	CB	76+80H	05	F9 key
013C'	CC	77+80H	06	F10 key
013D'	CD	121+80H	07	F11 key
013E'	F9	122+80H	08	F12 key
013F'	FA	84+80H	09	! /
0140'	D4	85+80H	10	@ /
0141'	D5	86+80H	11	# /
0142'	D6	87+80H	12	\$ /
0143'	D7	88+80H	13	% /
0144'	D8	89+80H	14	^ /
0145'	D9	90+80H	15	& /
0146'	DA	91+80H	16	* /
0147'	DB	92+80H	17	(/
0148'	DC	93+80H	18) /
0149'	DD	94+80H	19	- /
014A'	DE	95+80H	20	= /
014B'	DF	-1	21	BACK SPACE key
014C'	FF	-1	22	/ ~
014D'	FF	-1	23	NUM = (numeric pad)
014E'	E1	97+80H	24	NUM +
014F'	E2	98+80H	25	NUM SPACE key
0150'	E3	99+80H	26	NUM TAB key
0151'	E4	100+80H	27	NUM 1
0152'	FF	-1	28	(not implemented)
0153'	FF	-1	29	NUM 0
0154'	FF	-1	30	NUM ENTER key
0155'	FF	-1	31	NUM 4
0156'	FF	-1	32	NUM 5
0157'	FF	-1	33	NUM 9
0158'	FF	-1	34	NUM -
0159'	FF	-1	35	NUM 2
015A'	FF	-1	36	-----
015B'	FF	-1	37	(not implemented)
015C'	FF	-1	38	(not implemented)
015D'	FF	-1	39	(not implemented)
015E'	FF	-1	40	NUM 7
015F'	FF	-1		NUM 8

0194'	FF	777	DB	-1	93	---	(not implemented)
0195'	EA	778	DB	106+80H	94	---	Delete key (duplicate of 48)
0196'	E7	779	DB	103+80H	95	---	Insert key (duplicate of 47)
0197'	AD	780	DB	45+80H	96	---	Cursor DOWN key
0198'	FF	781	DB	-1	97	---	(not implemented)
0199'	FF	782	DB	-1	98	---	(not implemented)
019A'	FF	783	DB	-1	99	---	(not implemented)
019B'	FF	784	DB	-1	100	---	BREAK/PAUSE key
019C'	C4	785	DB	68+80H	101	---	F1 key
019D'	C5	786	DB	69+80H	102	---	F2 key
019E'	C6	787	DB	70+80H	103	---	F3 key
019F'	C7	788	DB	71+80H	104	---	F4 key
789		789					
790		790					
791		791					
792		792					
793		793					
794		794					
795		795					
796		796					
797		797					
798		798					
799		799					
800		800					
801		801					
802		802					
803		803					
804		804					
805		805					
806		806					
807		807					
808		808					
809		809					
810		810					
811		811					
812		812					
813		813					
814		814					
815		815					
816		816					
817		817					
818		818					
819		819					
820		820					
821		821					
822		822					
823		823					
824		824					
825		825					
826		826					
827		827					
828		828					

FUNCTION key lookup table - If the value in the normal table is greater than 7FH but less than OFCH, the lower 7 bits of the byte is used as an index into this table to find the actual function value.

KBFUNC	LABEL	BYTE	TABLE OFFSET:
01A0'	DB	KB_NUL	0
01A0'	DB	KBBKTB	1
01A1'	DB	KBALTB	2
01A2'	DB	KBALTB	3
01A3'	DB	KBALTB	4
01A4'	DB	KBALTB	5
01A5'	DB	KBALTB	6
01A6'	DB	KBALTB	7
01A7'	DB	KBALTB	8
01A8'	DB	KBALTB	9
01A9'	DB	KBALTB	10
01AA'	DB	KBALTB	11
01AB'	DB	KBALTB	12
01AC'	DB	KBALTB	13
01AD'	DB	KBALTB	14
01AE'	DB	KBALTB	15
01AF'	DB	KBALTB	16
01B0'	DB	KBALTB	17
01B1'	DB	KBALTB	18
01B2'	DB	KBALTB	19
01B3'	DB	KBALTB	20
01B4'	DB	KBALTB	21
01B5'	DB	KBALTB	22
01B6'	DB	KBALTB	23
01B7'	DB	KBALTB	24
01B8'	DB	KBALTB	25
01B9'	DB	KBALTB	26
01BA'	DB	KBALTB	27
01BB'	DB	KBALTB	28
01BC'	DB	KB_F1	29
01BD'	DB	KB_F2	30
01BE'	DB	KB_F3	31
01BF'	DB	KB_F4	31

01C0'	3F	829	DB	KB_F5	32
01C1'	40	830	DB	KB_F6	33
01C2'	41	831	DB	KB_F7	34
01C3'	42	832	DB	KB_F8	35
01C4'	43	833	DB	KB_F9	36
01C5'	44	834	DB	KB_F10	37
01C6'	47	835	DB	KB_HOM	38
01C7'	48	836	DB	KB_CUP	39
01C8'	49	837	DB	KBACUP	40
01C9'	4B	838	DB	KB_CLF	41
01CA'	4D	839	DB	KB_GRT	42
01CB'	4F	840	DB	KBALF	43
01CC'	50	841	DB	KB_CDN	44
01CD'	51	842	DB	KBACDN	45
01CE'	52	843	DB	KB_INS	46
01CF'	53	844	DB	KB_DEL	47
01D0'	54	845	DB	KB_BF1	48
01D1'	55	846	DB	KB_BF2	49
01D2'	56	847	DB	KB_BF3	50
01D3'	57	848	DB	KB_BF4	51
01D4'	58	849	DB	KB_BF5	52
01D5'	59	850	DB	KB_BF6	53
01D6'	5A	851	DB	KB_BF7	54
01D7'	5B	852	DB	KB_BF8	55
01D8'	5C	853	DB	KB_BF9	56
01D9'	5D	854	DB	KB_BF10	57
01DA'	5E	855	DB	KB_C_F1	58
01DB'	5F	856	DB	KB_C_F2	59
01DC'	60	857	DB	KB_C_F3	60
01DD'	61	858	DB	KB_C_F4	61
01DE'	62	859	DB	KB_C_F5	62
01DF'	63	860	DB	KB_C_F6	63
01E0'	64	861	DB	KB_C_F7	64
01E1'	65	862	DB	KB_C_F8	65
01E2'	66	863	DB	KB_C_F9	66
01E3'	67	864	DB	KB_C_F10	67
01E4'	68	865	DB	KB_AF1	68
01E5'	69	866	DB	KB_AF2	69
01E6'	6A	867	DB	KB_AF3	70
01E7'	6A	868	DB	KB_AF4	71
01E8'	6C	869	DB	KB_AF5	72
01E9'	6D	870	DB	KB_AF6	73
01EA'	6E	871	DB	KB_AF7	74
01EB'	6F	872	DB	KB_AF8	75
01EC'	70	873	DB	KB_AF9	76
01ED'	71	874	DB	KB_AF10	77
01EE'	72	875	DB	KBPTOL	78
01EF'	73	876	DB	KBCCLF	79
01F0'	74	877	DB	KBCCRT	80
01F1'	75	878	DB	KBCLF	81
01F2'	76	879	DB	KBCCDN	82
01F3'	77	880	DB	KBCHOM	83

KBTABL - Standard keyboard encoding tables
KBTABL.SRC

01F4'	78	881	DB	KBALTI	84
01F5'	79	882	DB	KBALT2	85
01F6'	7A	883	DB	KBALT3	86
01F7'	7B	884	DB	KBALT4	87
01F8'	7C	885	DB	KBALT5	88
01F9'	7D	886	DB	KBALT6	89
01FA'	7E	887	DB	KBALT7	90
01FB'	7F	888	DB	KBALT8	91
01FC'	80	889	DB	KBALT9	92
01FD'	81	890	DB	KBALTO	93
01FE'	82	891	DB	KBADSH	94
01FF'	83	892	DB	KBAEGU	95
0200'	84	893	DB	KBCCUP	96
0201'	8C	894	DB	KB_PFI	97
0202'	8D	895	DB	KB_PFI2	98
0203'	8E	896	DB	KB_PFI3	99
0204'	8F	897	DB	KB_PFI4	100
0205'	28	898	DB	KBSINS	101
0206'	29	899	DB	KBCINS	102
0207'	2A	900	DB	KBAINS	103
0208'	38	901	DB	KBSDEL	104
0209'	39	902	DB	KBCDEL	105
020A'	3A	903	DB	KBADEL	106
020B'	4C	904	DB	KBACLF	107
020C'	4E	905	DB	KBACRT	108
020D'	85	906	DB	KBAHOM	109
020E'	86	907	DB	KBSHOM	110
020F'	88	908	DB	KBSCUP	111
0210'	89	909	DB	KBSCDN	112
0211'	8A	910	DB	KBSCRT	113
0212'	8B	911	DB	KBSCLF	114
0213'	45	912	DB	KB_F11	115
0214'	46	913	DB	KB_F12	116
0215'	08	914	DB	KBSF11	117
0216'	09	915	DB	KBSF12	118
0217'	0A	916	DB	KBCF11	119
0218'	0B	917	DB	KBCF12	120
0219'	0C	918	DB	KBAF11	121
021A'	0D	919	DB	KBAF12	122
021B'	1D	920			
021B'	1D	921			
021C'	1B	922			
021C'	1B	923			
021D'	23	924			
021D'	23	925			
021D'	23	926			
021D'	23	927			
021B'	1D	928	KBNUMK	BYTE	
021B'	1D	929	DB	29	Scan code for NUMERIC 0 key
021C'	1B	930	DB	27	Scan code for NUMERIC 1 key
021D'	23	931	DB	35	Scan code for NUMERIC 2 key
021D'	23	932	DB		

Table of the NUMERIC keys in ascending order according to their legends.
This table is used by the keyboard DSR to implement the special ALT
NUM-pad function of allowing a 3-digit decimal value to be typed on
the numeric pad while the ALT key is held down. The resulting value
passed directly to the DSR caller.

021E' 28	933	DB	43	;	Scan code for NUMERIC 3 key
021F' 1F	934	DB	31	;	Scan code for NUMERIC 4 key
0220' 20	935	DB	32	;	Scan code for NUMERIC 5 key
0221' 29	936	DB	41	;	Scan code for NUMERIC 6 key
0222' 27	937	DB	39	;	Scan code for NUMERIC 7 key
0223' 28	938	DB	40	;	Scan code for NUMERIC 8 key
0224' 21	939	DB	33	;	Scan code for NUMERIC 9 key
	940				
	941	END			

No errors detected

```

1 ;*****
2 ; TITLE - KEYDSR - Keyboard device service routine
3 ; COMPUTER - BOBB Assembly Language
4 ;
5 ; ABSTRACT - This module contains the keyboard driver logic for the
6 ; Pegasus system ROM.
7 ;
8 ;*****
9 ; NAME KEYDSR - Keyboard device service routine
10 ;*****
11 ; PUBLIC DEFINITIONS
12 ;*****
13 ;
14 ;
15 PUBLIC FLUSH
16 PUBLIC KEY_ID
17 PUBLIC KEYIN
18 PUBLIC KEYINI
19 PUBLIC KEYISR
20 PUBLIC KEYOUT
21 PUBLIC KEYRBT
22 PUBLIC KPAUSE
23 PUBLIC STATUS
24 ;
25 ;*****
26 ; EXTERNAL REFERENCES
27 ;*****
28 ;
29 ;*****
30 ; ROMCOD
31 ; DELAY: NEAR ; 1 millisecond delay routine (ROMUTL)
32 ; PWRUP: FAR ; Keyboard reset causes system restart (REBET)
33 ; BBEEP: NEAR ; System beep routine (BELDSR)
34 ;
35 ;*****
36 ; ROMCOD
37 ; KB_ALT: BYTE ; Keyboard Alternate-Mode lookup table (KBTABL)
38 ; KBCTRL: BYTE ; Keyboard Control-Mode lookup table (KBTABL)
39 ; KBFUNC: BYTE ; Keyboard Function key lookup table (KBTABL)
40 ; KBNUMK: BYTE ; Keyboard Numeric pad keys table (KBTABL)
41 ; KBSHFT: BYTE ; Keyboard Shifted-Mode lookup table (KBTABL)
42 ; KBUNSH: BYTE ; Keyboard Unshifted-Mode lookup table (KBTABL)
43 ;
44 ;*****
45 ; ROMDAT
46 ; CRT screen hold flag (set by KPAUSE) (ROMDAT)
47 ; End of the keyboard type-ahead buffer (ROMDAT)
48 ; Status byte for Keyboard driver (ROMDAT)
49 ; Keyboard interrupt routine SP save (ROMDAT)
50 ; Keyboard interrupt routine SS save (ROMDAT)
51 ; Keyboard interrupt routine stack (ROMDAT)
52 ; Count of ALT/NUM keystrokes (ROMDAT)
53 ; Accumulator for ALT/NUM keystrokes (ROMDAT)
54 ; Current depth of queue (# chars) (ROMDAT)
55 ; Pointer to 'front' of keyboard buffer (ROMDAT)
56 ; Pointer to 'rear' of keyboard buffer (ROMDAT)

```

-0000

-0000

-0000




```
54 EXTRN QUEUE:WORD , Beginning of type-ahead buffer (ROMDAT)
55 *****
56 LOCAL CONSTANTS
57 *****
58 INCLUDE PEG:PORTADDR.EQU
59 INCLUDE PEG:UBART.EQU
60 INCLUDE PEG:INTCLR.EQU
61 INCLUDE PEG:KSPCL.EQU
62 INCLUDE PEG:KBCMD.EQU
63 INCLUDE PEG:KBMS9C.EQU
64 INCLUDE PEG:VECTOR.EQU
65 *****
66 *****
67 *****
68 *****
69 *****
70 *****
71 *****
72 *****
73 *****
74 *****
75 *****
76 *****
77 *****
78 *****
79 *****
80 *****
81 *****
82 *****
83 *****
84 *****
85 *****
86 *****
87 *****
88 *****
89 *****
90 *****
91 *****
92 *****
93 *****
94 *****
95 *****
96 *****
97 *****
98 *****
99 *****
100 *****
101 *****
102 *****
103 *****
104 *****
105 *****
106 *****
107 *****
108 *****
109 *****
110 *****
111 *****
112 *****
113 *****
114 *****
115 *****
116 *****
117 *****
118 *****
119 *****
120 *****
121 *****
122 *****
123 *****
124 *****
125 *****
126 *****
127 *****
128 *****
129 *****
130 *****
131 *****
132 *****
133 *****
134 *****
135 *****
136 *****
137 *****
138 *****
139 *****
140 *****
141 *****
142 *****
143 *****
144 *****
145 *****
```

-0000

BECT ROMCOD
ASSUME CB:ROMCOD

MODULE ENTRY POINT

KEYBOARD 'DSR' - KEYIN, STATUS, FLUSH - INT 4AH

INPUT: AH = Function (see individual routines for more detail):

- 0 - Read character from keyboard, return in AX
- 1 - Get status of keyboard:
 - ZF = 1 if no char available
 - ZF = 0 if character available & char returned in AX (character remains in buffer)
- 2 - Read keyboard shift state - returns last valid shift mode in register AL:
 - bit 0 = Control key
 - bit 1 = Alternate key
 - bit 2 = Shift key/s
 - bit 7 = Uppercase mode
- 3 - Flush keyboard buffer
- 4 - Send command character in AL to the keyboard
 - ZF = 1 if command accepted OK
 - ZF = 0 if error
- 5 - Insert 16-bit character in BX into the keyboard buffer (simulate a key being typed)
 - ZF = 0 if key was placed into the buffer
 - ZF = 1 if buffer was full (key not placed) (BX preserved for retry, but not AH)

OUTPUT: as noted above
USED: AX (BX in #5)
STACK:
ASSUME CB:ROMCOD, DS:NOTHING

```

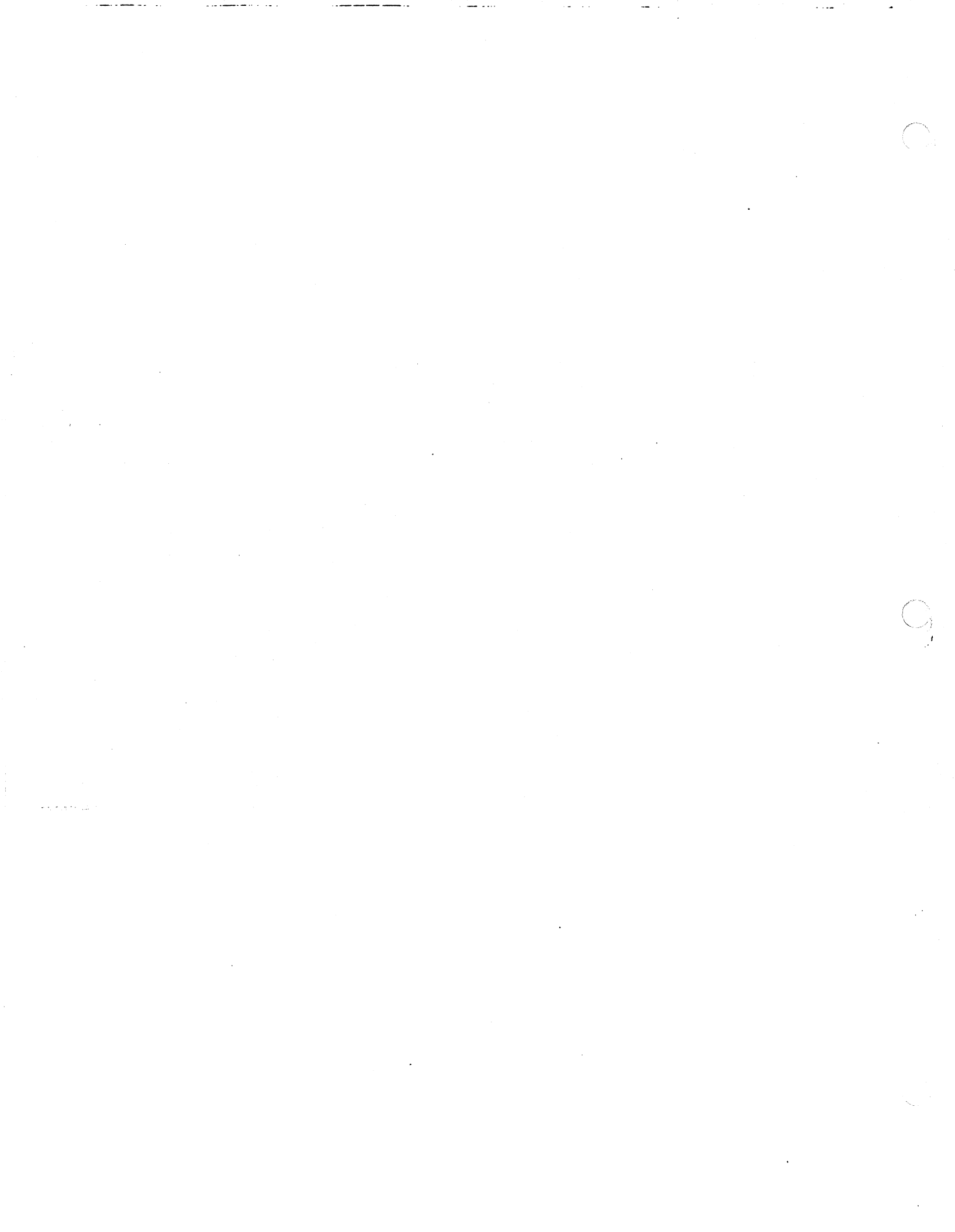
0000' 0000' KEY_10 PROC FAR
0001' FB BTI
0002' 1E PUSH
0003' 2E8E 1E C180 DB
0004' FC MOV DB,WORD PTR CB:DBADDR+CBWRAP ; set up my DB
0005' EB 0004 CLD ; forward strings
0006' 1F PDP DO_KEY
0007' CA 0002 RET DB
0008'
0009' DO_KEY PROC
0010' OR NEAR
0011' 74 15 AH, AH
0012' FE CC JZ KEYIN
0013' 74 2A FE CC DEC STATUS
0014' FE CC JZ AH - 1
0015' 74 47 FE CC DEC AH - 2
0016' FE CC JZ SHIFTS
0017' FE CC DEC AH - 3
0018' 74 47 FE CC JZ FLUSH
0019' FE CC DEC AH - 4
0020' 74 60 FE CC JZ KEYOUT
0021' FE CC DEC AH - 5
0022' 74 54 JZ KEYING
0023' C3 RET
0024'
0025' *****
0026' KEYBOARD INPUT ROUTINE
0027'
0028' INPUT: (none)
0029' OUTPUT: AX = character (waits until one is available)
0030' (ASCII values return in AL with AH=0,
0031' extended functions return with AL=0 and code in AH.)
0032'
0033' USED: AX
0034' STACK:
0035' ASSUME CB:ROMCOD, DB:ROMDAT
0036'
0037' KEYIN PROC NEAR
0038' CALL STATUS
0039' JZ KEYIN
0040' CLI
0041' PUSH BI
0042' 96 MOV BI, GFRONT
0043' 8B 36 0013" INCG
0044' 88 0020 CALL
0045' 89 36 0013" MOV GFRONT, BI
0046' FE 0E 0012" DEC GDEPTH
0047' 5E POP BI
0048' FB STI
0049' C3 RET
0050'
0051' *****
0052' KEYBOARD STATUS ROUTINE
0053' *****
0054'
0055' Loop til char ready
0056' lights out
0057'
0058' Point to current character
0059' Permanently remove from queue
0060' by updating front pointer
0061' And decrease Char-Waiting count
0062'
0063' *** RETURN ***

```

```

KEYDSR.SRC
498      INPUT: (none)
499      OUTPUT: ZF = reset (0) if ready, set (1) if not ready
500      AX = char (remains in buffer) (undefined if KB not ready)
501      USED: AX
502      STACK:
503      ASSUME CB:ROMCOD, DS:ROMDAT
504
505 STATUS PROC NEAR
506     CLI
507     PUSH SI
508     MOV SI, QFRONT
509     CMP SI, QREAR
510     JZ Q: Anything in queue ?
511     CALL JZ: return not-ready status
512     LODSB
513     OR SI, BI
514     POP SI
515     STI
516     RET
517
518 *****
519     INCG - increment queue pointer (assumes interrupts are disabled)
520     INCG:
521     INC SI
522     CMP SI, OFFSET ENDQ
523     JB JQ: quit
524     MOV SI, OFFSET QUEUE
525     MOV SI, OFFSET QUEUE
526     RET
527
528 *****
529     READ KEYBOARD SHIFT STATUS
530
531     INPUT: (none)
532     OUTPUT: AL = copy of keyboard mode byte
533     USED: AL
534     STACK:
535     ASSUME CB:ROMCOD, DS:ROMDAT
536
537 SHIFTS PROC NEAR
538     MOV AL, KBMODE
539     RET
540
541 *****
542     FLUSH THE KEYBOARD BUFFER
543
544     INPUT: (none)
545     OUTPUT: (keyboard queue empty)
546     USED: (none)
547     STACK: 4 bytes
548
549

```



```
530 ;
531 ; ASSUME CS:ROMCOD, DS:ROMDAT
532 ;
533 ; FLUSH PROC NEAR
534 ;     PUSHF
535 ;     CLI
536 ;     MOV     GREAR,OFFSET QUEUE
537 ;     MOV     GFRONT,OFFSET QUEUE
538 ;     MOV     BYTE PTR QDEPTH,00
539 ;     POPF
540 ;     RET
541 ; *****
542 ; KEYING - Put the character in BX into the keyboard queue
543 ;
544 ; INPUT:  BX = 16-bit character
545 ; OUTPUT: ZF = 0 if character was placed into the buffer
546 ;         ZF = 1 if buffer was full (character not placed, in this case)
547 ; USED:   AX
548 ; STACK:
549 ; ASSUME CS:ROMCOD, DS:ROMDAT
550 ;
551 ; KEYOUT - Keyboard output routine - used to send commands to the keyboard.
552 ; To use, place the command in register AL. The command is not range-
553 ; checked, so be sure to use a valid command. Upon return, check the
554 ; Z-flag. If it is set (ZF = 1), then everything happened OK, and the
555 ; keyboard has accepted and acted upon the command. If the Z-flag is
556 ; reset (ZF = 0), then some error has occurred. To see what caused
557 ; the error, look at AL. If AL is OFFH, then the routine timed-out
558 ; while looking for the acknowledge char from the keyboard. If AL is
559 ; OOH, then there was an error on the received character. Otherwise,
560 ; AL has the character returned by the keyboard.
561 ; INPUT:  AL = code to be sent to keyboard (see PEQ:KBCMD.EGU)
562 ;         There is no range-checking on the command
563 ; OUTPUT: AL = OFFH --) error in received character
564 ;         = OFFH --) keyboard did not respond (time out)
565 ;         else --) other error (ROM, RAM, etc.)
566 ;         ZF = 1 if OK (AL will be 70H in this case)
567 ;         ZF = 0 if error (then other reply codes apply)
568 ; USED:   AX
569 ; STACK:  8 Bytes (including call)
570 ; ASSUME DS:ROMDAT
571 ;
572 ;
573 ;
574 ;
575 ;
576 ; *****
577 ;
578 ;
579 ;
580 ;
581 ;
582 ;
583 ;
584 ;
585 ;
586 ;
587 ;
588 ;
589 ;
590 ;
591 ;
592 ;
593 ;
594 ;
595 ;
596 ;
597 ;
598 ;
599 ;
600 ;
601 ;
```



```

00D1' 3C 70      CMP     AL,KB_OK
00D3' 74 12      JZ      KO_END
00D5' AB FF      TEST    AL,OFFH
00D7' 75 0C      JNZ     KO_ERR
00D9' FE C0      INC     AL
00DB' EB 08      JMP     SHORT KO_ERR
,
00DD'          ,
00DD'          KO_BAD:
00DD' E4 10      IN      AL,KBDAT
00DF' B0 34      MOV     AL,(UC_ERR + UC_RXE + UC_RT8)
00E1' E6 11      OUT    KBCMD,AL
00E3' B0 FE      MOV     AL,OFEH
00E5'          KO_ERR:
00E5' 08 C0      OR     AL,AL
00E7'          KO_END:
00E7' E0 A9      LOOPNZ KOLoop
,
00E9' 50        PUSH   AX
00EA' 9C        PUSHF
00EB' FA        CLI
00EC' E4 19      IN     AL,INTA01
00EE' 24 7F      AND    AL,ENAB7
00F0' E6 19      OUT    INTA01,AL
00F2' 9D        POPF
00F3' 58        POP    AX
,
00F4' 59        POP    CX
00F5' 5B        POP    BX
00F6' C3        RET
,
00F7'          ; *****
00F7'          ; KEYBOARD INITIALIZATION LOGIC - resets keyboard and sets up local storage
00F7'          ; This routine assumes that the interrupt controller, UBART, and the
00F7'          ; interrupt vector have already been initialized.
00F7'          ; *****
00F7'          ; *** RETURN ***
,
00F7'          INPUT:  DB - ROMDAT
00F7'          OUTPUT: (keyboard queue empty)
00F7'          USED:   (none)
00F7'          STACK: 6 bytes
00F7'          ASSUME CB:ROMCOD, DB:ROMDAT
-----
00F7'          KEYINI  PROC  NEAR
00F7'          EB FF6C      ; Initialize the queue
00FA' 30 C0      ; Clear a reg
00FC' A2 000C    ; Init mode/status byte
00FF' A2 0011    ; Init ALT/NUM stuff
0102' A2 0010    ;
0105' E4 10      ; Read rcvr to reset any interrupt
0107' B0 34      ; AL,(UC_ERR + UC_RXE + UC_RT8)
0109' E6 11      ; Reset any possible errors
010B' C3        ; *** RETURN ***
,
00F7'          ; *****
00F7'          ; *****

```



```

706 ; KEYRST - Keyboard USART initialization routine
707 ;
708 ; INPUT: (none)
709 ; OUTPUT: (none)
710 ; USED: AX,CX,DX,SI
711 ; STACK: 2 bytes
712 ; ASSUME DS:ROMDAT
713 ;
714 ;
715 ; Table of bytes to send to USART for reset
716 ;
717 KRTABL LABEL BYTE
718 DB 00H ; Null command
719 DB 00H ; Null command
720 DB 00H ; Null command
721 DB (UC_IRB + UC_TXE + UC_RXE + UC_RTS) ; Internal reset
722 DB 7FH ; MODE=8 data bits, even parity, 1 stop bit, 64K clock)
723 DB (UC_ERR + UC_RTS) ; Reset any errors
724 KRTEND LABEL BYTE
725 KRTLEN EQU KRTEND-KRTABL ; Length of table
726 ;
727 ; The routine itself
728 ;
729 KEYRST PROC NEAR
730 MOV SI,OFFSET KRTABL ; Point to table of reset commands
731 MOV CX,OFFSET KRTLEN ; Length of table to CX
732 MOV DX,KBCMD ; Set up command port I/O address
733 ;
734 LODB CS:BYTE PTR [SI] ; Pick up the command byte
735 OUT DX,AL ; Send it to the command port
736 LOOP KRI ; 'Til they're all done
737 ;
738 ;
739 RET ; ** RETURN *** (= 5.4 usec)
740 ;
741 ; KPAUSE - Keyboard default pause key handler. Sets the CRTIHL (CRT hold)
742 ; flag when the pause key is struck in order to freeze the screen.
743 ; Note that striking any other key (including the PAUSE key) will
744 ; restart the CRT.
745 ; INPUT: none
746 ; OUTPUT: (CRT hold flag set)
747 ; USED: none
748 ; STACK: 8 bytes
749 ; ASSUME DS:ROMDAT
750 ;
751 KPAUSE PROC FAR ; (Interrupt Call)
752 PUSH DS
753 DS,WORD PTR CS:DSADDR+CBWRAP ; Point to my DS
754 TEST CRTHLD,OFFH ; Q: Is the flag already set?
755 MOV CRTHLD,-1 ; (pre)Set the flag
756 JZ KP1 ; N: Then it should be set
757 MOV CRTHLD,00 ; Y: Then it should be reset

```

```

013B' 1F
013B' FB
0139' F9
013A' CA 0002
013B' CA 0002
758 KP1: POP DB
759 BTI
760 BTC
761 RET 2
762
763
764
765
766
767
768
769
770
771
772
773
774
775
776
777
778
779
780
781
782
783
784
785
786
787
788
789
790
791
792
793
794
795
796
797
798
799
800
801
802
803
804
805
806
807
808
809

```

KEYBOARD INTERRUPT HANDLER (no regs used) - responsible for all keyboard encoding functions.

The information received from the keyboard is formatted as follows:

1	7	1	6	1	5	1	4	1	3	1	2	1	1	0	1
FIRST BYTE ('mode' byte) (not always sent)															
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Control															
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Alternate															
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Shift															
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
= 1111 (denotes 1st byte)															
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Uppercase															

1	7	1	6	1	5	1	4	1	3	1	2	1	1	0	1
SECOND BYTE ('scan' byte)															
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Scan code															
1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Repeated code (typamatic)															

ASSUME CS:ROMCOD, DS:ROMDAT

```

013E' 1E
013E' 2EBE 1E C180
013F' BC 16 000E"
0144' B9 26 000D"
0148' BC DC
014C' BE D4
014E' BC 000F"
0150' 2EFE 06 C19A
0153' 90
0158' 50
0159' 53

KEYIBR PROC FAR
PUSH DS
MOV DB,WORD PTR CS:DSADDR+CSHRAP ;;; save current DS
MOV KBSSV,SB ;;; save user's SB, SP
MOV KBSPV,SP ;;;
MOV SP,DS ;;; (Use SP as temp reg)
MOV SS,SP ;;; Local stack segment = DS
MOV SP,OFFSET KEYIBP ;;; Set up local stack
INC BYTE PTR CS:INTCTR+CSHRAP ;;; Bump the interrupt counter
PUSH AX
PUSH BX ;;; Save commonly used registers on the stack

```

0000	FF FF FF FF	DD	-1
0004	8000	DW	8000h
0006	0058 R	DW	OFFSET init
0008	006C R	DW	OFFSET doio
000A	47 45 54 4F 46 53	DB	'GETOFS'
	20 20		
0012	0000	DW	0
0014	0000	DW	0
0016	0000	DW	0
0018	0000	DW	0
001A	00	DB	0
001B	00	DB	0
001C	28 [00]	DB	40 DUP(0)
0044	00	DB	0
0045	47 65 74 6F 66 73	DB	'Getofs loaded X ',13,10,'\$'

~~BBCEBIA~~

977 E: 0001 - added
code

P. 150

6872

03 CE
0679

B-1 3D

B0 00

net

16

16

16

0666

60109
OFF

E 200 - 488

F400 B C B B

```

015A' 06      810      PUSH      EB
015B' 51      811      PUSH      CX
015C' 57      812      PUSH      DI
015D' 56      813      PUSH      SI
015E' E4 11   814      IN          AL,KBSTA
0160' AB 02   815      TEST     AL,US_RXR
0162' 74 1F   816      JZ       KBXTSS
0164' AB 3B   817      TEST     AL,(US_FE + US_DE + US_PE) ; Q: any errors ?
0166' 80 34   818      MOV      AL,(UC_ERR + UC_RXE + UC_RTB)
0168' E6 11   819      OUT     KBCMD,AL
016A' 75 17   820      JNZ     KBXTSS
016C' E4 10   821      IN      AL,KB DAT
016E' FB      822      STI
016F' BA E0   823      MOV      AH,AL
0171' 24 7B   824      AND     AL,NOT KBMMSK
0173' 3C 7B   825      CMP     AL,NOT KBMMSK
0175' 75 0F   826      JNE     KBIO
0177' 80 E4 87 827      AND     AH,KBMMSK
017A' 80 26 000C" 7B 828      AND     KBMODE,NOT KBMMSK
017F' 08 26 C'0C" 829      OR      KBMODE,AH
0183' 831      JMP      KBXTSS:
0183' E9 0127 832      JMP      KBIO:
0186' BA C4      833      MOV      BA,C4
0188' 80 E4 7F   834      AND     80,E4,7F
018B' 80 FC 6B   835      CMP     80,FC,6B
018E' 77 F3     836      JA      77,F3
0190' 3C 00     837      CMP     0190,3C,00
0192' 74 EF     838      JE      0192,74,EF
0194' 3C 30     839      CMP     0194,3C,30
0196' 74 04     840      JE      0196,74,04
0198' 3C 5E     841      CMP     0198,3C,5E
019A' 75 11     842      JNE     019A,75,11
019C' BA 26 000C" 843      MOV      BA,26,000C"
019C' 80 E4 03   844      AND     80,E4,03
01A0' 80 FC 03   845      CMP     80,FC,03
01A3' 75 05     846      JNE     01A3,75,05
01A6' EA 0002" 0000" 847      MOV      EA,0002"
01AB' 80 26 000C" F7 848      AND     80,26,000C"
01AD' AB 80      849      TEST    AB,80
01B2' 74 05     850      JZ      01B2,74,05
01B6' 80 0E 000C" 0B 851      OR      80,0E,000C"
01B8' BA 26 000C" 852      MOV      BA,26,000C"
01BB' F9        853      BTC     F9
01BF' CD 5B   854      INT     CD,5B
01C0' CD 5B   855      INT     CD,5B
01C0' CD 5B   856      INT     CD,5B
01C0' CD 5B   857      INT     CD,5B
01C0' CD 5B   858      INT     CD,5B
01C0' CD 5B   859      INT     CD,5B
01C0' CD 5B   860      INT     CD,5B
01C0' CD 5B   861      INT     CD,5B

```

; ; ; Locally used register (don't forget to POP)
 ; ; ; Look at status
 ; ; ; Q: Was it the receiver ?
 ; ; ; N: ignore any other (xmit or coprocessor)
 ; ; ; (Reset any errors)
 ; ; ; Y: ignore the character
 ; ; ; N: get the char & reset the interrupt
 ; ; ; Interrupts OK now
 ; ; ; Save the character for a while
 ; ; ; Mask off the 'mode' field
 ; ; ; Q: is this a mode byte ?
 ; ; ; N: continue
 ; ; ; Y: Mask off all but the mode bits
 ; ; ; Update mode/status byte
 ; ; ; ('KBIXIT' stepping stone, jmp) 12B)
 ; ; ; *** EXIT ***
 ; ; ; Get scan code in AL
 ; ; ; Mask off the typamatic bit
 ; ; ; Q: scan code too high ?
 ; ; ; Y: ignore it (what was it, anyway ?)
 ; ; ; Q: is it zero (not allowed) ?
 ; ; ; Y: ignore it
 ; ; ; Q: Is this the Delete key ?
 ; ; ; Y: Then check for CTRL/ALT
 ; ; ; Q: What about the other Delete key ?
 ; ; ; N: Then keep going
 ; ; ; Y: Get mode/status byte
 ; ; ; G: Are CONTROL and ALT depressed also ?
 ; ; ; N: Continue
 ; ; ; Y: *** OD DO POWERUP RESET ***
 ; ; ; Reset the typamatic bit
 ; ; ; G: Typamatic character (high bit set) ?
 ; ; ; N: Keep going (bit already reset)
 ; ; ; Y: Then set the typamatic bit in KBMODE
 ; ; ; AL has scan code, AH has mode
 ; ; ; Set the carry-flag - If the user encodes the
 ; ; ; key, he should reset the carry flag before
 ; ; ; before returning, if the key is to be
 ; ; ; ignored, return with carry set and AL-OFFH.
 ; ; ; G: Does anybody want to take a shot at it ?

```

862 01C2' 73 44
863 01C4' 3C FF
864 01C6' 74 BB
865
866 ;
867 ;
868 ;
869 ;
870 ;
871 ;
872 ;
873 ;
874 ;
875 ;
876 ;
877 ;
878 ;
879 ;
880 ;
881 ;
882 ;
883 ;
884 ;
885 ;
886 ;
887 ;
888 ;
889 ;
890 ;
891 ;
892 ;
893 ;
894 ;
895 ;
896 ;
897 ;
898 ;
899 ;
900 ;
901 ;
902 ;
903 ;
904 ;
905 ;
906 ;
907 ;
908 ;
909 ;
910 ;
911 ;
912 ;
913 ;

01C8' F6 C4 02
01C8' 74 3E
01CD' 0E
01CE' 07
01CF' BF 0007"
01D2' 8F 000A
01D5' 24 7F
01D7' F2
01D8' AE
01D9' 75 30
01DB' F6 06 000C" 0B
01E0' 75 A1
01E2' 81 EF 0007"
01E6' 4F
01E7' A0 0011"
01EA' B4 0A
01EC' F6 E4
01EE' 01 F8
01FO' A2 0011"
01F3' FE 06 0010"
01F7' 80 3E 0010" 03
01FC' 75 B5
01FE'
01FE' B4 00
0200' 8B 26 0011"
0204' 8B 26 0010"
0208'
0208' E9 007E

0208'
0208' 86 06 0011"
020F' 0B C0
0211' 75 EB
0213' 86 06 0011"
0217' FE C8
0219' F6 C4 02
021C' BB 0004"
021F' 75 13
0221' F6 C4 01
0224' BB 0003"
0227' 75 0B
0229' F6 C4 04
022C' BB 000B"
022F' 75 03

JNB KBI088 ; Y: User encoded key - put it in the queue
CMP AL,-1 ; Q: should this key be ignored ?
JE KBXT88 ; Y: go to exit
; N: Encode it
;
; Check for ALT-NUM fanciness
;
TEST AH,ALTBK ; Q: Are we in ALT mode ?
JZ KBIENC ; N: Continue with normal encoding
CB
POP EB ; Set up EB to point to tables
DI,OFFSET KBNUMK ; Y: Point to Numeric key table
MOV CX,10 ; Table will always contain ten keys
AND AL,NOT TYPBIT ; Strip off any typematic bit so index is OK
REPNE
SCAB
JNE KBIENC ; Q: Is this a numeric key ?
; N: Then skip rest of numeric stuff
TEST KBMODE,TYPMSK ; Q: Was this a typematic character ?
JNZ KBXT88 ; Y: No typematics allowed in ALT/NUM mode
SUB DI,OFFSET KBNUMK ; N: Get offset into table
DEC DI ; DI now has value of key (0-9)
MOV AL,NUMVAL ; Pick up the current value
MOV AH,10 ; and multiply it by 10
MUL AH
ADD AX,DI ; Add in the latest arrival
MOV NUMVAL,AL ; ... and update that value in memory
INC NUMCNT ; Bump the keystroke counter
CMP NUMCNT,3 ; Q: Have there been 3 'ALT/NUM' keys, yet ?
JNZ KBXT88 ; N: Ignore this key and go get next one
;
MOV AH,0 ; Y: Pass scan code of zero
MOV NUMVAL,AH ; And clear NUMVAL accumulator for next time
MOV NUMCNT,AH ; ... also the keystroke counter
; 'Stepping stone' for KBI0 (JMP ) 12B)
JMP KBI0 ; do put the created character in the queue
;
; Normal encoding starts here
;
KBI0:
;
KBI088:
;
KBIENC:
;
XCHO NUMVAL,AL ; Get the current value of ALT/NUM accumulator
OR AL,AL ; Q: Is an incomplete ALT/NUM value waiting ?
JNE KBI3 ; Y: Then send it and blow off this keystroke
XCHO NUMVAL,AL ; N: Continue normal encoding
DEC AL ; Tables start at 0 not 1, so adjust scan code
TEST AH,ALTBK ; Q: Is this an ALT function ?
MOV BX,OFFSET KB_ALT ; (Point to ALT encoding table)
JNZ KBIMAP ; Y: go to it
TEST AH,CTLMSK ; Q: Is this a CONTROL function ?
MOV BX,OFFSET KBCTRL ; (Point to CONTROL encoding table)
JNZ KBIMAP ; Y: go map it
TEST AH,SHFMSK ; Q: Is either SHIFT key down ?
MOV BX,OFFSET KBSHFT ; (point to SHIFT encoding table)
JNZ KBIMAP ; Y: go map it

```

```

0231' BB 0009"
      MOV BX,OFFSET KBUNSH; N: then it must be NORMAL encoding
      JMP SHORT KBIMAP ; Go map it
      ;
      ; Map the character according to the tables
      ;
      ; KBIMAP:
0234'
0234' 24 7F AL,NOT TYRBIT ; Strip off the typematic bit
0236' 2ED7 XLAT BYTE PTR CB:[BX] ; Map the key according to appropriate table
0238' 3C FF CMP AL,-1 ; G: Should this key be ignored ?
023A' 74 0D JE KBXSS1 ; Y: Exit
023C' 3C FE CMP AL,PAUFNC ; G: Is this the Program Pause Key code ?
023E' 75 08 JNE KB14 ; N: Continue
0240' 30 C0 XOR AH,AL ; Y: Flag this as extended code
0242' B4 01 MOV AH,KBPPAU ; Set up Program Pause function code
0244' F8 CLC ; reset carry flag in preparation
0245' CD 5C INT PAUINT ; Call the Pause routine
0247' 73 40 JNB KBIG ; If user encoded the key, then put it in queue
0249'
0249' EB 62 JMP SHORT KBIXIT ; KBIXIT stepping stone # 2
024B'
024B' 3C FD CMP AL,PBFNC ; G: Is this the Program Break Key code ?
024D' 75 08 JNE KB15 ; N: Continue
024F' 30 C0 XOR AL,AL ; Y: Flag this as an extended code
0251' B4 00 MOV AH,KBPRBK ; Set up Program Break function code
0253' F8 CLC ; reset carry flag in preparation
0254' CD 5D INT PBKINT ; Call the Break routine
0256' 73 31 JNB KBIG ; If user encoded the key, then put it in queue
0258' EB 53 JMP SHORT KBIXIT ; else exit
025A'
025A' 3C FC CMP AL,PSFNC ; G: Is this the Print Screen Key code ?
025C' 75 07 JNE KB16 ; N: Continue
025E' F9 STC ; Y: Set carry flag in preparation
025F' CD 5E INT PSCINT ; Call the Print Screen routine
0261' 73 26 JNB KBIG ; If user encoded the key, then put it in queue
0263' EB 48 JMP SHORT KBIXIT ; else exit
0265'
0265' A8 80 TEST AL,80H ; G: Should I look in the function key table ?
0267' 74 0D JZ KB17 ; N: High bit not set so not a function key
0269' BB 0006" MOV BX,OFFSET KBFUNC ; Y: Point to function key table
026C' 24 7F AND AL,7FH ; Strip off the high bit
026E' 2ED7 XLAT BYTE PTR CB:[BX] ; and pick up the real function key code
0270' BA E0 MOV AH,AL ; Put it in AH
0272' 30 C0 XOR AL,AL ; and zero into AL
0274' EB 13 JMP SHORT KBIG
0276'
0276' F6 C4 80 TEST AH,UFCMBK ; G: is the UPPERCASE LOCK switch down ?
0278' B4 00 MOV AH,0 ; (Here set up standard ASCII return w/AH=0)
027A' 74 0C JZ KB10 ;
027C' 3C 61 CMP AL,'a' ;
027E' 72 08 JB KBIG ; N: encoding done, put char in queue
0280' 3C 7B CMP AL,'z'+1 ;
0282' 73 04 JNB KBIG ; Y: then check for lower case

```

```

0285' 2C 20
0287' EB 00

966 SUB AL,20H ; ... and map to upper
967 JMP SHORT KBIG ; put it in the queue
968
969 ;
970 ; Put the encoded character in the queue
971 KBIG:
972 TEST CRTHLD,OFFH ; Q: Is the CRT in the hold mode ?
973 JZ KBIGO ; N: Then continue
974 MOV CRTHLD,00 ; Y: Then reset the flag and unfreeze CRT
975
976 KBIGO:
977 TEST KBMODE,TYPMASK ; Q: Was this a typamatic character ?
978 JZ KBIG1 ; N: Continue
979 CMP GDEPTH,1 ; Q: Is there more than one character waiting ?
980 JLE KBIG1 ; N: Continue
981 JMP SHORT KBIXIT ; Y: Then blow off the typamatic char
982
983 KBIG1:
984 CALL KBFG ; Put the char into the queue
985 JNZ KBIXIT ; Q: Did the character fit?
986 CALL BBEEP ; Y: exit
987 ; N: ring bell
988
989 KBIXIT:
990 POP SI ; Pop locally-used registers
991 POP DI
992 POP CX
993 MOV EB,KBSSV ; EB = BB of interrupted code
994 MOV BX,KBSPSV ; BX = SP of interrupted code
995 JMP DWORD PTR CS:XITVEC*4+CSHRAP ; Take common interrupt exit
996
997 *****
998 ; KBFG - Routine to place 16-bit character in queue. Called from KEYISR and
999 ; as opcode 5 to simulate the keyboard putting key into queue.
1000
1001 INPUT: AX = character to be placed into queue
1002 (GREGAR), (GFRONT)
1003 OUTPUT: ZF = 0 if character was placed into the buffer
1004 ZF = 1 if buffer was full (char not placed, remains AX)
1005 USED: BI, (AX if put into buffer)
1006 STACK:
1007
1008 KBFG PROC NEAR
1009 CLI ; Shields up
1010 MOV SI,GREGAR ; Bump queue pointer
1011 INCG ; Q: Any room in the queue ?
1012 CALL SI,GFRONT ; N: EXIT
1013 CMP KBIG2 ; Y: put character in queue
1014 JZ ; Put character in queue
1015 ; (added at rear of queue)
1016 MOV [BI],AX ; Bump char-waiting count
1017 GREGAR,BI ; Tell the user (OS?) we just queued a char
1018 GDEPTH ;
1019 QUEINT ;
1020 AX,AX ; Insure Z-flag reset
1021 INC AL ; Shields down
1022 RET
1023
028D' FA
028E' BB 36 0014"
028F' EB FD91
0290' 3B 36 0013"
0291' 74 10
0292' 89 04
0293' 89 36 0014"
0294' FE 06 0012"
0295' CD 5F
0296' 31 C0
0297' FE C0
0298' FB
0299' C3

```


KEYDSR - Keyboard device service routine
KEYDSR.SRC

CR8086/11 version 10.34.17

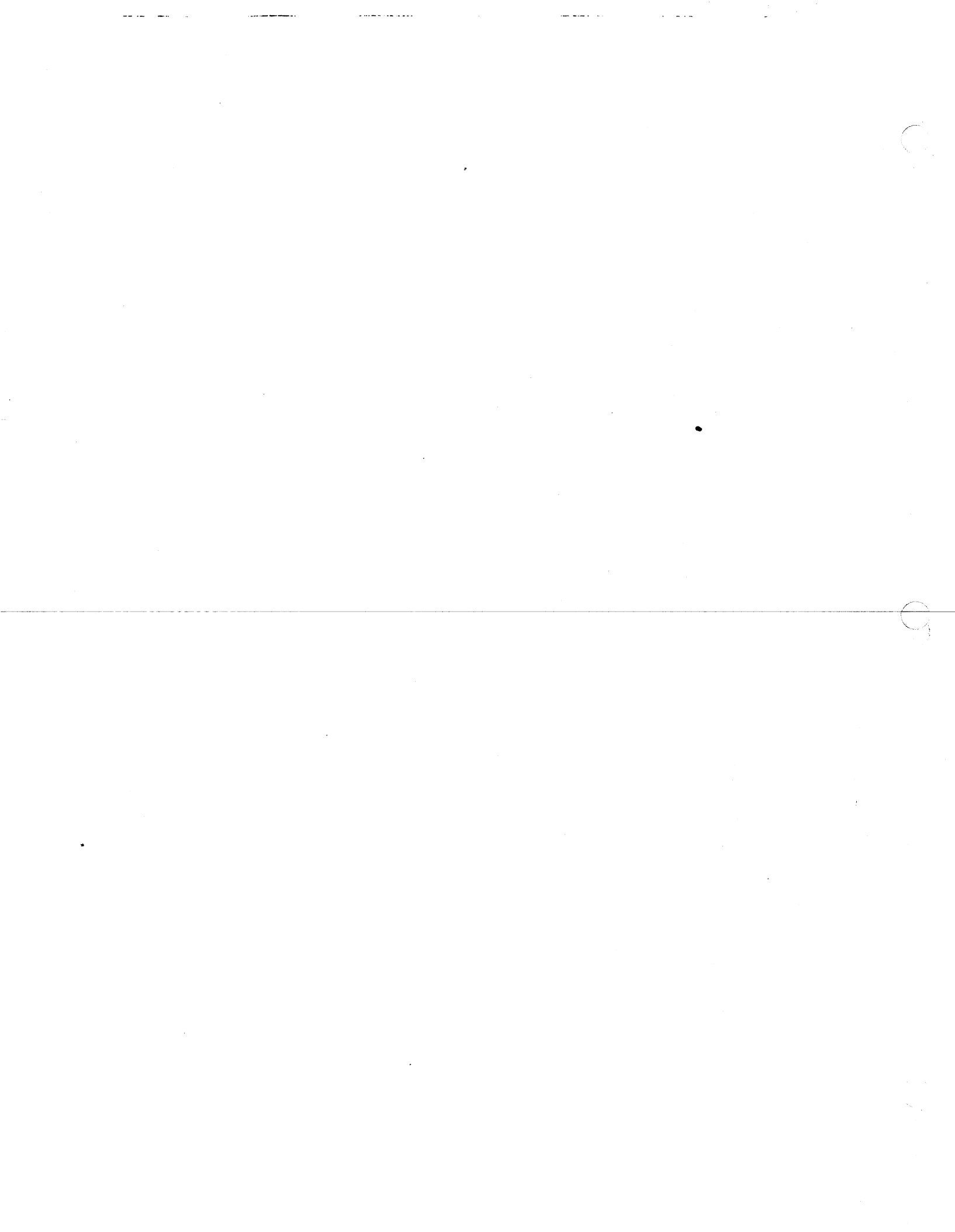
3-Aug-83 16:42:19

Page 1-13

1018

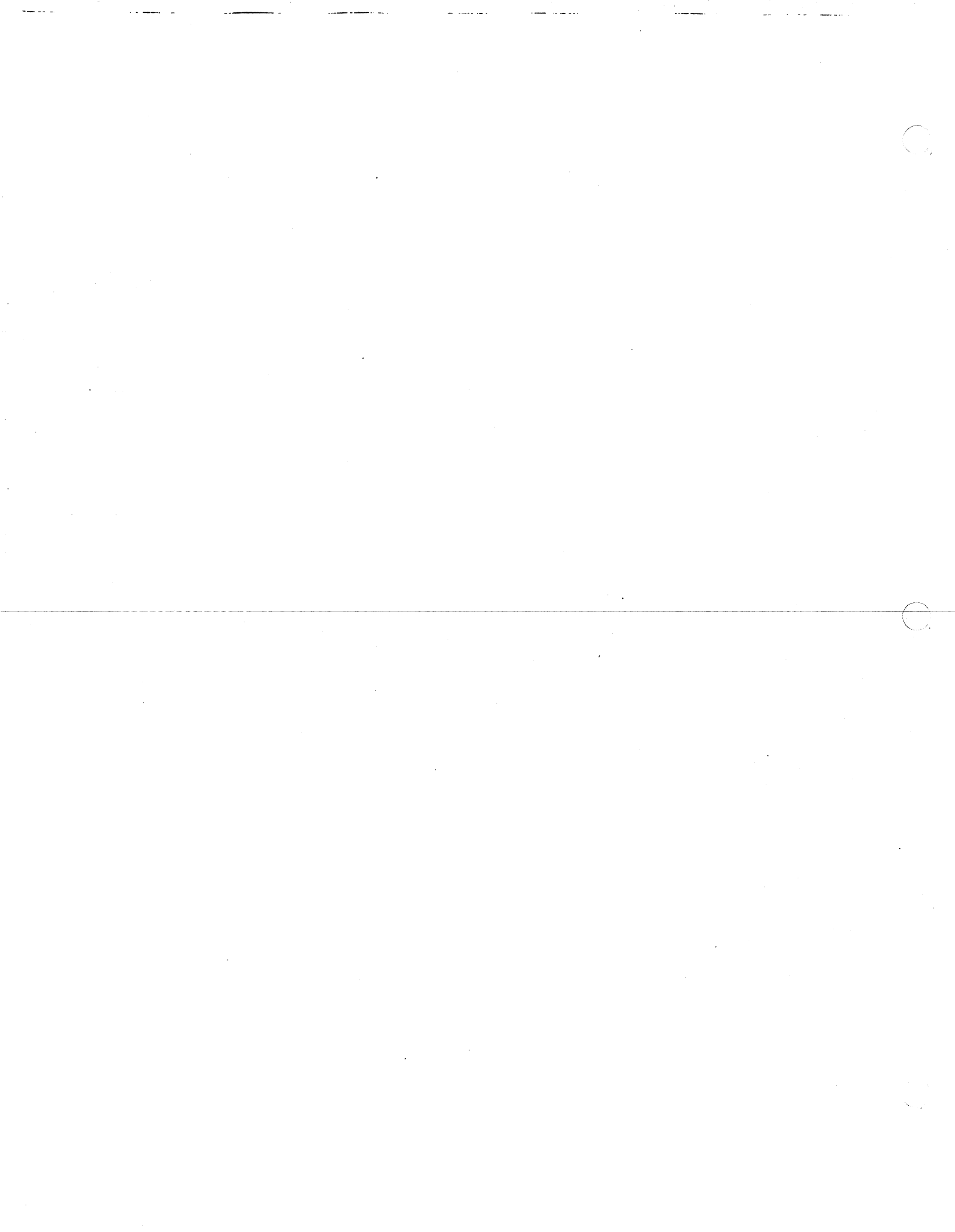
END

No errors detected



```

1 *****
2 ; TITLE - KEYTST - Keyboard initialization logic *****
3 ; COMPUTER - BOBB Assembly Language *****
4 ; ABSTRACT - This module contains the powerup initialization code for the
5 ; keyboard. *****
6 *****
7 ; NAME KEYTST - Keyboard initialization logic *****
8 *****
9 ; PUBLIC DEFINITIONS *****
10 *****
11 ; PUBLIC KEYTST *****
12 ; *****
13 ; *****
14 ; *****
15 ; ***** EXTERNAL REFERENCES *****
16 ; *****
17 ; *****
18 ; *****
19 ; EXTRN DSPKR:NEAR ; Keyboard error display routine (OUTPUT)
20 ; EXTRN KEYINI:NEAR ; Keyboard driver initialization (KEYDSR)
21 ; EXTRN KEYOUT:NEAR ; Keyboard output routine (KEYDBR)
22 ; EXTRN PUPTSI:NEAR ; Continuation of powerup test logic (PUPTSI)
23 ; *****
24 ; Keyboard error codes *****
25 ; *****
26 ; EXTRN KNIERR:ABS ; Keyboard 'not installed' error code (ROMERR)
27 ; EXTRN KNRRERR:ABS ; Keyboard 'no response' error code (ROMERR)
28 ; EXTRN KRAERR:ABS ; Keyboard 'RAM failure' error code (ROMERR)
29 ; EXTRN KRCERR:ABS ; Keyboard 'receive error' error code (ROMERR)
30 ; EXTRN KRDERR:ABS ; Keyboard 'ROM failure' error code (ROMERR)
31 ; EXTRN KUNERR:ABS ; Keyboard 'unknown' error code (ROMERR)
32 ; *****
33 ; ***** LOCAL CONSTANTS *****
34 ; *****
35 ; INCLUDE PEG:PORTADDR.EQU *****
36 ; INCLUDE PEG:VECTOR.EQU *****
37 ; INCLUDE PEG:INTCTLR.EQU *****
38 ; INCLUDE PEG:USART.EQU *****
39 ; INCLUDE PEG:KBCMD.EQU *****
40 ; *****
218 *****
219 *****
220 *****
221 *****
222 *****
223 *****
224 *****
225 *****
226 *****
227 *****
228 *****
229 *****
230 *****
    
```



```

0000' 231 KEYTST PROC NEAR
0000' 232 ;
0000' 233 MOV DS,WORD PTR CS:DSADDR+CSWRAP
0000' 234 ;
0005' 235 ; 'KEYRST' has already been called (from INTTST) so that the
0007' 236 ; USART is already set up.
0009' 237 ;
0009' 238 ; CALL KEYRST ; Init the USART
0009' 239 IN AL,KBDAT ; Read RCVR to reset any interrupts
0009' 240 MOV AL,(UC_ERR + UC_RXE + UC_RTS)
0009' 241 OUT KBCMD,AL ; Reset any errors
0009' 242 ;
0009' 243 ; Check for keyboard installed (plugged in) by looking at transmit
0009' 244 ; signal looped-back through the DSR pin. Simple test, just looks
0009' 245 ; for a wiggle, doesn't time it.
0009' 246 ;
0009' 247 ; IN AL,KBSTA ; Look at USART status
0009' 248 TEST AL,US_DSR ; G: is line idle (pulled low)
0009' 249 JNZ KEYNIS ; N: then keyboard isn't there
0009' 250 MOV AL,55H ; This 'command' shouldn't do
0009' 251 OUT KBDAT,AL ; anything to the keyboard
0009' 252 MOV AL,(UC_RXE + UC_TXE + UC_RTS)
0009' 253 OUT KBCMD,AL ; Enable the transmitter
0009' 254 ;
0009' 255 MOV AH,5 ; If line hasn't wiggled in 5 msec,
0009' 256 ; then give it up
0009' 257 ;
0009' 258 ; KTO:
0009' 259 ; IN AL,KBSTA ; Look at DSR (xmitter loopback)
0009' 260 TEST AL,US_DSR ; G: Has it gone high ?
0009' 261 JNZ KT1 ; Y: Keyboard there, continue
0009' 262 MOV CX,294 ; N: delay a millisecond
0009' 263 LOOP $
0009' 264 DEC AH
0009' 265 JNZ KTO ; Unbump msec counter
0009' 266 MOV AH,KNIERR ; Loop if not timed out
0009' 267 JMP SHORT KEYERR ; Report keyboard 'Not installed'
0009' 268 ;
0009' 269 ; Simple timeout loop to keep from locking up if USART is bad
0009' 270 ;
0009' 271 ; KTI:
0009' 272 ; IN AL,KBSTA ; Look at status
0009' 273 TEST AL,US_TXR ; G: through transmitting ?
0009' 274 LOOPZ KT1 ; N: loop til done OR timeout
0009' 275 JNZ KT2 ; Y: continue
0009' 276 DEC AH ; decrement outer loop counter
0009' 277 JNZ KT1 ; (fall thru if timeout)
0009' 278 ;
0009' 279 ; KTI:
0009' 280 MOV AL,(UC_RXE + UC_RTS) ; Then shut it off
0009' 281 OUT KBCMD,AL
0009' 282 ;
0009' 283 ; CLI
0009' 284 ; Protect this

```

```

003F' E4 19      AL,INTA01
0041' 24 7F      AL,ENAB7
0043' E6 19      INTA01,AL
0045' FB         ; Enable the keyboard interrupt

0046' 80 00      MOV     AL,KBPHRU
0048' EB 0003"   CALL    KEYOUT
004B' 74 21      JZ     KEYFIN
004D' B4 06*    MOV     AH,KNRERR
004F' 3C FF      CMP     AL,OFFH
0051' 74 14      JZ     KEYERR
0053' B4 08*    MOV     AH,KRCERR
0055' 3C FE      CMP     AL,OFEH
0057' 74 0E      JZ     KEYERR

; At this point, assume its a 'regular' error
298
299
300
0059' B4 09*    MOV     AH,KRDERR
005B' 3C 71      CMP     AL,KB_ROM
005D' 74 08      JZ     KEYERR
005F' B4 07*    MOV     AH,KRAERR
0061' 3C 72      CMP     AL,KB_RAM
0063' 74 02      JZ     KEYERR

;
0065' B4 0A*    MOV     AH,KUNERR

;
0067' 30 D2      KEYERR: XOR    DL,DL
0069' 8A C4      MOV     AL,AH
006B' EB 0001"   CALL    DSPKER

;
006E' EB 0002"   KEYFIN: CALL   KEYINI
0071' E9 0004"   JMP     DECLD
;
; Do driver initialization in KEYDSR
; *** NO LED code for keyboard ***
; Go to next
;
END

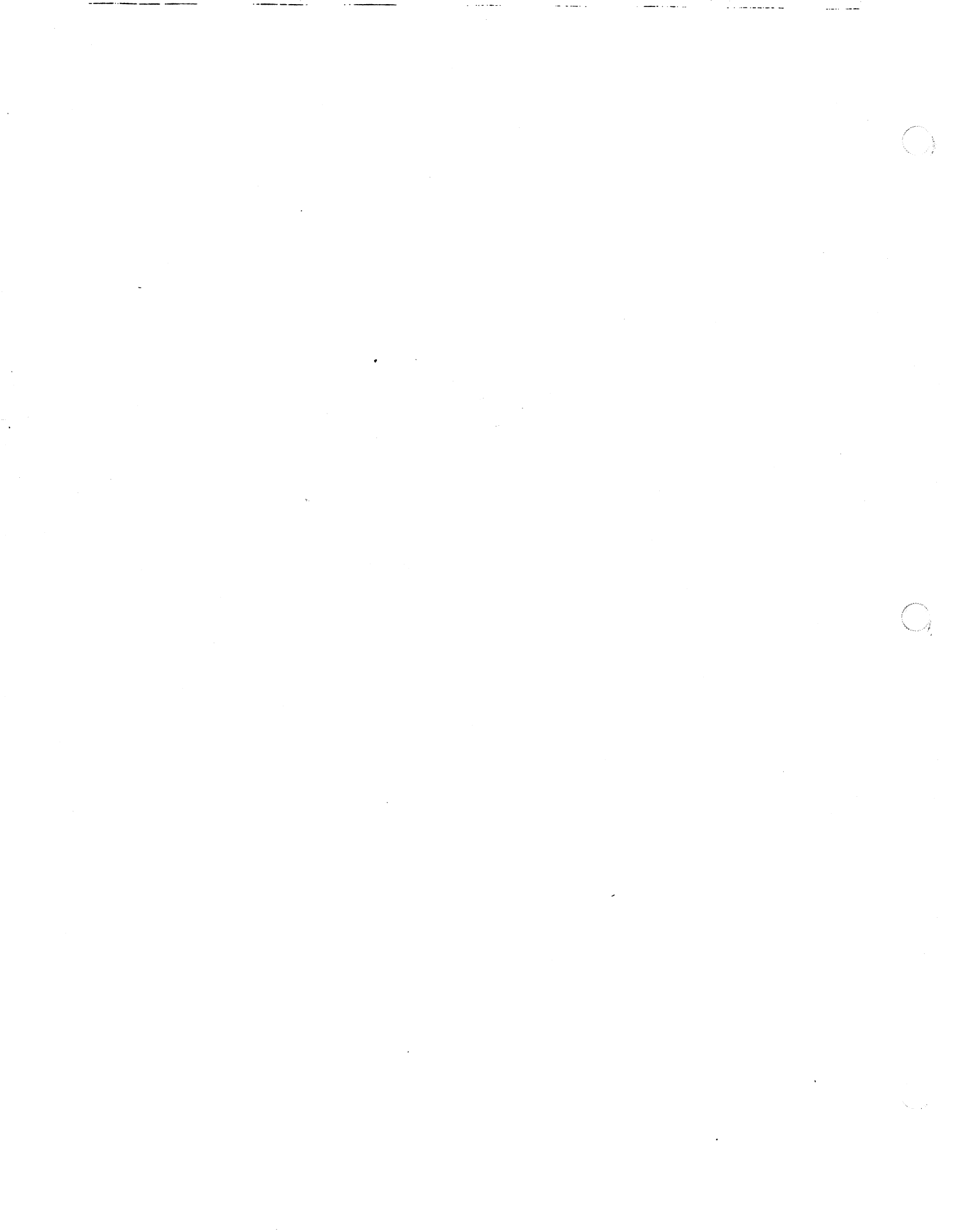
```

No errors detected

```

1 *****
2 ; TITLE - MSCTST - Miscellaneous initialization *****
3 ; COMPUTER - 8088 Assembly Language
4 ; ABSTRACT - This module is responsible for initializing the system
5 ; timer, the clock DSR, and updating the System Configuration
6 ; word with various information. These routines assume the
7 ; System Config words were initialized to 0000H by RAMINI.
8 ; *****
9 ; NAME MSCTBT - Miscellaneous initialization *****
10 ; *****
11 ; PUBLIC DEFINITIONS *****
12 ; *****
13 ; *****
14 ; PUBLIC MSCTBT *****
15 ; *****
16 ; *****
17 ; *****
18 ; EXTERNAL REFERENCES *****
19 ; *****
20 ; *****
21 ; EXTRN KEYST:NEAR ; Keyboard test logic (KEYTBT)
22 ; EXTRN SETDAT:NEAR ; Date setting logic (CLKDSR)
23 ; EXTRN SETTIM:NEAR ; Time setting logic (CLKDSR)
24 ; *****
25 ; *****
26 ; SECT ROMDAT
27 ; EXTRN DSKC_M:BYTE ; Memory copy of disk control latch (ROMDAT)
28 ; EXTRN QUEUE:WORD ; Keyboard queue area (used for temp) (ROMDAT)
29 ; EXTRN BYSCON:WORD ; System Configuration word (ROMDAT)
30 ; *****
31 ; LOCAL CONSTANTS *****
32 ; *****
33 ; *****
34 ; TBTLOC EQU 0000H ; Offset used for installation determination
35 ; TSTVAL EQU 1144H ; Read/write value for installation testing
36 ; *****
37 ; INCLUDE PEG:PORTADDR.EQU
38 ; INCLUDE PEG:INTCTLR.EQU
39 ; INCLUDE PEG:TIMERS.EQU
40 ; INCLUDE PEG:LATCHES.EQU
41 ; INCLUDE PEG:BYSCON.EQU
42 ; INCLUDE PEG:VECTOR.EQU
43 ; INCLUDE PEG:COMM.EQU
44 ; INCLUDE PEG:CRT.EQU
45 ; *****
429 ; *****
430 ; CODE SEGMENT DEFINITION *****
431 ; *****
432 ; *****
433 ; *****
434 ; SECT ROMCOD
435 ; ASSUME CS:ROMCOD
436 ; DB SELWRO
437 ; DB SELWR9
438 ; DB
439 ; DB
440 ; DB
441 ; DB
442 ; DB
443 ; DB
444 ; DB
445 ; DB
446 ; DB
447 ; DB
448 ; DB
449 ; DB
450 ; DB
451 ; DB
452 ; DB
453 ; DB
454 ; DB
455 ; DB
456 ; DB
457 ; DB
458 ; DB
459 ; DB
460 ; DB
461 ; DB
462 ; DB
463 ; DB
464 ; DB
465 ; DB
466 ; DB
467 ; DB
468 ; DB
469 ; DB
470 ; DB
471 ; DB
472 ; DB
473 ; DB
474 ; DB
475 ; DB
476 ; DB
477 ; DB
478 ; DB
479 ; DB
480 ; DB
481 ; DB
482 ; DB
483 ; DB
484 ; DB
485 ; DB
486 ; DB
487 ; DB
488 ; DB
489 ; DB
490 ; DB
491 ; DB
492 ; DB
493 ; DB
494 ; DB
495 ; DB
496 ; DB
497 ; DB
498 ; DB
499 ; DB
500 ; DB
501 ; DB
502 ; DB
503 ; DB
504 ; DB
505 ; DB
506 ; DB
507 ; DB
508 ; DB
509 ; DB
510 ; DB
511 ; DB
512 ; DB
513 ; DB
514 ; DB
515 ; DB
516 ; DB
517 ; DB
518 ; DB
519 ; DB
520 ; DB
521 ; DB
522 ; DB
523 ; DB
524 ; DB
525 ; DB
526 ; DB
527 ; DB
528 ; DB
529 ; DB
530 ; DB
531 ; DB
532 ; DB
533 ; DB
534 ; DB
535 ; DB
536 ; DB
537 ; DB
538 ; DB
539 ; DB
540 ; DB
541 ; DB
542 ; DB
543 ; DB
544 ; DB
545 ; DB
546 ; DB
547 ; DB
548 ; DB
549 ; DB
550 ; DB
551 ; DB
552 ; DB
553 ; DB
554 ; DB
555 ; DB
556 ; DB
557 ; DB
558 ; DB
559 ; DB
560 ; DB
561 ; DB
562 ; DB
563 ; DB
564 ; DB
565 ; DB
566 ; DB
567 ; DB
568 ; DB
569 ; DB
570 ; DB
571 ; DB
572 ; DB
573 ; DB
574 ; DB
575 ; DB
576 ; DB
577 ; DB
578 ; DB
579 ; DB
580 ; DB
581 ; DB
582 ; DB
583 ; DB
584 ; DB
585 ; DB
586 ; DB
587 ; DB
588 ; DB
589 ; DB
590 ; DB
591 ; DB
592 ; DB
593 ; DB
594 ; DB
595 ; DB
596 ; DB
597 ; DB
598 ; DB
599 ; DB
600 ; DB
601 ; DB
602 ; DB
603 ; DB
604 ; DB
605 ; DB
606 ; DB
607 ; DB
608 ; DB
609 ; DB
610 ; DB
611 ; DB
612 ; DB
613 ; DB
614 ; DB
615 ; DB
616 ; DB
617 ; DB
618 ; DB
619 ; DB
620 ; DB
621 ; DB
622 ; DB
623 ; DB
624 ; DB
625 ; DB
626 ; DB
627 ; DB
628 ; DB
629 ; DB
630 ; DB
631 ; DB
632 ; DB
633 ; DB
634 ; DB
635 ; DB
636 ; DB
637 ; DB
638 ; DB
639 ; DB
640 ; DB
641 ; DB
642 ; DB
643 ; DB
644 ; DB
645 ; DB
646 ; DB
647 ; DB
648 ; DB
649 ; DB
650 ; DB
651 ; DB
652 ; DB
653 ; DB
654 ; DB
655 ; DB
656 ; DB
657 ; DB
658 ; DB
659 ; DB
660 ; DB
661 ; DB
662 ; DB
663 ; DB
664 ; DB
665 ; DB
666 ; DB
667 ; DB
668 ; DB
669 ; DB
670 ; DB
671 ; DB
672 ; DB
673 ; DB
674 ; DB
675 ; DB
676 ; DB
677 ; DB
678 ; DB
679 ; DB
680 ; DB
681 ; DB
682 ; DB
683 ; DB
684 ; DB
685 ; DB
686 ; DB
687 ; DB
688 ; DB
689 ; DB
690 ; DB
691 ; DB
692 ; DB
693 ; DB
694 ; DB
695 ; DB
696 ; DB
697 ; DB
698 ; DB
699 ; DB
700 ; DB
701 ; DB
702 ; DB
703 ; DB
704 ; DB
705 ; DB
706 ; DB
707 ; DB
708 ; DB
709 ; DB
710 ; DB
711 ; DB
712 ; DB
713 ; DB
714 ; DB
715 ; DB
716 ; DB
717 ; DB
718 ; DB
719 ; DB
720 ; DB
721 ; DB
722 ; DB
723 ; DB
724 ; DB
725 ; DB
726 ; DB
727 ; DB
728 ; DB
729 ; DB
730 ; DB
731 ; DB
732 ; DB
733 ; DB
734 ; DB
735 ; DB
736 ; DB
737 ; DB
738 ; DB
739 ; DB
740 ; DB
741 ; DB
742 ; DB
743 ; DB
744 ; DB
745 ; DB
746 ; DB
747 ; DB
748 ; DB
749 ; DB
750 ; DB
751 ; DB
752 ; DB
753 ; DB
754 ; DB
755 ; DB
756 ; DB
757 ; DB
758 ; DB
759 ; DB
760 ; DB
761 ; DB
762 ; DB
763 ; DB
764 ; DB
765 ; DB
766 ; DB
767 ; DB
768 ; DB
769 ; DB
770 ; DB
771 ; DB
772 ; DB
773 ; DB
774 ; DB
775 ; DB
776 ; DB
777 ; DB
778 ; DB
779 ; DB
780 ; DB
781 ; DB
782 ; DB
783 ; DB
784 ; DB
785 ; DB
786 ; DB
787 ; DB
788 ; DB
789 ; DB
790 ; DB
791 ; DB
792 ; DB
793 ; DB
794 ; DB
795 ; DB
796 ; DB
797 ; DB
798 ; DB
799 ; DB
800 ; DB
801 ; DB
802 ; DB
803 ; DB
804 ; DB
805 ; DB
806 ; DB
807 ; DB
808 ; DB
809 ; DB
810 ; DB
811 ; DB
812 ; DB
813 ; DB
814 ; DB
815 ; DB
816 ; DB
817 ; DB
818 ; DB
819 ; DB
820 ; DB
821 ; DB
822 ; DB
823 ; DB
824 ; DB
825 ; DB
826 ; DB
827 ; DB
828 ; DB
829 ; DB
830 ; DB
831 ; DB
832 ; DB
833 ; DB
834 ; DB
835 ; DB
836 ; DB
837 ; DB
838 ; DB
839 ; DB
840 ; DB
841 ; DB
842 ; DB
843 ; DB
844 ; DB
845 ; DB
846 ; DB
847 ; DB
848 ; DB
849 ; DB
850 ; DB
851 ; DB
852 ; DB
853 ; DB
854 ; DB
855 ; DB
856 ; DB
857 ; DB
858 ; DB
859 ; DB
860 ; DB
861 ; DB
862 ; DB
863 ; DB
864 ; DB
865 ; DB
866 ; DB
867 ; DB
868 ; DB
869 ; DB
870 ; DB
871 ; DB
872 ; DB
873 ; DB
874 ; DB
875 ; DB
876 ; DB
877 ; DB
878 ; DB
879 ; DB
880 ; DB
881 ; DB
882 ; DB
883 ; DB
884 ; DB
885 ; DB
886 ; DB
887 ; DB
888 ; DB
889 ; DB
890 ; DB
891 ; DB
892 ; DB
893 ; DB
894 ; DB
895 ; DB
896 ; DB
897 ; DB
898 ; DB
899 ; DB
900 ; DB
901 ; DB
902 ; DB
903 ; DB
904 ; DB
905 ; DB
906 ; DB
907 ; DB
908 ; DB
909 ; DB
910 ; DB
911 ; DB
912 ; DB
913 ; DB
914 ; DB
915 ; DB
916 ; DB
917 ; DB
918 ; DB
919 ; DB
920 ; DB
921 ; DB
922 ; DB
923 ; DB
924 ; DB
925 ; DB
926 ; DB
927 ; DB
928 ; DB
929 ; DB
930 ; DB
931 ; DB
932 ; DB
933 ; DB
934 ; DB
935 ; DB
936 ; DB
937 ; DB
938 ; DB
939 ; DB
940 ; DB
941 ; DB
942 ; DB
943 ; DB
944 ; DB
945 ; DB
946 ; DB
947 ; DB
948 ; DB
949 ; DB
950 ; DB
951 ; DB
952 ; DB
953 ; DB
954 ; DB
955 ; DB
956 ; DB
957 ; DB
958 ; DB
959 ; DB
960 ; DB
961 ; DB
962 ; DB
963 ; DB
964 ; DB
965 ; DB
966 ; DB
967 ; DB
968 ; DB
969 ; DB
970 ; DB
971 ; DB
972 ; DB
973 ; DB
974 ; DB
975 ; DB
976 ; DB
977 ; DB
978 ; DB
979 ; DB
980 ; DB
981 ; DB
982 ; DB
983 ; DB
984 ; DB
985 ; DB
986 ; DB
987 ; DB
988 ; DB
989 ; DB
990 ; DB
991 ; DB
992 ; DB
993 ; DB
994 ; DB
995 ; DB
996 ; DB
997 ; DB
998 ; DB
999 ; DB
1000 ; DB

```




```

1 ;*****
2 ; TITLE - OUTPUT - Various screen output and error-handling routines
3 ; COMPUTER - 8088 Assembly Language
4 ; ABSTRACT - This module contains the error-reporting and the general-
5 ; purpose screen output routines.
6 ; * Add entry point for external users of DSPERR which accepts error code in BX
7 ;*****
8 NAME OUTPUT - Screen output and error-handling routines
9 ;*****
10 PUBLIC DEFINITIONS
11 ;*****
12
13 PUBLIC ARGHHH
14 PUBLIC DSPDIE
15 PUBLIC DSPERR
16 PUBLIC DSPKER
17 PUBLIC DSPERZ
18 PUBLIC FATAL
19 PUBLIC MSG
20 PUBLIC NMIGD
21 PUBLIC PUPNMI
22 PUBLIC WILD$$
23 ;*****
24 ;***** EXTERNAL REFERENCES *****
25 ;*****
26 ;*****
27 ;*****
28 SECT ROMCOD
29 EXTRN CRTOUT:NEAR ; CRT character output routine (CRTDSR)
30 EXTRN FATERR:ABS ; Fatal software error code (ROMERR)
31 EXTRN IVIERR:ABS ; Invalid interrupt Interrupt port code (TSERR)
32 EXTRN LED100:ABS ; Interrupt error code (ROMERR)
33 EXTRN LED101:ABS ; LED error code (used for PUPNMI) (ROMERR)
34 EXTRN PWRUP:FAR ; Powerup reset routine (RESET)
35 EXTRN RMPERR:ABS ; 'RAM parity error' (ROMERR)
36 EXTRN WLDERR:ABS ; Wild interrupt error code (ROMERR)
37 EXTRN XNMERR:ABS ; 'Unexpected NMI' error code (ROMERR)
38 ;*****
39 SECT ROMDAT
40 EXTRN DSKC_M:BYTE ; RAM copy of disk control port (ROMDAT)
41 EXTRN PRCD_M:BYTE ; RAM copy of printer control port (ROMDAT)

```

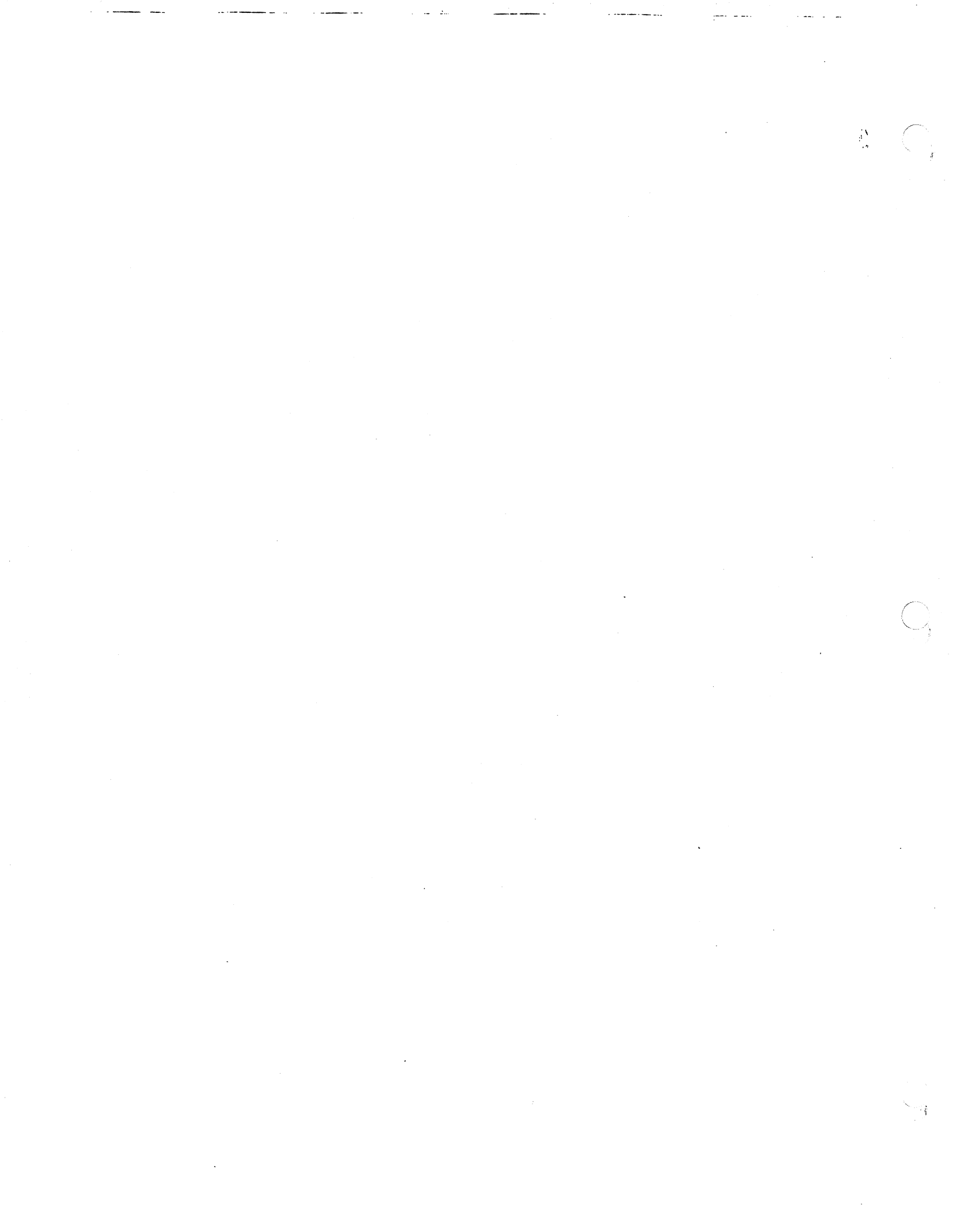
=0000

=0000



```

43 ;*****
44 ; LOCAL CONSTANTS
45 ;*****
46 ;
47 BELTMR EQU 1563 ; 1563 ==) 800 Hz for system beeper
48 CR EQU ODH
49 LF EQU OAH
50
51 ; INCLUDE PEG:CRTOP.EQU
52 ; INCLUDE PEG:INTCTLR.EQU
53 ; INCLUDE PEG:CRT.EQU
54 ; INCLUDE PEG:LATCHES.EQU
55 ; INCLUDE PEG:PORTADDR.EQU
56 ; INCLUDE PEG:TIMERS.EQU
57 ; INCLUDE PEG:USART.EQU
58 ; INCLUDE PEG:VECTOR.EQU
59 ;
384 ;*****
385 ; CODE SEGMENT DEFINITION
386 ;*****
387 ;
388 BECT ROMCOD
389 ASSUME CB:ROMCOD
390 ;
391 ;*****
392 ; LOCAL DATA AREA
393 ;*****
394 ;
395 KERMS0 DB CR,LF,LF
396 ;** Keyboard Error **
397
398 DB 00 ; End of string
399
400 SER1MS DB CR,LF,LF
401 SER2MS DB '** System Error **'
402
403 SEREND LABEL BYTE
404 SERMSL EQU SEREND-SER2MS ; End of string
405
406 BINASC LABEL DB
407 '0123456789ABCDEF', Binary to ASCII lookup table - used by XLAT
408
409 DIESTK LABEL WORD
410 DW OFFSET DDO ; Fake return address for DSPDIE
    DW OFFSET DD1 ; Fake return address for DSPDIE
    
```



```

0046' 00BE"
0048' 0094"
004A' 0099"
004C' 00A0"

411 DW OFFSET DD2 ; Fake return address for DSPDIE
412 DW OFFSET DD3 ; Fake return address for DSPDIE
413 DW OFFSET DD4 ; Fake return address for DSPDIE
414 DW OFFSET DD5 ; Fake return address for DSPDIE
415 ;
416 ; *****
417 ; MODULE ENTRY POINT
418 ; *****
419 ;
420 ; DSPDIE - Display error message and 'die'
421 ; This routine is intended for use by the powerup diagnostics.
422 ; If an error occurs during powerup tests, this routine should be
423 ; 'jumped to' with the low-order byte of the error code in AL.
424 ; It displays the message '** System Error ** - 00xx' by
425 ; writing directly to screen RAM (because during powerup the CRT
426 ; DSR has not yet been initialized). If the factory test interface
427 ; is connected (signified by 'PAPER OUT' and 'NOT BUSY'), the error
428 ; code is also sent out the parallel printer port. A code passed
429 ; in register DL will be written to the printer port (as one character)
430 ; after the ASCII error code. The system 'bell' is then sounded and
431 ; the CPU halts with interrupts disabled. Any restart attempts
432 ; must be by cycling power.
433 ;
434 ; INPUT: AL = 2-digit error code to be displayed (DSPDIE adds leading 00)
435 ; Error code is not range-checked, but should be in the
436 ; range from 0 to 9 because of simple ASCII conversion.
437 ; DL = LED value to be placed on the parallel port before dying.
438 ; OUTPUT: (error message to screen/parallel port, beep)
439 ; USED:
440 ; STACK:
441 ; ASSUME CS:ROMCOD, DS:ROMDAT, EB:CRTDAT
442 ; -----
004E' 004E' NEAR
004F' FC
0050' FA
0051' 2E8E 1E C180
0052' BE 001D"
0053' BB 0000"
0054' BE C3
0055' 26C6 06 1800 0F
0056' BF 0000
0057' B9 0014
0058' F2
0059' 2EA4
005A' BC C9
005B' BE D1
005C' BC 0042"
005D' OC 70
005E' BA EB
005F' F6 D0
0060' 24 07
0061' BA FO
0062'

443 DSPDIE PROC
444 CLD
445 CLI
446 MOV DS,WORD PTR CS:DSADDR+CSWRAP ; Set up local DS
447 SI,OFFSET SER2MS ; Point to 'SYSTEM ERROR' message
448 BX,CRTDAT
449 MOV EB,BX ; EB = screen RAM segment
450 MOV BYTE PTR EB:ATTLAT,OFH ; Set attribute latch to default
451 MOV DI,0000 ; Start in up left corner of screen
452 MOV CX,OFFSET SERMSL ; Get length of message
453 REP
454 MOVSB ; Put string on screen
455 MOV CX,CS
456 MOV SS,CX
457 MOV SP,OFFSET DIESTK ; Set up fake stack for no-RAM calls
458 OR AL,(PRAUT+PRSTRB+PRINIT) ; Force printer interface to active
459 MOV CH,AL ; Save LED state for now.
460 NDT ; Invert the error code byte
461 AND AL,07H ; Mask off low-order 3 bits
462 MOV DH,AL ; Save AL error

```



```

00B0' 3C 20      CMP      AL,PRPAO AND (NOT PRBUSY) , Q: Is the factory connected ?
00B2' 75 1E      JNE      PWEXIT
00B4' B1 02      MOV      CL,02
00B6' B8 C3      MOV      AL,BL
00B8'
00BB' E6 02      OUT      PRDA_P,AL
00BA' B8 C5      MOV      AL,CH
00BC' 24 EF      AND      AL,NOT PRAUTO
00BE' E6 03      OUT      PRCD_P,AL
00C0' 90          NOP
00C1' 34 10      XOR      AL,PRAUTO
00C3' E6 03      OUT      PRCD_P,AL
00C5' 90          NOP
00C6'
00C6' E4 01      IN       AL,PRCI_P
00C8' AB 10      TEST      AL,PRBUSY
00CA' 75 FA      JNZ      PW2
00CC' B8 C7      MOV      AL,BH
00CE' FE C9      DEC      CL
00D0' 75 E6      JNZ      PW1
00D2'
00D2' C3          RET
*****
; *****
; DSPERR - System error display subroutine
; DSPKR - Keyboard error display subroutine
; These subroutines perform a function similar to DSPDIE, except
; that they are callable NEAR subroutines, returning rather than
; 'dying'. Also, they use the CRT DSR to do the screen output.
; DSPERR outputs the message '** System Error ** - 00xx'
;
; INPUT: AL = 2-digit error code to be displayed (DSPERR adds leading 00)
; (DSPERX) AX = 4-digit error code to be displayed
; OUTPUT: (error message to screen/parallel port, beep)
; USED:  AX,BX,CX
; STACK:
; ASSUME  CB:ROMCOD
-----
00D3' 8B C3      DSPERZ  PROC  NEAR
00D5' EB 0A      MOV      AX,BX
00D7' 30 E4      JMP      SHORT DSPERX
00D9' 96
00DA' BE 0000"
00DD' EB 06
00DF'
00DF' 30 E4      DSPKR   PROC  NEAR
00E1' 56          XOR      AH,AH
00E2' BE 001A"   PUSH     SI
00E4' 56          MOV      SI,OFFSET KERMSG
00E6' 56          JMP      SHORT DSPERI
00E8'
00E8' 30 E4      DSPERR  PROC  NEAR
00EA' 56          XOR      AH,AH
00EB' BE 001A"   PUSH     SI
00ED' 56          MOV      SI,OFFSET SERIMS
00EF'

```

```

00E5' 00E5' 9C
00E6' 00E6' 1E
00E7' 2EBE 1E C180
00E8' 00E8' 50
00E9' E8 00BF
00F0' B8 202D
00F1' E8 00F9
00F2' 58
00F3' 58
00F4' 50
00F5' B6 E0
00F6' E8 00EF
00F7' 58
00F8' E8 00EB
00F9' B8 0A0D
0100' E8 FF9C
0101' E8 00BB
0102' 1F
0103' 9D
0104' 5E
0105' C3

567 DBPER1:
568 PUSHF
569 PUSH DB
570 MOV DB,CB:WORD PTR DBADDR+CSHRAP
571 PUSH AX
572 CALL MSG
573 MOV AX, '-'
574 CALL OUT2CH
575 POP AX
576 PUSH AX
577 XCHQ AH,AL
578 CALL OUTBYT
579 POP AX
580 CALL OUTBYT
581 MOV AX,0A0DH
582 CALL PRTWDX
583 CALL PUPBEL
584 POP DB
585 POPF
586 POP BI
587 RET
588 *****
589 ; NMI interrupt entry point for normal processing
590 ;
591 ; Checks for parity interrupt and shuts down and reports error if so,
592 ; reports unexpected NMI otherwise. If anyone ever uses NMI for
593 ; anything else, they should intercept the vector to the beginning of
594 ; the routine.
595 ;
596 ; STACK: Who cares?
597 ;
598 PARM50 DB , Ignore, Reboot7',00

599 ;
600 NM100 PROC FAR
601 PUSH AX
602 MOV AL,UC_DTR+UC_RXE+UC_RT8 ; Save registers
603 OUT KBCMD,AL ; Force interrupt on permanently
604 IN AL,PRCI_P ; Get parity interrupt status.
605 AND AL,PARINT ; G: Parity interrupt?
606 MOV AL,XNMERR ; Set up unexpected NMI error code
607 JZ NM104 ; N: Jump and exit.
608 MOV AL,MPERR ; Y: Display the RAM parity error.
609 DEC WORD PTR CS:MENBIZ+CSHRAP ; require full powerup on warm reset.
610 PUSH BX ; Save the user registers
611 PUSH CX
612 PUSH DX
613 PUSH BI
614 MOV AH,10H
615 CALL DSPERX
616 MOV BI,OFFSET PARM50 ; 104X
; Display the error message.
; Parity error message.

```



```

0140' EB 006C
0143' 30 E4
0145' CD 4A
0147' 24 DF
0149' 3C 52
014B' 75 05
014D' EA 0006" 0000"
0152' 3C 49
0154' 75 ED
0156' 5E
0157' 5A
0158' 59
0159' 58
015A' 58
015B' CF

        M50
        AH,AH
        KEYINT
        AL,ODFH
        AL,'R'
        NMIOB
        PWRUP
        AL,'I'
        NMIO6
        SI
        DX
        POP CX
        POP BX
        POP AX
        IRET

        NMIO6:
        CALL XDR
        INT
        AND
        CMP
        JNZ
        JMP
        CMP
        JNZ
        POP
        POP
        POP
        POP
        IRET

        NMIOB:
        NMIO6
        SI
        DX
        POP CX
        POP BX
        POP AX
        IRET

; *****
; NMI interrupt entry point for powerup
;
; Checks for parity interrupt and shuts down and reports error if so,
; reports unexpected NMI otherwise. If anyone ever uses NMI for
; anything else, they should intercept the vector to the beginning of
; the routine.
;
; STACK: Who cares?
;
PUPNMI PROC FAR
MOV AL,UC_DTR+UC_RXE+UC_RTS ; Force interrupt on permanently
OUT KBCMD,AL
IN AL,PRCI_P
AND AL,PARINT
MOV AL,LED100
MOV DL,IVERR
JZ PUPNM4
MOV AL,LED101
XOR DL,DL
PUPNM4: JMP DSPDIE
; *****
; WILD$$ - Illegal (soft or hard) interrupt exit
; This exit is taken if an interrupt occurs from any of the unused
; interrupt vectors. The cause could be either a software interrupt
; or an errant hardware interrupt. This routine puts a fixed error
; code into AL and jumps to ARGHHH.
; INPUT: (none)
; OUTPUT: Error message to the screen (fixed error code), PC, beep
; USED:
; STACK:
; ASSUME CB:ROMCOD
;
WILD$$ PROC FAR
MOV AL,WLDERR
; Set up 'Wild' interrupt error code
    
```

```

0173' EB 02      JMP      SHORT ARGNER      , Exit the standard way
669
670
671
672 *****
673 FATAL - Fatal error exit (software buggie)
674
675 This exit is taken as the last resort by any software routine
676 which has given up on the normal error-recovery procedures. It
677 puts a fixed error code into AL and jumps to ARGHH. Typical user
678 would be ZEX.
679 INPUT: (none)
680 OUTPUT: Error message to the screen (fixed error code), PC, beep
681 USED:
682 STACK:
683 ASSUME CB:ROMCOD
-----
0175'          FATAL PROC FAR
0175'          MOV     AL,FATERR      , Set up fatal error code
686          JMP     SHORT ARGNER      , Do the standard thing
687 *****
688 ARGHH - (Just like it sounds) Display error message and wait.
689 This routine is used by anyone that wants to halt with an
690 error message but still be alive enough to restart from the
691 keyboard. This should be used for all cases except during
692 powerup. In addition to the standard 'SYSTEM ERROR' message
693 and beep, the calling CB:IP is displayed as a debugging aid.
694
695 INPUT: AL = 2-digit error code to be displayed (ARGHH adds leading 10)
696 OUTPUT: Error message to the screen, PC, beep
697 USED:
698 STACK:
699 ASSUME CB:ROMCOD
-----
0177'          ARGNER PROC NEAR
0177'          ARGHH PROC FAR
0177'          MOV     AH,10H
0179'          MOV     DL,LF
0179'          B4 10      , Set high byte of error to 10
0179'          B2 0A      ,
0178'          CALL    DSPERX
017E'          MOV     AX,'@'
017E'          B8 4020    , Print error
0181'          CALL    OUT2CH
0181'          EB 0068    , Delimiter ('@')
0184'          MOV     BP,SP
0184'          B8 EC      , Set up BP
0186'          MOV     AX,2IBPJ
0186'          B8 46 02    , Get user's CB
0186'          EB 0059    ,
0186'          CALL    OUTWRD
0186'          B0 3A      , Display it
018C'          MOV     AL,'.'
018C'          B4 0E      , Delimiter
018E'          MOV     AH,CRINTY
018E'          EB 0001"    , Use TTY screen
0190'          CALL    CRTOUT
0190'          B8 46 00    , Output it (to screen only)
0193'          MOV     AX,[BP]
0193'          EB 004C    , Get user's IP
0196'          CALL    OUTWRD
0196'          B8 0A0D    , Display it
0199'          MOV     AX,CR + (LF SHL 8)
0199'          EB 0050    , CRLF
019C'          CALL    OUT2CH
019C'          EB 0050    ,
019F'          MOV     AL,(ENAB7)
019F'          B0 7F      , Leave only keyboard enabled
01A1'          OUT     INTA01,AL
01A1'          E6 19      , Set mask register
01A3'          MOV     AL,11000110B
01A3'          B0 C6      , Readjust priority of controller
01A3'          EB 00 C6
700
701          ARGNER PROC NEAR
702          ARGHH PROC FAR
703          MOV     AH,10H
704          MOV     DL,LF
705          B4 10      , Set high byte of error to 10
705          B2 0A      ,
706          CALL    DSPERX
706          B8 4020    , Print error
707          MOV     AX,'@'
707          CALL    OUT2CH
707          EB 0068    , Delimiter ('@')
708          MOV     BP,SP
708          B8 EC      , Set up BP
709          MOV     AX,2IBPJ
709          B8 46 02    , Get user's CB
710          EB 0059    ,
710          CALL    OUTWRD
710          B0 3A      , Display it
711          MOV     AL,'.'
711          B4 0E      , Delimiter
712          MOV     AH,CRINTY
712          EB 0001"    , Use TTY screen
713          CALL    CRTOUT
713          B8 46 00    , Output it (to screen only)
714          MOV     AX,[BP]
714          EB 004C    , Get user's IP
715          CALL    OUTWRD
715          B8 0A0D    , Display it
716          MOV     AX,CR + (LF SHL 8)
716          EB 0050    , CRLF
717          CALL    OUT2CH
717          EB 0050    ,
718          MOV     AL,(ENAB7)
718          B0 7F      , Leave only keyboard enabled
719          OUT     INTA01,AL
719          E6 19      , Set mask register
720          MOV     AL,11000110B
720          B0 C6      , Readjust priority of controller
    
```

```

01A5' E6 1B      721      OUT      INTA00,AL      ; So keyboard is highest priority
01A7' B0 0F      722      MOV      AL,OFH      ; Turn off floppy motors
01A9' E6 04      723      MOV      DSKS_P,AL
01AB' FB         724      STI
01AC' F4         725      ARHALT:      ; Turn interrupts back on
01AD' EB FD      726      HLT
01AE'           727      JMP      SHORT ARHALT      ; Wait.....
01AF'           728      ; *****
01B0'           729      ; MSG - Output string of characters in the current CS to the CRT.
01B1'           730      ; The string should be terminated with a zero byte.
01B2'           731      ; INPUT: SI = string offset in current CS
01B3'           732      ; OUTPUT: (screen)
01B4'           733      ; USED: AX,SI
01B5'           734      ; STACK:
01B6'           735      ; ASSUME CS:ROMCOD, DS:NOTHING
01B7'           736      ;
01B8'           737      MSG0:      PROC      NEAR
01B9'           738      PUSH    CX
01BA'           739      PUSH    DX
01BB'           740      CLD
01BC'           741      ;
01BD'           742      MSG0:      MOV      AH,CRTWTY      ; Set CRT for TTY output
01BE'           743      LODS   CS:BYTE PTR [SI] ; Pickup byte
01BF'           744      OR      AL,AL      ; Check for end
01C0'           745      JZ      MSG1
01C1'           746      CALL   CRTOUT      ; Print char
01C2'           747      JMP      SHORT MSG0
01C3'           748      ;
01C4'           749      MSG1:      POP      DX
01C5'           750      POP      CX
01C6'           751      RET
01C7'           752      ; *****
01C8'           753      ; PUPBEL - This routine is used to sound the system beeper without
01C9'           754      ; using timing services or any stack other than that used to call
01CA'           755      ; this subroutine (which can be faked).
01CB'           756      ;
01CC'           757      ; NOTE THAT INTERRUPTS ARE DISABLED IN THIS ROUTINE FOR THE DURATION
01CD'           758      ; OF THE 1 SECOND BEEP DELAY AND NOT REENABLED. THIS IS NOT
01CE'           759      ; INTENDED TO BE A GENERAL PURPOSE ROUTINE.
01CF'           760      ;
01D0'           761      ; OUTPUT: (***) BEEP (***)
01D1'           762      ; USED: AL
01D2'           763      ; STACK: 2 bytes (usually faked)
01D3'           764      ; ASSUME CS:ROMCOD, DS:NOTHING
01D4'           765      ;
01D5'           766      PUPBEL:      PROC      NEAR
01D6'           767      CLI
01D7'           768      MOV     AL,(TC_SCO + TC_WRD + TC_MD3) ; Set the timer
01D8'           769      OUT     TIMCMD,AL
01D9'           770      MOV     AL,LOW BELTMR ; 1.25 MHz / BELTMR = frequency
01DA'           771      OUT     TIMERO,AL
01DB'           772      MOV     AL,HIGH BELTMR
    
```

```

01CD' E6 14          773      OUT      TIMER0,AL
01CF' A0 000A"      774      MOV      AL,DSKC_M
01D2' 0C 01          775      OR       AL,BPKREN
01D4' E6 00          776      OUT      DBKC_P,AL
01D6' 31 C9          777      XOR      CX,CX
01D8' E2 FE          778      LOOP   $
01DA' E2 FE          779      LOOP   $
01DC' E2 FE          780      LOOP   $
01DE' E2 FE          781      LOOP   $
01E0' 34 01          782      XOR      AL,BPKREN
01E2' E6 00          783      OUT      DBKC_P,AL
01E4' C3            784      RET

*****
; OUTWRD/OUTBYT - output word(byte) in AX(AL) to the screen in hex
;
;
; INPUT:  AX/AL = number
; OUTPUT: (display)
; USED:   AX,BX
; STACK:
; ASSUME CB:ROMCOD (LOTS)
;
OUTWRD  PROC  NEAR
01E5'          794      PUSH  AX
01E5' 50          795      MOV   AL,AH
01E6' 8A C4          796      CALL OUTBYT
01E8' EB 0001          797      POP  AX
01EB' 5B          799      JMP  OUT2CH
;
OUTBYT  PROC  NEAR
01EC'          800      CALL  CNVBYT
01EC' EB 001B          801      JMP  OUT2CH
;
OUT2CH  - Output two chars to console, and factory test interface,
; if connected.
;
; INPUT:  AL = 1st char to be output
;         AH = 2nd char to be output
; OUTPUT: (display & parallel port)
; USED:   AX,BX
; STACK:
; ASSUME CB:ROMCOD
;
OUT2CH  PROC  NEAR
01EF'          814      PUSH  CX
01EF' 51          815      PUSH  DX
01F0' 52          816      MOV   BX,AX
01F1' 8B DB          817      MOV   AH,CRTWTY
01F3' B4 0E          818      CALL  CRTOUT
01F5' EB 0001"      819      MOV   AL,BH
01F8' BA C7          820      MOV   AH,CRTWTY
01FA' B4 0E          821      CALL  CRTOUT
01FC' EB 0001"      822      MOV   AX,BX
01FF' 8B C3          823      MOV   PRTNDX
0201' EB FE9F          824      CALL  PRTNDX
;
; Save the characters
; Set up for TTY output
; Output char in AL as usual
; Now do char in AH
; Restore AX
; and send it to the test I/F

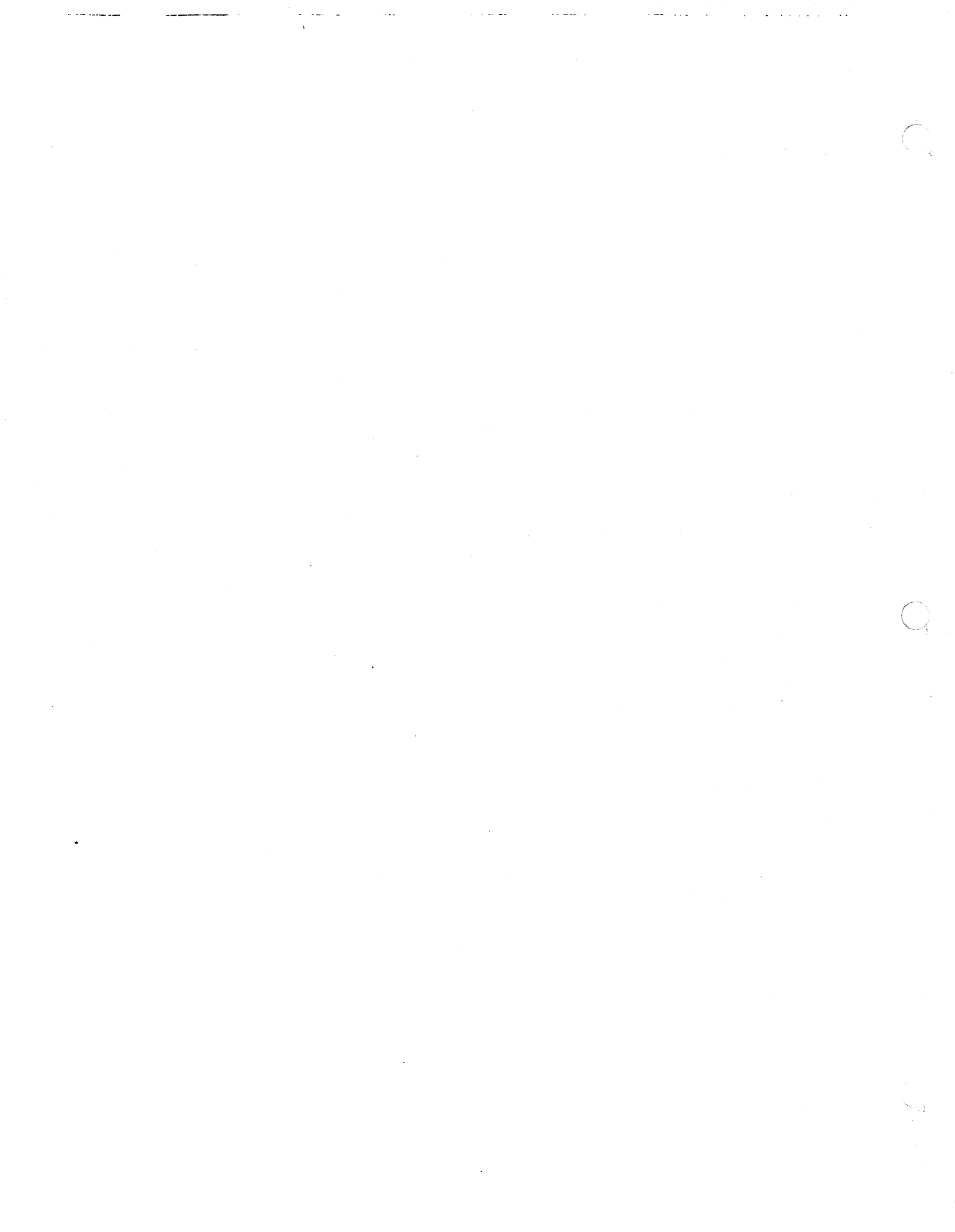
```

```

0204' 5A      POP      DX
0205' 59      POP      CX
0206' C3      RET
0208'          ; *** RETURN ***
-----
0207'          CNVBYT - Convert binary byte to Hex-ASCII
0207'          INPUT: AL = byte to be converted
0208'          OUTPUT: AL = ASCII representation for high-order nibble
0209'          AH = ASCII representation for low-order nibble
020A'          USED: AX, BX
020B'          STACK: 4
020C'          ASSUME CS:ROMCOD
-----
020D'          CNVBYT PROC NEAR
020E'          PUSH  CX
020F'          MOV   BX, OFFSET BINASC
0210'          MOV   CL, AL
0211'          ; Must save CX
0212'          ; Address of lookup table
0213'          ; Save other nibble
0214'          ; Right nibble first
0215'          AND   AL, OFH
0216'          XLAT  BYTE PTR CS:[BX]
0217'          MOV   AH, AL
0218'          ; Extract (3:0)
0219'          ; Translate AL (--- [BX+AL])
021A'          ; Save result
021B'          ; Then left nibble
021C'          MOV   AL, CL
021D'          MOV   CL, A
021E'          SHR  AL, CL
021F'          XLAT  BYTE PTR CS:[BX]
0220'          POP   CX
0221'          RET
0222'          END
-----
0213' BA C1    ; Retrieve other nibble
0215' B1 04    ; Prepare for 4 shifts
0217' D2 EB    ; Extract (7:4)
0219' 2ED7    ; translate: AL (--- [BX+AL])
021B' 59      ; restore
021C' C3      ; *** RETURN ***

```

No errors detected



```
1 *****  
2 ; TITLE - PRTDSR - Printer DSR  
3 ; COMPUTER - BOBB Assembly Language  
4 ; ABSTRACT - This module contains the interface logic for the parallel  
5 ; printer port.  
6 ; *****  
7 ; NAME PRTDSR - Printer interface routines  
8 ; SUBTTL  
9 ; *****  
10 ; PUBLIC DEFINITIONS  
11 ; *****  
12 ;  
13 ;  
14 PUBLIC PRINT  
15 PUBLIC PRT_IO  
16 PUBLIC PRTINI  
17 ;  
18 ;  
19 ; EXTERNAL REFERENCES  
20 ; *****  
21 ;  
22 ; SECT ROMDAT  
23 EXTRN PRCD_M:BYTE ; Printer control latch memory (ROMDAT)  
24 ;  
25 ; *****  
26 ; LOCAL CONSTANTS  
27 ; *****  
28 ;  
29 ; INCLUDE PEG:PORTADDR.EGU  
30 ; INCLUDE PEG:LATCHES.EGU  
31 ; INCLUDE PEG:VECTOR.EGU  
32 ;  
187  
188 PRMASK EQU (PRBUSY+PRPAPO+PRSLCT+PRFAUL) ; Make a printer mask  
189  
190 ; *****  
191 ; CODE SEGMENT DEFINITION  
192 ; *****  
193 ;  
194 ; SECT ROMCOD  
195 ASSUME CS:ROMCOD, DS:ROMDAT  
196 ;  
197 ; *****  
198 ; MODULE ENTRY POINT  
199 ; *****  
200 ;  
201 ; PARALLEL PRINT PORT 'DSR' - Interrupt 4BH  
202 ;  
203 ; INPUT: AH = function: (see the file PEG:PRTOP.EGU)  
204 ; 0 - output char to printer  
205 ; 1 - initialize the printer  
206 ; 2 - return printer status  
207 ; AL = character to be printed (function 0 only)
```



```

208 ;
209 ;
210 ;
211 ;
212 ;
213 ;
214 ;
215 ;
216 ;
217 ;
218 ;
219 ;
220 ;
221 ;
222 ;
223 ;
224 ;
225 ;
226 ;
227 ;
228 ;
229 ;
230 ;
231 ;
232 ;
233 ;
234 ;
235 ;
236 ;
237 ;
238 ;
239 ;
240 ;
241 ;
242 ;
243 ;
244 ;
245 ;
246 ;
247 ;
248 ;
249 ;
250 ;
251 ;
252 ;
253 ;
254 ;
255 ;
256 ;
257 ;
258 ;
259 ;

0000'
0001'
0002'
0003'
0004'
0009'
000C'
000D'
000E'
000F'

0010'
0011'
0012'
0014'
0016'
0018'
001A'
001C'

001D'
001E'
0021'
0023'
0025'
0028'
002B'
002E'
002A'
002C'

FB
1E
51
53
2EBE 1E C180
EB 0004
58
59
1F
CF

08 E4
74 2A
FE CC
74 05
FE CC
74 15
C3

FA
A0 0001"
24 BF
E6 03
B9 0096
E2 FE
OC 40
E6 03

208 ; OUTPUT: AH = Status of printer as follows:
209 ;
210 ; Bit: 7 - 1 = FAULT
211 ; 6 - 1 = SELECTED
212 ; 5 - 1 = PAPER OUT
213 ; 4 - 1 = BUSY
214 ; 0 - 1 = PRINTER TIME OUT
215 ;
216 ; AL = character (unchanged)
217 ; USED: AX
218 ; STACK: 12 bytes + what the function uses
219 ;
220 ;-----
221 PRT_ID PROC FAR
222 STI
223 PUSH DS
224 PUSH CX
225 PUSH BX
226 MOV DS,WORD PTR CS:DSADDR+CSWRAP ; Set my DS
227 CALL DO_PRT ; Do the function
228 POP BX
229 POP CX
230 POP DS
231 IRET
232 ;
233 ;
234 ; DO_PRT PROC
235 OR NEAR
236 AH,AH
237 PRINT
238 AH
239 JZ
240 DEC
241 JZ
242 DEC
243 JZ
244 PRSTAT
245 RET
246 ;
247 ;
248 ;
249 ;
250 ;
251 ;
252 ;
253 ;
254 ;
255 ;
256 ;
257 ;
258 ;
259 ;
260 ;
261 ;
262 ;
263 ;
264 ;
265 ;
266 ;
267 ;
268 ;
269 ;
270 ;
271 ;
272 ;
273 ;
274 ;
275 ;
276 ;
277 ;
278 ;
279 ;
280 ;
281 ;
282 ;
283 ;
284 ;
285 ;
286 ;
287 ;
288 ;
289 ;
290 ;
291 ;
292 ;
293 ;
294 ;
295 ;
296 ;
297 ;
298 ;
299 ;
300 ;
301 ;
302 ;
303 ;
304 ;
305 ;
306 ;
307 ;
308 ;
309 ;
310 ;
311 ;
312 ;
313 ;
314 ;
315 ;
316 ;
317 ;
318 ;
319 ;
320 ;
321 ;
322 ;
323 ;
324 ;
325 ;
326 ;
327 ;
328 ;
329 ;
330 ;
331 ;
332 ;
333 ;
334 ;
335 ;
336 ;
337 ;
338 ;
339 ;
340 ;
341 ;
342 ;
343 ;
344 ;
345 ;
346 ;
347 ;
348 ;
349 ;
350 ;
351 ;
352 ;
353 ;
354 ;
355 ;
356 ;
357 ;
358 ;
359 ;
360 ;
361 ;
362 ;
363 ;
364 ;
365 ;
366 ;
367 ;
368 ;
369 ;
370 ;
371 ;
372 ;
373 ;
374 ;
375 ;
376 ;
377 ;
378 ;
379 ;
380 ;
381 ;
382 ;
383 ;
384 ;
385 ;
386 ;
387 ;
388 ;
389 ;
390 ;
391 ;
392 ;
393 ;
394 ;
395 ;
396 ;
397 ;
398 ;
399 ;
400 ;
401 ;
402 ;
403 ;
404 ;
405 ;
406 ;
407 ;
408 ;
409 ;
410 ;
411 ;
412 ;
413 ;
414 ;
415 ;
416 ;
417 ;
418 ;
419 ;
420 ;
421 ;
422 ;
423 ;
424 ;
425 ;
426 ;
427 ;
428 ;
429 ;
430 ;
431 ;
432 ;
433 ;
434 ;
435 ;
436 ;
437 ;
438 ;
439 ;
440 ;
441 ;
442 ;
443 ;
444 ;
445 ;
446 ;
447 ;
448 ;
449 ;
450 ;
451 ;
452 ;
453 ;
454 ;
455 ;
456 ;
457 ;
458 ;
459 ;
460 ;
461 ;
462 ;
463 ;
464 ;
465 ;
466 ;
467 ;
468 ;
469 ;
470 ;
471 ;
472 ;
473 ;
474 ;
475 ;
476 ;
477 ;
478 ;
479 ;
480 ;
481 ;
482 ;
483 ;
484 ;
485 ;
486 ;
487 ;
488 ;
489 ;
490 ;
491 ;
492 ;
493 ;
494 ;
495 ;
496 ;
497 ;
498 ;
499 ;
500 ;
501 ;
502 ;
503 ;
504 ;
505 ;
506 ;
507 ;
508 ;
509 ;
510 ;
511 ;
512 ;
513 ;
514 ;
515 ;
516 ;
517 ;
518 ;
519 ;
520 ;
521 ;
522 ;
523 ;
524 ;
525 ;
526 ;
527 ;
528 ;
529 ;
530 ;
531 ;
532 ;
533 ;
534 ;
535 ;
536 ;
537 ;
538 ;
539 ;
540 ;
541 ;
542 ;
543 ;
544 ;
545 ;
546 ;
547 ;
548 ;
549 ;
550 ;
551 ;
552 ;
553 ;
554 ;
555 ;
556 ;
557 ;
558 ;
559 ;
560 ;
561 ;
562 ;
563 ;
564 ;
565 ;
566 ;
567 ;
568 ;
569 ;
570 ;
571 ;
572 ;
573 ;
574 ;
575 ;
576 ;
577 ;
578 ;
579 ;
580 ;
581 ;
582 ;
583 ;
584 ;
585 ;
586 ;
587 ;
588 ;
589 ;
590 ;
591 ;
592 ;
593 ;
594 ;
595 ;
596 ;
597 ;
598 ;
599 ;
600 ;
601 ;
602 ;
603 ;
604 ;
605 ;
606 ;
607 ;
608 ;
609 ;
610 ;
611 ;
612 ;
613 ;
614 ;
615 ;
616 ;
617 ;
618 ;
619 ;
620 ;
621 ;
622 ;
623 ;
624 ;
625 ;
626 ;
627 ;
628 ;
629 ;
630 ;
631 ;
632 ;
633 ;
634 ;
635 ;
636 ;
637 ;
638 ;
639 ;
640 ;
641 ;
642 ;
643 ;
644 ;
645 ;
646 ;
647 ;
648 ;
649 ;
650 ;
651 ;
652 ;
653 ;
654 ;
655 ;
656 ;
657 ;
658 ;
659 ;
660 ;
661 ;
662 ;
663 ;
664 ;
665 ;
666 ;
667 ;
668 ;
669 ;
670 ;
671 ;
672 ;
673 ;
674 ;
675 ;
676 ;
677 ;
678 ;
679 ;
680 ;
681 ;
682 ;
683 ;
684 ;
685 ;
686 ;
687 ;
688 ;
689 ;
690 ;
691 ;
692 ;
693 ;
694 ;
695 ;
696 ;
697 ;
698 ;
699 ;
700 ;
701 ;
702 ;
703 ;
704 ;
705 ;
706 ;
707 ;
708 ;
709 ;
710 ;
711 ;
712 ;
713 ;
714 ;
715 ;
716 ;
717 ;
718 ;
719 ;
720 ;
721 ;
722 ;
723 ;
724 ;
725 ;
726 ;
727 ;
728 ;
729 ;
730 ;
731 ;
732 ;
733 ;
734 ;
735 ;
736 ;
737 ;
738 ;
739 ;
740 ;
741 ;
742 ;
743 ;
744 ;
745 ;
746 ;
747 ;
748 ;
749 ;
750 ;
751 ;
752 ;
753 ;
754 ;
755 ;
756 ;
757 ;
758 ;
759 ;
760 ;
761 ;
762 ;
763 ;
764 ;
765 ;
766 ;
767 ;
768 ;
769 ;
770 ;
771 ;
772 ;
773 ;
774 ;
775 ;
776 ;
777 ;
778 ;
779 ;
780 ;
781 ;
782 ;
783 ;
784 ;
785 ;
786 ;
787 ;
788 ;
789 ;
790 ;
791 ;
792 ;
793 ;
794 ;
795 ;
796 ;
797 ;
798 ;
799 ;
800 ;
801 ;
802 ;
803 ;
804 ;
805 ;
806 ;
807 ;
808 ;
809 ;
810 ;
811 ;
812 ;
813 ;
814 ;
815 ;
816 ;
817 ;
818 ;
819 ;
820 ;
821 ;
822 ;
823 ;
824 ;
825 ;
826 ;
827 ;
828 ;
829 ;
830 ;
831 ;
832 ;
833 ;
834 ;
835 ;
836 ;
837 ;
838 ;
839 ;
840 ;
841 ;
842 ;
843 ;
844 ;
845 ;
846 ;
847 ;
848 ;
849 ;
850 ;
851 ;
852 ;
853 ;
854 ;
855 ;
856 ;
857 ;
858 ;
859 ;
860 ;
861 ;
862 ;
863 ;
864 ;
865 ;
866 ;
867 ;
868 ;
869 ;
870 ;
871 ;
872 ;
873 ;
874 ;
875 ;
876 ;
877 ;
878 ;
879 ;
880 ;
881 ;
882 ;
883 ;
884 ;
885 ;
886 ;
887 ;
888 ;
889 ;
890 ;
891 ;
892 ;
893 ;
894 ;
895 ;
896 ;
897 ;
898 ;
899 ;
900 ;
901 ;
902 ;
903 ;
904 ;
905 ;
906 ;
907 ;
908 ;
909 ;
910 ;
911 ;
912 ;
913 ;
914 ;
915 ;
916 ;
917 ;
918 ;
919 ;
920 ;
921 ;
922 ;
923 ;
924 ;
925 ;
926 ;
927 ;
928 ;
929 ;
930 ;
931 ;
932 ;
933 ;
934 ;
935 ;
936 ;
937 ;
938 ;
939 ;
940 ;
941 ;
942 ;
943 ;
944 ;
945 ;
946 ;
947 ;
948 ;
949 ;
950 ;
951 ;
952 ;
953 ;
954 ;
955 ;
956 ;
957 ;
958 ;
959 ;
960 ;
961 ;
962 ;
963 ;
964 ;
965 ;
966 ;
967 ;
968 ;
969 ;
970 ;
971 ;
972 ;
973 ;
974 ;
975 ;
976 ;
977 ;
978 ;
979 ;
980 ;
981 ;
982 ;
983 ;
984 ;
985 ;
986 ;
987 ;
988 ;
989 ;
990 ;
991 ;
992 ;
993 ;
994 ;
995 ;
996 ;
997 ;
998 ;
999 ;
1000 ;

```

```

002E' FB
002F' B4 00
260
261
262
263
264
265
266
267
268
269
270
271
272
273
274
275
276
277
278
279
280
281
282
283
284
285
286
287
288
289
290
291
292
293
294
295
296
297
298
299
300
301
302
303
304
305
306
307
308
309
310
311
0031'
0031' 50
0032' E4 01
0034' 24 F0
0036' 34 80
0038' 08 C4
003A' 59
003B' BA C1
003D' C3
260 BTI
261 MOV AH,00
262 JMP SHORT PRSTAT
263
264
265
266
267
268
269
270
271
272
273
274
275
276
277
278
279
280
281
282
283
284
285
286
287
288
289
290
291
292
293
294
295
296
297
298
299
300
301
302
303
304
305
306
307
308
309
310
311
    ; ; Restore interrupt status
    ; ; Set 'no error status'
    ; ; Return with printer status
    ;
    ;-----
    ; PRINTER STATUS ROUTINE
    ;-----
    ; INPUT: AH = bit zero has timeout status
    ;         (Note that AH=0 when STATUS called as a function thru PRT_10)
    ; OUTPUT: AH = status (see above)
    ; USED:  AH,CX
    ; STACK: 4
    ;
    ; PRSTAT PROC NEAR
    ; PUSH AX
    ; IN AL,PRCI_P
    ; AND AL,PRMABK
    ; XOR AL,PRFAUL
    ; OR AH,AL
    ; POP CX
    ; MOV AL,CL
    ; RET
    ; Save character
    ; Read printer status port
    ; Mask off the non-printer stuff
    ; Flip the (low true) FAULT bit
    ; OR AL (real status) into AH (has timeout)
    ; Restore the character
    ; *** RETURN ***
  
```

```

    ; PRINTER OUTPUT ROUTINE - Contains factory interface logic. If PAPER OUT
    ; remains active and BUSY is inactive, then the data is strobed with
    ; the AUTOFEED line instead of the normal BISTROBE signal.
    ;
    ; INPUT: AL = character to be printed
    ; OUTPUT: AL = the character (unchanged)
    ;         AH = printer status (see above)
    ; USED:  AH,BL,CX
    ; STACK: 4 bytes
    ;
    ;-----
    ; PRINT PROC NEAR
    ; MOV BL,AL
    ; MOV AH,00
    ; MOV CX,50000
    ; OUT PRDA_P,AL
    ;
    ; PR1:
    ; IN AL,PRCI_P
    ; TEST AL,PRBUSY
    ; JZ PR2
    ; LOOP PR1
    ; MOV AH,1
    ; JMP SHORT PRXIT
    ;
    ; PR2:
    ; MOV CX,10
    ; LOOP $
    ; IN AL,PRCI_P
    ; TEST AL,PRPAPD
    ;
    ;
    ; Save character
    ; Reset status initially
    ; Delay count for about 1/3 second
    ; Put printer data out to latch
    ; Read printer status latch
    ; G: Printer busy ?
    ; N: Continue
    ; Y: Loop
    ; TIMEOUT: Set timeout bit
    ; and exit
    ; do a short wait before writing data
    ; Look at printer status latch (already in AL)
    ; G: Paper Out ? (Paperout AND Not Busy is
    ; Indication of factory connection)
  
```

```
005A' FA      312  
005B' A0 0001" 313  
005E' 74 09    314  
0060' 24 EF    315  
0062' E6 03    316  
0064' 90      317  
0065' 34 10    318  
0067' EB 07    319  
0069'         320  
0069' 24 DF    321  
0069' 24 DF    322  
006B' E6 03    323  
006D' 90      324  
006E' 34 20    325  
0070'         326  
0070' E6 03    327  
0072' FB      328  
0073'         329  
0073' BA C3    330  
0075' EB BA    331  
0075' EB BA    332  
0075' EB BA    333
```

```
      (Protect this)  
      ; ; ; Get copy of latch from memory  
      ; ; ; N: Use normal interface  
      ; ; ; Y: Use factory interface  
      ; ; ; (Use the Autofeed signal as data strobe)  
      ; ; ; Set the autofeed signal low  
      ; ; ; Delay  
      ; ; ; Finish up like normal
```

```
AL,PRCD_M      ; ; ;  
PR3            ; ; ;  
AL,NOT PRAUTO ; ; ;  
PRCD_P.AL     ; ; ;  
AL,PRAUTO     ; ; ;  
SHORT PR4     ; ; ;  
AL,NOT PRSTRB ; ; ;  
PRCD_P.AL     ; ; ;  
AL,PRSTRB     ; ; ;  
PRCD_P.AL     ; ; ;  
AL,BL         ; ; ;  
SHORT PRSTAT ; ; ;
```

```
CLI  
MOV  
JZ  
  
AND  
OUT  
NOP  
XOR  
JMP  
  
AND  
OUT  
NOP  
XOR  
  
OUT  
STI  
  
MOV  
JMP  
  
END
```

```
PR3:  
  
PR4:  
  
PRXIT:
```

No errors detected

5

6

7

```
1 ; *****  
2 ; TITLE - PUPT91 - PEGABUS POWERUP TEST CODE, OPTION RAM AND ROMS TEST.  
3 ; ABSTRACT - THE TEST IS DESIGNED TO CHECK OUT THE OPTION ROM AND RAM.  
4 ; ANY ADDITIONAL ROMS ARE CALLED IF THEY TEST OUT OK TO ALLOW  
5 ; INITIALIZATION OR BOOTING OF OTHER DEVICES IN THE SYSTEM.  
6 ;  
7 ; REGISTERS USED - ALL  
8 ;  
9 ; STACK USED - 4 BYTES UNLESS THERE IS A ROM ERROR, THEN "DSPERR"+2.  
10 ;  
11 ; * Register is saved during call to DSPERR to fix crash after bad option ROM  
12 ; CRC.  
13 ; * EXTERNS are put into BECT sections to correct bug causing incorrect value  
14 ; of PRCD_M to be linked which caused unexpected printer reset.  
15 ; * powerup_RAM test changed to allow 786432 bytes of main memory (originally  
16 ; 524288 bytes) also fires bugs concerning large amounts of main RAM  
17 ; *****  
18 ; NAME PUPT91  
19 ; SUBTTL PEGABUS POWERUP DIAGNOSTIC TESTS  
20 ; *****  
21 ; PUBLIC DEFINITIONS  
22 ; *****  
23 ; PUBLIC PUPT91  
24 ; *****  
25 ; MODULE ENTRY POINT  
26 ; *****  
27 ; EXTERNAL REFERENCES  
28 ; *****  
29 ; EXTERN OPRERR:ABS ;OPTION RAM FAIL DURING PUPT91  
30 ; EXTERN RMPERR:ABS ;RAM PARITY ERROR  
31 ; EXTERN ROMEX:ABS ;ROM ERRORS ERROR CODE  
32 ; EXTERN ROMSIZ:ABS ;SIZE OF ROM SPACE.  
33 ; EXTERN XNMERR:ABS ;UNEXPECTED NMI  
34 ; SECT ROMCOD  
35 ; ASSUME CS:ROMCOD  
36 ; EXTERN ARGHH:NEAR ;ADDRESS OF FAILURE ROUTINE  
37 ; EXTERN DKBOOT:NEAR ;ADDRESS OF DECREMENT LED ROUTINE  
38 ; EXTERN DSPERR:NEAR ;ADDRESS OF POWERUP BOOT ROUTINE  
39 ; EXTERN NMIGD:FAR ;ADDRESS OF FAILURE ROUTINE  
40 ; EXTERN ROMTST:NEAR ;NMI INTERRUPT ENTRY  
41 ; EXTERN SYSROM:BYTE ;ADDRESS OF TEST SUBROUTINE  
42 ; SECT ROMDAT  
43 ; EXTERN PRCD_M:BYTE ;ADDRESS OF LATCH SAVE BYTE  
44 ; *****  
45 ; LOCAL CONSTANTS  
46 ; *****  
47 ; INCLUDE PEG:PORTADDR.EGU  
48 ; INCLUDE PEG:LATCHES.EGU  
49 ; INCLUDE PEG:VECTOR.EGU  
50 ;  
205 NMIVC EQU NMIINT*4 ;NMI INTERRUPT VECTOR LOCATION  
206 ; *****  
207 ; *****
```

=0000

=0000

=000B

9

9

9

```

208 ;
209 ; ***** MODULE ENTRY POINT *****
210 SECT ROMCOD ; ALL CODE IS PLACE HERE *****
211 ASSUME CS:ROMCOD,DS:ABS0,ES:ABS0
212 ;
213 ; DO NOTHING IF WARM RESET, ELSE CHECK OPTION RAM.
214 ;
215 PUP7B1 PROC NEAR
216 MOV AX,WORD PTR CS:MEMSIZ+CSWRAP ;GET SIZE OF RAM.
217 ; 1000H (= RAMSIZ (= C000H IF WARM RESET
218 ; ADJUST SIZE SO THAT RANGE IS FROM
219 ; 1 (= AX (= 0CH
220 ; 0 (= AX (= 0BH
221 CMP AX,0000CH ;G: WARM RESET?
222 JB OPRXIT ; Y: EXIT TEST ROUTINE
223 ;
224 ; TEST OPTION RAM. SET UP PARITY VECTOR AND FILL RAM.
225 ;
226 OPRM: CLI ; DISABLE INTERRUPTS
227 MOV WORD PTR CS:NMIVEC+CSWRAP,OFFSET DERROR ;CHANGE PARITY VECTOR.
228 SI,1000H ; INITIAL OPTION RAM SEGMENT
229 MOV DS,BI ;SET UP RAM SEGMENT
230 MOV ES,BI
231 MOV BP,00000010B ; WALKING ONE
232 XOR DI,DI ;SET UP START OF RAM TO TEST
233 MOV AX,BP ;GET WALKING ONE TO AX.
234 ROR AX,1 ;SET UP THE CARRY BIT
235 MOV CX,8000H ;SIZE OF OPTION RAM
236 STOB WORD PTR(DI) ;STORE WALKING ONE
237 RCL AX,1 ; WALK IT
238 LOOP OPR1 ;DO THE WHOLE SEGMENT.
239
240 ;
241 ; READ AND CHECK THE OPTION RAM SEGMENT
242 ;
243 MOV BX,BP ;GET INITIAL PATTERN
244 ROR BX,1 ;SET UP THE CARRY BIT
245 LAHF ;SAVE FLAGS
246 MOV DX,BX ;CHECK BYTE
247 DX,DI ; IS THIS OK?
248 JNZ OERROR ; N: JUMP
249 SAHF ;GET FLAGS
250 RCL BX,1 ;NEXT PATTERN TO CHECK
251 INC DI ;BUMP POINTERS
252 INC DI ;BUMP POINTER
253 JNZ OPR2 ;CONTINUE TILL FINISHED
254 NOT BP ;SET UP FOR WALKING ZERO
255 AND BP,BP ;G: SECOND LOOP?
256 JS OPRO ;N: GO FOR NEXT LOOP
257 ADD SI,1000H ;BUMP TO NEXT SEGMENT.
258 CMP SI,0C000H ;G: END OF POSSIBLE RAM?
259 OPRAM ; N: JUMP AND TEST IT.
    
```



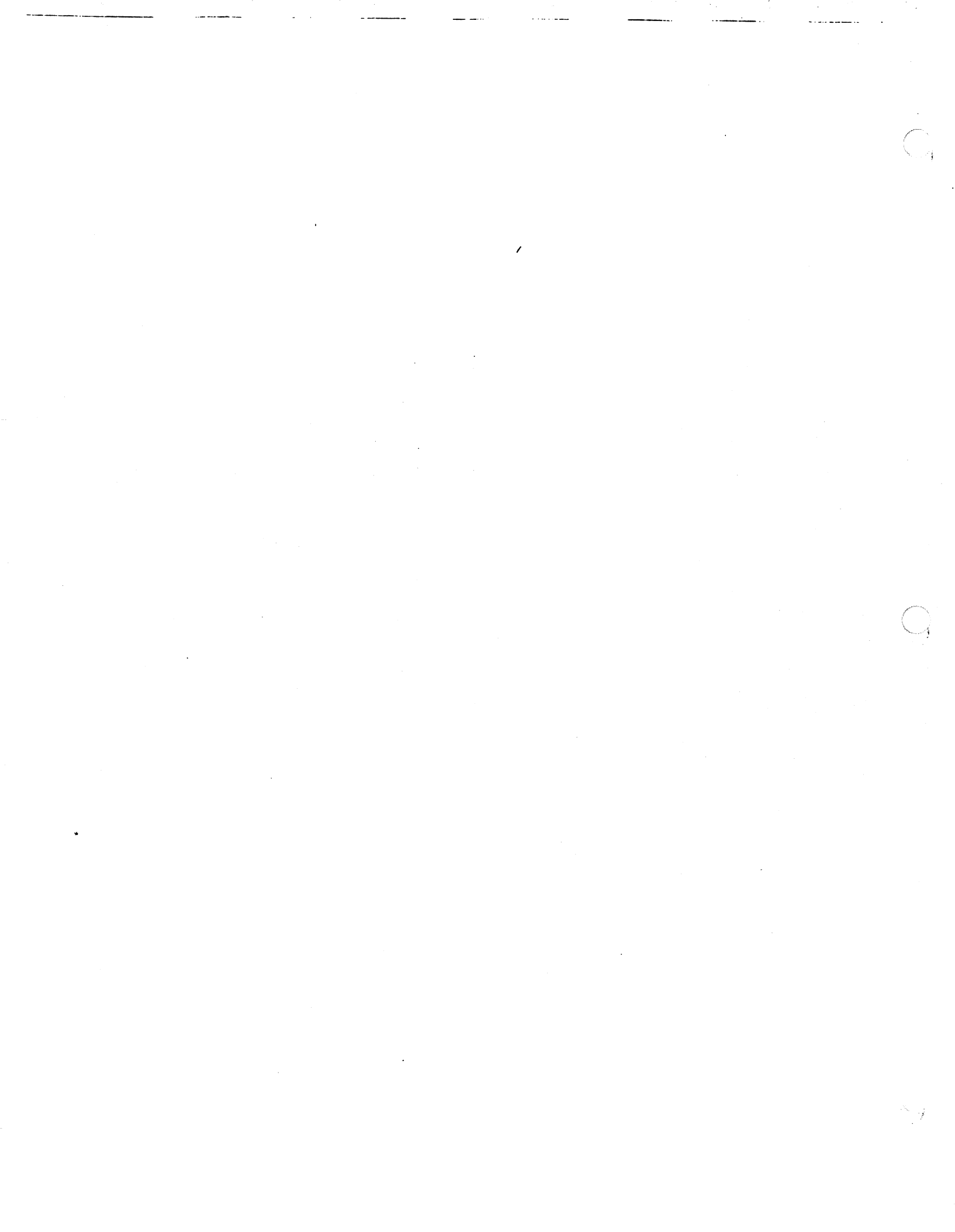

```

1 *****
2 ; TITLE - PUPST - PEGASUS POWERUP TEST CODE
3 ; ABSTRACT - THE TEST IS DESIGNED TO CHECK OUT THE SYSTEM ROM AND RAM AND TO
4 ; ALLOW THE FACTORY TO ACTIVATE ANOTHER EPROM FOR TEST PURPOSES. THE
5 ; TEST IS RUN WITH INTERRUPTS DISABLED AND NEEDS NO RAM FOR INITIAL
6 ; CHECKOUT.
7 ;
8 ; REGISTERS USED - ALL
9 ;
10 ; STACK USED - 68:8P POINTS TO A ROM RETURN TABLE TO AVOID USING RAM.
11 ;
12 ; *****
13 NAME PUPST
14 SUBTTL PEGASUS POWERUP DIAGNOSTIC TESTS
15 *****
16 ; PUBLIC DEFINITIONS
17 ;
18 ; *****
19 PUBLIC ROMTST ;ROM CRC TEST ROUTINE
20 PUBLIC PUPST ;POWER UP TEST ENTRY
21 *****
22 ; EXTERNAL REFERENCES
23 ; *****
24 SECT ROMCOD
25 EXTERN INICRT:NEAR ;CRT HARDWARE INITIALIZATION ROUTINE
26 EXTERN EPROM:BYTE ;ADDRESS OF BEGINNING OF EPROM
27 EXTERN SYSROM:BYTE ;ADDRESS OF BEGINNING OF SYSTEM ROM
28 EXTERN DSPDIE:NEAR ;ADDRESS OF FAILURE ROUTINE
29 EXTERN LED110:ABS ;ROM FAILED ERROR CODE
30 EXTERN LED101:ABS ;SYSTEM RAM FAILED ERROR CODE
31 EXTERN LED100:ABS ;PASSED SYSTEM RAM TEST , NEXT.
32 EXTERN RAMINI:NEAR ;ADDRESS OF RAM INIT ROUTINE.
33 EXTERN ROMSIZ:ABS ;SIZE OF THE SYSTEM ROM
34 EXTERN ROMF4:NEAR ;ADDRESS OF FACTORY ROM
35 *****
36 ; LOCAL CONSTANTS
37 ;
38 INCLUDE PEG:PORTADDR.EQU
39 INCLUDE PEG:LATCHES.EQU
40 INCLUDE PEG:FLOPPY.EQU
41 INCLUDE PEG:VECTOR.EQU
42 INCLUDE PEG:WINCHSTR.EQU
43 *****
237 RAMBEG EQU BRKINT*4 ;START OF VALID RAMTEST
238 NMIVEC EQU NMIINT*4 ;NMI INTERRUPT VECTOR LOCATION
240 RMTSSZ EQU (O-RAMBEG) SHR 1 ;SIZE OF RAM IN WORDS
241 ;
242 ; *****
243 ; MODULE ENTRY POINT
244 ; *****
245 SECT ROMCOD ;ALL CODE IS PLACE HERE
246 ASSUME CS:ROMCOD
  
```

=0000

=000C
 =000B
 =7FFA

=0000




```

0048' EB F7
299 JMP ERROR
300 ; *****
301 ; RAMTEST ***** THIS IS A SIMPLE TEST OF THE SYSTEM RAM. IT IS
302 ; USED TO CLEAR THE PARITY BIT IN ALL OF THE RAM, QUICKLY CHECK
303 ; THE RAM FOR ERRORS.
304 ; *****
305 RAMTST: MOV AL,(LED101+PRAUTO+PRSTRB+PRINT) ;ADVANCE LED INDICATION
306 ; PRCD_P,AL ; FOR NEXT TEST
307 XOR SP,SP ;CLEAR STACK POINTER
308 MOV SS,SP ;SET UP STACK SEGMENT
309 MOV DS,SP ;SET UP DATA SEGMENT
310 MOV EB,SP ;SET UP EXTRA SEGMENT
311 ;
312 ASSUME DS:AB80, EB:AB80, ES:AB80
313 MOV SP,WORD PTR MEMBIZ ;GET WARM RESET FLAG INTO SP.
314 MOV SI,OFFSET NMIVEC ;POINTER TO NMI VECTOR
315 MOV WORD PTR(CB1),OFFSETRROR ;STORE THE NMI ERROR VECTOR OFFSET
316 MOV 2(CB1),CB ;STORE THE SEGMENT(SAME AS THIS CODE)
317 CMP WORD PTR(CB1),OFFSETRROR ;0, NMI ERROR VECTOR OFFSET OK?
318 JNZ VECERR ;N, JUMP AND FLAG ERROR.
319 CMP WORD PTR 2(CB1),ROMCOD ;0, NMI ERROR VECTOR SEGMENT OK?
320 JZ R000 ;Y, CONTINUE THE TEST.
321 VECERR: MOV SP,55AAH ;FLAG VECTOR ERROR OCCURRED.
322 JMP SHORT R00
323 R000: MOV AL,(PARIEN+LED101+PRAUTO+PRSTRB+PRINT) ;TURN ON PARITY INTR.
324 OUT PRCD_P,AL
325 MOV AX,SP
326 SUB AH,10H ;1000H (= RAMBIZ (= 8000H IF WARM RESET
327 AND AX,BFFFH ;0: WARM RESET?
328 JZ REND ;Y, EXIT TEST ROUTINE
329 MOV BP,00000010B ;WALKING ONE
330 MOV CX,OFFSET RMTB9Z ;SET UP SIZE OF RAM TO TEST
331 MOV DI,RAMBEO ;SET UP START OF RAM.
332 MOV SI,DI ;COPY TO SI FOR LATER.
333 MOV AX,BP ;GET WALKING ONE TO AX.
334 ROR AX,1 ;SET UP THE CARRY BIT
335 STOS WORD PTR [DI] ;STORE WALKING ONE
336 RCL AX,1 ;WALK IT
337 LOOP R1
338 LOOP
339 ;
340 ; READ AND CHECK THE STORED PATTERN
341 ;
342 ;
343 MOV BX,BP ;GET INITIAL PATTERN
344 MOV CX,OFFSET RMTB9Z ;SIZE OF RAM IN WORDS
345 ROR BX,1 ;SET UP THE CARRY BIT
346 LAHF ;SAVE FLAGS
347 MOV DX,BX ;GET THE WORD THAT WAS WRITTEN.
348 XOR DX,CB1J ;0, IS THIS OK?
349 JNZ RERROR ;N, JUMP
350 INC SI ;BUMP POINTERS
    
```

```

00A9' 46      INC      SI
00AA' 9E      SAHF
00AB' D1 D3   RCL      BX,1
00AD' E2 F2   LOOP
00AF' F7 D5   NOT
00B1' 21 ED   AND      BP,BP
00B3' 78 D2   JS
00B5' 81 FC 55AA  CMP     SP,55AAH
00B9' 74 89   JZ      ERROR
360      MAIN RAM OK.
361
362
363      JMP      RAMINI
364
*****
365      LOCAL SUBROUTINES
366
367      ROM TEST
368      ROUTINE
369
370      ON ENTRY
371
372      BP= SIZE OF ROM
373      BX= OFFSET OF ROM
374      ES= SEGMENT OF ROM
375      AX,CX ARE USED
376      SI,DI,DS,ES ARE NOT CHANGED
377      DX=REMAINDER
378      ZF=SET IF DX=0
379
380      ROMTST PROC NEAR
381      XOR      DX,DX
382      ROMTST: MOV     AH,ES:[BX]
383
384      THIS IS THE KERNAL OF THE CRC16 ROUTINE (FOR A BYTE).
385
386      CX, B
387      MOV     AL,AH
388      XOR     AL,DL
389      SHR     DX,1
390      SHR     AX,1
391      JNB    T4
392      XOR     DX,0A001H
393      LOOP   T3
394
395      INC     BX
396      DEC     BP
397      JNZ    ROMTST1
398      XOR     DX,DX
399      AND     DX,DX
400      RET
401
402
00BE' 31 D2
00CE' 26BA 27

00C3' B9 000B
00C6' 8A C4
00C8' 30 D0
00CA' D1 EA
00CC' D1 E8
00CE' 73 04
00D0' 81 F2 A001
00D4' E2 F0

00D6' 43
00D7' 4D
00DB' 75 E6
00DA' 31 D2
00DC' 21 D2
00DE' C3
    
```

 MODULE DATA DEFINITIONS

```
403 ; *****  
404 ;  
405 TSTACK LABEL WORD ; A STACK WHICH IS USED DURING POWERUP TO RETURN  
406 OFFBET T1P5 OFFBET T1P6 ; RETURN ADDRESS FOR INICRT ROUTINE  
407 DW DW OFFBET T1P6 ; RETURN ADDRESS FOR ROM TEST ROUTINE  
408 ;  
409 END
```

No errors detected

5

6

7

```

54 EXTRN FL2EVT:WORD ; * Floppy disk general timing event (ROMDAT)
55 EXTRN PRCO_M:BYTE ; Printer control latch memory (ROMDAT)
56 EXTRN SYSCON:WORD ; System Configuration word (ROMDAT)
57 EXTRN TIKCTR:BYTE ; System timer tick counter (load loc) (ROMDAT)
58 ;
59 ; ***** LOCAL CONSTANTS *****
60 ; INCLUDE PEG:PORTADDR.EGU
61 ; INCLUDE PEG:VECTOR.EGU
62 ; INCLUDE PEG:LATCHES.EGU
63 ; *****
64 ; *****
65 ; *****
220 ; *****
221 ; *****
222 ; *****
223 ; *****
224 ; *****
225 ; *****
226 ; *****
227 ; *****
228 ; *****
229 ; *****
230 ; *****
231 ; *****
232 ; *****
233 ; *****
234 ; *****
235 ; *****
236 ; *****
237 ; *****
238 ; *****
239 ; *****
240 ; *****
241 ; *****
242 ; *****
243 ; *****
244 ; *****
245 ; *****
246 ; *****
247 ; *****
248 ; *****
249 ; *****
250 ; *****
251 ; *****
252 ; *****
253 ; *****
254 ; *****
255 ; *****
256 ; *****
257 ; *****
258 ; *****
259 ; *****

-0000

SECT ROMCOD
ASSUME CS:ROMCOD

***** LOCAL DATA AREA *****
***** THIS TABLE MUST MATCH THE CORRESPONDING DEFINITION *****
***** STRUCTURE IN THE MODULE 'ROMDAT' (STARTING AT TIKCTR) *****
*****

EVTINI LABEL BYTE
; System timer data area
TIKCTR LABEL BYTE
DB 00
; Timing services permanent queue entries
RONEVT LABEL WORD
DW OFFSET DSKEVT
; Start of timing events linked-list
DSKEVT LABEL DWORD
DW offset FL1EVT
DB OFFH
DB 00
DW 0000
DW offset TIMOUT
; Floppy disk motor-off event
FL1EVT LABEL DWORD

```



```

0039' B8 0000'
003C' BE C0
003E' BE 0000"
0041' BF 0012"
0044' B9 0023
0047' F2
0048' A4
0049' 2EA3 C1B0
004D' BE D8

004F' B0 A0
0051' A2 0008"
0054' E6 00
0056' B0 00"
0058' A2 0010"
005B' E6 03
005D' B0 FF

005F' E6 04
0061' 31 C0
0063' A3 0011"

0066' BE C0
0068' E9 0006"

312 AX,ROMDAT
313 ES,AX
314 SI,OFFSET EVTINI
315 DI,OFFSET TIKCTR
316 CX,OFFSET EVTLEN
317 REP
318 MOVSB
319 WORD PTR CS:DSADDR+CSWRAP,AX
320 DS,AX
321 ;
322 ;
323 ;
324 AL,(SIDE_0+ACCFDC)
325 DSKC_M,AL
326 DSKC_P,AL
327 AL,(LED100+PARIEN+PRAUTO+PRSTRB+PRINIT)
328 PRCD_M,AL
329 PRCD_P,AL
330 AL,OFFH
331 DSKB_M,AL
332 DSKB_P,AL
333 AX,AX
334 SYBCON,AX
335 ;
336 ;
337 ;
338 ;
339 AX,ABSO
340 ES,AX
341 INTTST
342 END

    Note that DS = CS = ROMCOD
    ES points to initial ROMDAT loc.
    DS:SI have source for move
    ES:DI have destination for move
    CX has size to move

    Do the move
    Set floating DS (ROMDAT)
    Set new DS (ROMDAT)

Initialize the ports to match their RAM counterparts

    Set up initial value
    Set the RAM version
    Set the latch
    Initial value
    (LEDs still OK)
    Initial value

    Clear the configuration word

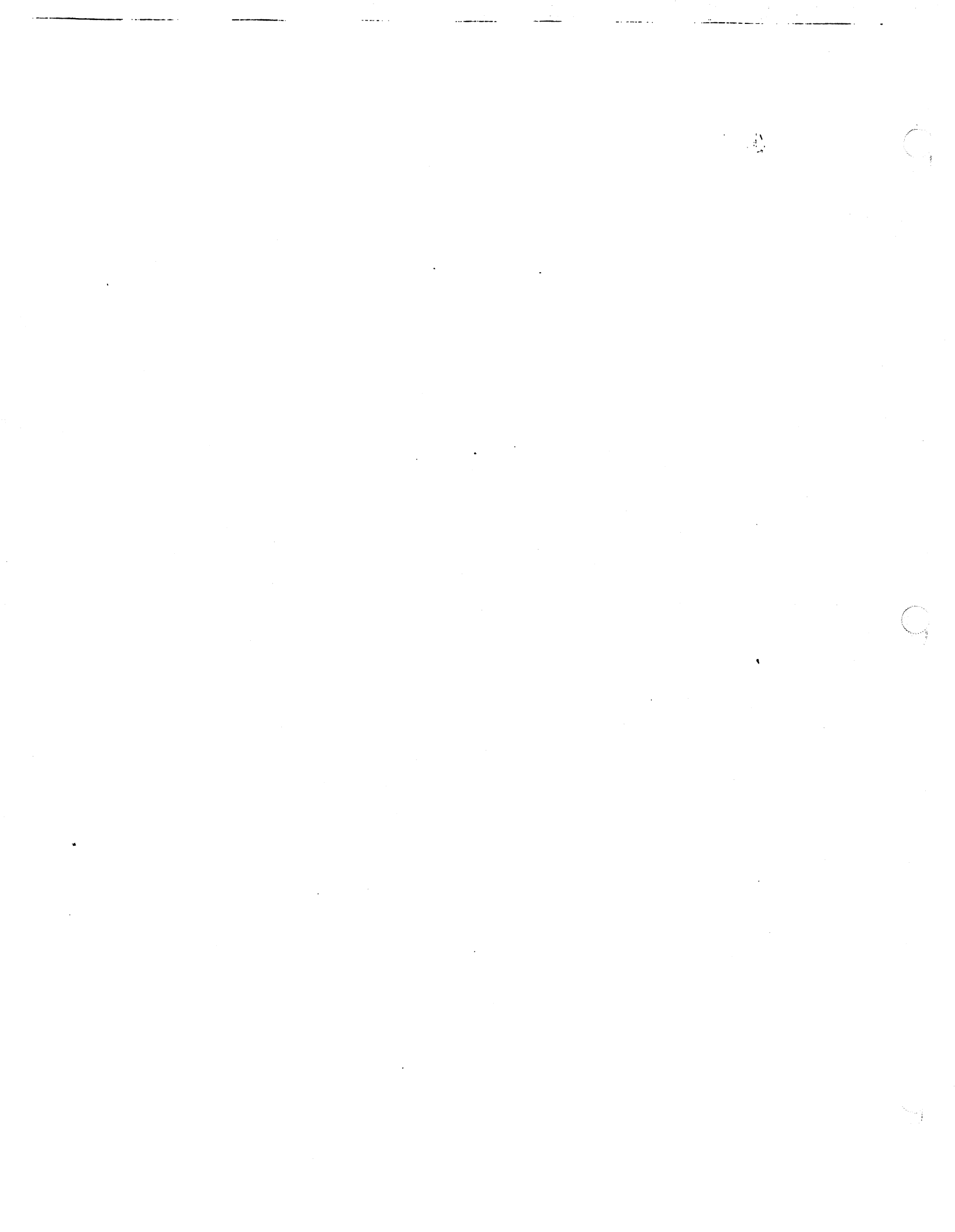
    INTTST assumes that
    ES points to ABSO
    (Interrupts still disabled)
  
```

No errors detected



```
1 ;*****  
2 ; TITLE - RESET - Powerup reset code  
3 ; COMPUTER - BOBB Assembly Language (BSD Assembler)  
4 ; ABSTRACT - The module contains the code that goes in the RESET segment  
5 ;*****  
6 NAME RESET - Powerup reset code  
7 SUBTTL  
9 ;*****  
10 PUBLIC DEFINITIONS  
11 ;*****  
12 ;  
13 PUBLIC PWRUP  
14 ;  
15 ;*****  
16 ; EXTERNAL REFERENCES  
17 ;*****  
18 ;  
19 EXTRN PUPTST:FAR ; Start of powerup test code (PUPTST)  
20 ;  
21 ;*****  
22 ; MODULE ENTRY POINT  
23 ;*****  
24 ;  
25 ; RESET is located at absolute memory location OFFF0H. This is where the  
26 ; processor starts after being reset (with CS = FFFFH, other seg-regs = 0)  
27 ;  
28 SECT RESET  
29 ASSUME CS:RESET  
30 ;  
31 ORG 0000H  
32 ;  
33 PWRUP PROC FAR  
34 CLI ; Disable interrupts (for keyboard restart)  
35 CLD ; Start out with forward strings  
36 JMP PUPTST ; Intersegment jump to powerup test code  
37 ;  
38 DB 'Pegasus'  
39 ORG 000EH  
40 DW 9CEAH  
41 ;  
42 ; CRC of the system ROM (this is determined  
43 ; empirically with the procedure described  
44 ; in the file README.DOC)  
45 END
```

No errors detected




```

1 *****
2 ;
3 ; TITLE - ROMDAT - Pegasus system ROM data areas
4 ; COMPUTER - BOBB Assembly Language (BSD Assembler)
5 ; ABSTRACT - This module contains the RAM data areas for the various
6 ; ROM-based system routines. Note that the actual location of these
7 ; variables in memory is variable, depending on the current value
8 ; for the ROM DS pointer, 'DSADDR'. This module is just a skeleton
9 ; used to define the offsets of the variables. The initialization
10 ; is handled by the various routines that actually use the RAM.
11 ; One exception to this is the routine 'RAMINI', which sets up some
12 ; of the constant data (TIKCTR and Timing Event blocks).
13 ;*****
14 NAME ROMDAT - TIPC system ROM data areas
15 SUBTTL
17 DEBUG EQU OFFFFH
18 *****
19 ; PUBLIC DEFINITIONS
20 ;*****
21 ;
22 PUBLIC ATTBV
23 PUBLIC BELEV
24 PUBLIC CRTCNT
25 PUBLIC CRTERR
26 PUBLIC CRTHLD
27 PUBLIC CURPOS
28 PUBLIC CURTYP
29 PUBLIC D_DBGN
30 PUBLIC D_DEND
31 PUBLIC D_DIT
32 PUBLIC D_EKNT
33 PUBLIC D_NCND
34 PUBLIC D_NSTA
35 PUBLIC D_POS
36 PUBLIC D_REQ
37 PUBLIC D_SOFT
38 PUBLIC D_STAT
39 PUBLIC D_WAIT
40 PUBLIC D_WKSP
41 PUBLIC DATE
42 PUBLIC DISBEG
43 PUBLIC DISEND
44 PUBLIC DKSPSV
45 PUBLIC DKSSSV
46 PUBLIC DSKC_M
47 PUBLIC DSKVKT
48 PUBLIC DSKISP
49 PUBLIC DSKS_M
50 PUBLIC ENDDG
51 PUBLIC FL1EVT
52 PUBLIC FL2EVT
53 PUBLIC HOURS
54
55 ;* Beginning of disk driver data area
56 ;* End of disk driver data area
57 ;* Disk table of Disk Interface Tables
58 ;* Disk error counter
59 ;* Disk next operation to perform
60 ;* Disk next state for driver
61 ;* Disk table of current positions
62 ;* Disk Request Information Block
63 ;* Disk software interrupt flag
64 ;* Disk driver current state
65 ;* Disk waiting for I/O completion flag
66 ;* Disk driver workspace
67
68 ;* Disk driver timeout event
69
70 ;* Floppy disk motor-off event
71 ;* Floppy general timing event

```



```

54 PUBLIC HUNS
55 PUBLIC IXSPSV
56 PUBLIC IXSSV
57 PUBLIC KBMODE
58 PUBLIC KBSSV
59 PUBLIC KBSPSV
60 PUBLIC KEYISP
61 PUBLIC MINS
62 PUBLIC NUMCNT
63 PUBLIC NUMDRV
64 PUBLIC NUMVAL
65 PUBLIC PRCD_M
66 PUBLIC GDEPTH
67 PUBLIC GFRONT
68 PUBLIC GREAR
69 PUBLIC QUEUE
70 PUBLIC RETFLO
71 PUBLIC RMDTBO
72 PUBLIC RMDTSZ
73 PUBLIC ROMEVT
74 PUBLIC SECS
75 PUBLIC STATLN
76 PUBLIC SYBSCN
77 PUBLIC THSLIN
78 PUBLIC TIMCTR
79 PUBLIC TIMISP
80 PUBLIC TMSPSV
81 PUBLIC TMSBSV
  
```

;* Number of disk drive units

```

EXTERNAL REFERENCES
BEEPFD:NEAR ; System beeper turnoff subroutine (OUTPUT)
FLTIME:NEAR ; * Floppy disk timing event routine (FLPXXX)
LEDIO:ABS ; Interrupt/timer failure LED pattern (ROMERR)
MOTOFF:NEAR ; * Floppy disk motor turnoff routine (FLPXXX)
SSSTD:NEAR ; * Single-side, single track dens DIT (FLPDIT)
TIMOUT:NEAR ; * Disk controller timeout subroutine (DSKUTL)
  
```

DISK DRIVER STATE DEFINITIONS
 INCLUDE PEG: DSKSTA.EQU

REQUEST INFORMATION BLOCK STRUCTURE DEFINITION
 INCLUDE PEG: DSKRIB

LOCAL CONSTANTS

```

NUMDRV EQU 8 ; * Max number of disk drives
GSIZE EQU 16 ; Keyboard type-ahead buffer size
  
```

```

132 *****
133 BEGINNING OF DATA AREA
134 *****
135 *****
136 NOTE))) The memory locations from DSKC_M through DIBEO must
137 remain in their relative locations in later versions of the ROM.
138
139 SECT ROMDAT
140
141 RMDTBO LABEL BYTE
142
143 Hardware 'memory' -- NOTE: These three bytes must be the first in ROMDAT
144
145 DSKC_M DB 1 ; BYTE - 'DSKC_P' latch
146 PRCD_M DB 1 ; BYTE - 'PRCO_P' latch
147 DSKB_M DB 1 ; BYTE - 'DSKB_P' latch
148
149
150 System configuration word - See 'PEO:SYBCON.EGU' for bit definitions
151
152 SYBCON DB 2 ; WORD - System config - initialized during powerup
153
154
155 Timing services permanent queue entries - This area is initialized from
156 a table in the module 'RAMINI'. Any changes made here should also
157 be made in RAMINI.
158
159 *****
160 *** The following data items (up through DIBEO) ***
161 *** must remain as contiguous memory locations. ***
162 *****
163
164 TIKCTR DB 1 ; BYTE - System timer (25 msec) tick counter
165
166 ROMEVT LABEL WORD ; Start of timing events linked-list
167 DB 2 ; WORD - Pointer to first event block
168
169 DBKEVT LABEL WORD ; Disk driver timeout event
170 DB 2 ; WORD - Linked list pointer
171 DB 1 ; BYTE - Task ID (=OFFH for permanent events)
172 DB 1 ; BYTE - Flags
173 DB 2 ; WORD - Counter
174 DB 2 ; WORD - Subroutine address (disk TIMEOUT)
175
176 FL1EVT LABEL WORD ; Floppy disk motor-off event
177 DB 2 ; WORD - Linked list pointer
178 DB 1 ; BYTE - Task ID (=OFFH for permanent events)
179 DB 1 ; BYTE - Flags
180 DB 2 ; WORD - Counter
181 DB 2 ; WORD - Subroutine address (floppy MOTOR OFF)
182
183 FL2EVT LABEL WORD ; Floppy disk general timing event

```

184	-0002	DS	2	WORD	Linked list pointer (last entry)
185	-0001	DS	1	BYTE	Task ID (=OFFH for permanent events)
186	-0001	DS	1	BYTE	Flags
187	-0002	DS	2	WORD	Counter
188	-0002	DS	2	WORD	Subroutine address (Floppy TIMEing)
189					
190	0020'	LABEL	WORD		
191	-0002	DS	2	WORD	Linked list pointer
192	-0001	DS	1	BYTE	Task ID (=OFFH for permanent events)
193	-0001	DS	1	BYTE	Flags
194	-0002	DS	2	WORD	Counter
195	-0002	DS	2	WORD	Subroutine address (system BEEPer OFF)
196					
197					
198					
199					
200	0028'	DS	2	WORD	Begin address of display.
201	-0002	DS	2	WORD	End address of display.
202	002A'	DS	2	WORD	Address of current cursor position.
203	002E'	DS	2	WORD	Address of current line of cursor.
204	0030'	DS	2	WORD	Number of chars in current line.
205	0032'	DS	2	WORD	Cursor type (block & 1/4 sec. blink @ purup).
206	0034'	DS	2	WORD	TTY status line begin = no stat line @ purup.
207	0036'	DS	1	BYTE	Cond. ret. flag for internal use only !!!
208	0037'	DS	1	BYTE	Current CRT errors (used by CRTTST).
209	0038'	DS	1	BYTE	Status byte for pausing CRT I/O operations.
210	0039'	DS	1	BYTE	Attribute latch save.
211					
212					
213					
214					
215	003A'	DS	2	WORD	Pointer to current 'front' of keyboard queue
216	003C'	DS	2	WORD	Pointer to current 'rear' of keyboard queue
217	003E'	DS	1	BYTE	Current number of chars waiting in queue
218	003F'	DS	1	BYTE	Keymode mode/status byte
219	0040'	DS	1	BYTE	ALT/NUM accumulator
220	0041'	DS	1	BYTE	ALT/NUM keystroke counter
221					
222	0042'	LABEL	WORD		The keyboard queue itself
223	-0020	DS	DSIZE*2		(*2 because queue entries are words)
224	0062'	LABEL	WORD		
225					
226					
227					
228					
229	0062'	DS	4*NUMDRV		Table of disk unit DIT's
230	0082'	DS	1	BYTE	Error counter
231	0083'	DS	1	BYTE	Next disk operation to perform
232	0084'	DS	1	BYTE	Next state for driver
233	0085'	DS	NUMDRV		Table of current disk positions
234	008D'	DS	RIBL		Request Information Block
235	009E'	DS	1	BYTE	Non-zero indicates software interrupt

ofset 28

```

236 009F' -0001      D_BSTAT DB      1      ; * was used to enter DBKISR
237 00A0' -0001      D_WAIT  DB      1      ; * BYTE - Current state of driver
238 00A1' -0004      D_WKSP   DB      4      ; * BYTE - Waiting for disk I/O completion flag
239                                     ; * BYTE#4 - Disk driver workspace
240
241 -0062      D_DBN   EQU      D_DIT      ; * Beginning of disk driver data area
242 -00A5      D_DEND   EQU      D_WKSP+4  ; * End of disk driver data area
243
244
245 -0001      DB      1      ; This forces ROMDAT size to be multiple of 16
246                                     ; and stacks to be on an even word boundary
247
248 ; Interrupt handler stack save areas:
249
250 00A6' -0002      KBSSV   DB      2      ; Keyboard interrupt stack segment save
251 00AB' -0002      KBSPSV DB      2      ; Keyboard interrupt stack pointer save
252
253 00AA' -0002      DKSSV   DB      2      ; Disk interrupt stack segment save
254 00AC' -0002      DKSPSV DB      2      ; Disk interrupt stack pointer save
255
256 00AE' -0002      TMSVV   DB      2      ; Timer interrupt stack segment save
257 00B0' -0002      TMSPSV DB      2      ; Timer interrupt stack pointer save
258
259 00B2' -0002      IXSSV   DB      2      ; Common interrupt exit stack segment save
260 00B4' -0002      IXPSV   DB      2      ; Common interrupt exit stack pointer save
261
262 -0030      DB      48      ; Keyboard interrupt service routine's stack
263
264
265 -0030      KEYISP LABEL WORD      48      ; * Disk interrupt service routine's stack
266
267
268 -0024      DB      36      ; System time interrupt routine's stack
269
270
271
272 ; Time-of-day clock DBR data area
273
274 ; *** HUNB thru DATE must be contiguous ***
275
276 HUNB DB      1      ; Hundredths of seconds (tenth sec resolution)
277 SECB DB      1      ; Seconds
278 MINS DB      1      ; Minutes
279 HOURS DB      1      ; Hours
280 DATE DB      2      ; Days since 1-1-80
281
282 RMDTEN LABEL BYTE      ; Last location of ROMDAT
283 RMDT8Z EQU      RMDTEN-RMDT80 ; The size of ROMDAT
284
285 -0140
286 -0000      IF (RMDT8Z MOD 10H) NE 0000 ; Blow up if ROMDAT size is not a multiple of 10H
287 BARFO      ENDIF

```

ROMDAT - TIPC system ROM data areas
ROMDAT.BRC

CR8086/11 version 10.34.17

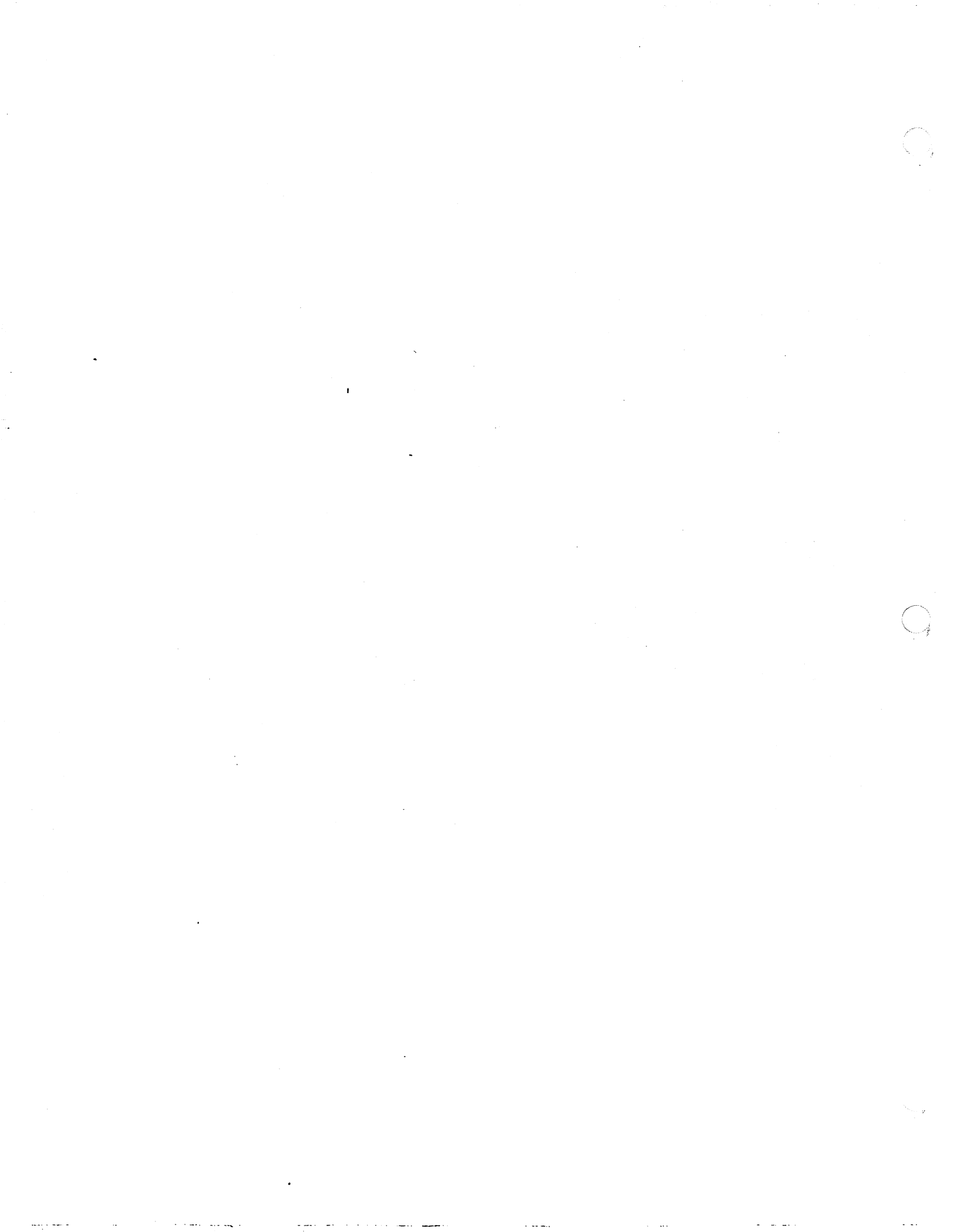
3-Aug-83 16:46:48

Page 1-5

288
289

END

No errors detected




```
1 ; *****  
2 ; TITLE - ROMERR (ROM SYSTEM ERROR CODES)  
3 ; ABSTRACT - This module contains the definitions of ROM based system  
4 ; error codes.  
5 ; *****  
6 NAME ROMERR (ROM BASED SYSTEM ERROR CODES)  
7 SUBTTL  
9 ; *****  
10 ; PUBLIC DEFINITIONS  
11 ; *****  
12 PUBLIC DBCERR ; Boot - Bad CRC on boot sector  
13 PUBLIC DCRERR ; Boot - CRC error  
14 PUBLIC DFMERR ; Boot - Disk format error  
15 PUBLIC DNIERR ; Boot - No drives installed  
16 PUBLIC DNRERR ; Boot - Disk not ready  
17 PUBLIC DNSERR ; Boot - Not a TI system disk  
18 PUBLIC DSKERR ; Boot - Seek error  
19 PUBLIC DSNERR ; Boot - Sector-not-found error  
20 PUBLIC DUNERR ; Boot - Data error (should not occur)  
21 PUBLIC DDMERR ; Boot - DRQ error (hardware failure)  
22 PUBLIC FATERR ; Fatal software error  
23 PUBLIC KNIERR ; Keyboard not installed  
24 PUBLIC KNRERR ; Keyboard - no response  
25 PUBLIC KRAERR ; Keyboard RAM failure  
26 PUBLIC KRCERR ; Keyboard acknowledge char receipt error  
27 PUBLIC KROERR ; Keyboard ROM failure  
28 PUBLIC KUNERR ; Keyboard - Unknown error  
29 PUBLIC LED111 ; All LEDs on, complete system fail.  
30 PUBLIC LED110 ; System ROM failure  
31 PUBLIC LED101 ; System RAM failure (P port CONTAINS BITS BAD)  
32 PUBLIC LED100 ; Interrupt or timer failure  
33 PUBLIC LED011 ; Floppy controller failure  
34 PUBLIC LED010 ; CRT hardware failure  
35 PUBLIC LED001 ; Option failure (used by PUPNMI)  
36 PUBLIC LED000 ; Basic system test passed  
37 PUBLIC OPRERR ; Optional RAM has an error during powerup test  
38 PUBLIC PURMPE ; RAM parity error during powerup  
39 PUBLIC PUXNME ; Unexpected NMI during powerup  
40 PUBLIC RMPERR ; RAM parity error  
41 PUBLIC ROMERX ; Option ROM at 0F4000H is in error  
42 PUBLIC XNMERR ; Unexpected NMI  
43 PUBLIC WLDERR ; 'Wild' interrupt  
44  
45 ; *****  
46 ; POWERUP AND DIE ERROR CODES - '00xx'  
47 ; 'xx' in the range 00H --> 09H  
48 ;  
49 ; Note that LED bits are inverted, since LEDs are low = 'on'  
50 ; *****  
51 LED111 EQU 0000000B ; "7" All LEDs on, complete system fail.  
52 LED110 EQU 0000001B ; "6" System ROM failure  
53 LED101 EQU 0000010B ; "5" System RAM failure
```

=0000
=0001
=0002



```

-0003 54 LED100 EQU 00000011B ; "4" Interrupt or timers failed
-0004 55 LED011 EQU 00000100B ; "3" Floppy controller failed
-0005 56 LED010 EQU 00000101B ; "2" CRT hardware failed
-0006 57 LED001 EQU 00000110B ; "1" Keyboard failed (not used yet)
-0007 58 LED000 EQU 00000111B ; "0" Basic system test passed
;
-0008 60 PUXNME EQU 8 ; Unexpected NMI during powerup sequence
-0009 61 PURMPE EQU 9 ; RAM parity error during powerup sequence
;
; *****
; ***** KEYBOARD ERRORS - '00xx' *****
; ***** 'xx' in the range 10H --> 19H *****
; *****
64 ; *****
65 ; *****
66 ; *****
67 ; *****
68 ; *****
69 ; *****
70 ; *****
71 ; *****
72 ; *****
73 ; *****
74 ; *****
75 ; *****
76 ; *****
77 ; *****
78 ; *****
79 ; *****
80 ; *****
81 ; *****
82 ; *****
83 ; *****
84 ; *****
85 ; *****
86 ; *****
87 ; *****
88 ; *****
89 ; *****
90 ; *****
91 ; *****
92 ; *****
93 ; *****
94 ; *****
95 ; *****
96 ; *****
97 ; *****
98 ; *****
99 ; *****
100 ; *****
101 ; *****
102 ; *****
103 ; *****
104 ; *****
105 ; *****

-0010 69 KNIERR EQU 10H ; Keyboard not installed
-0011 70 KNRRERR EQU 11H ; Keyboard - installed but no response
-0012 71 KRAERR EQU 12H ; Keyboard RAM failure
-0013 72 KROERR EQU 13H ; Keyboard ROM failure
-0014 73 KUNERR EQU 14H ; Keyboard Unknown failure (unexpected response)
-0015 74 KRCERR EQU 15H ; Keyboard acknowledge char receipt error
;
; *****
; ***** OPTIONAL MEMORY ERRORS - '00xx' *****
; ***** 'xx' in the range 20H --> 2FH *****
; *****
81 ; *****
82 ; *****
83 ; *****
84 ; *****
85 ; *****
86 ; *****
87 ; *****
88 ; *****
89 ; *****
90 ; *****
91 ; *****
92 ; *****
93 ; *****
94 ; *****
95 ; *****
96 ; *****
97 ; *****
98 ; *****
99 ; *****
100 ; *****
101 ; *****
102 ; *****
103 ; *****
104 ; *****
105 ; *****

-0020 81 OPRERR EQU 20H ; Option RAM bank 1 has an error in puptst.
82 EQU 21H ; Option RAM bank 2 has an error in puptst.
83 EQU 22H ; Option RAM bank 3 has an error in puptst.
84 EQU 23H ; Option RAM bank 4 has an error in puptst.
85 EQU 24H ; Option RAM bank 5 has an error in puptst.
86 EQU 25H ; Option RAM bank 6 has an error in puptst.
87 EQU 26H ; Option RAM bank 7 has an error in puptst.
88 ; *****
89 ; *****
90 ; *****
91 ; *****
92 ; *****
93 ; *****
94 ; *****
95 ; *****
96 ; *****
97 ; *****
98 ; *****
99 ; *****
100 ; *****
101 ; *****
102 ; *****
103 ; *****
104 ; *****
105 ; *****

-0028 88 ROMERX EQU 28H ; Option ROM at OF6000H is in error
89 EQU 29H ; Option ROM at OF4000H is in error
90 EQU 2AH ; Option ROM at OFB000H is in error
91 EQU 2BH ; Option ROM at OFA000H is in error
92 EQU 2CH ; Option ROM at OFC000H is in error
93 EQU 2DH ; MAIN ROM at OFE000H is in error
94 ; *****
95 ; *****
96 ; *****
97 ; *****
98 ; *****
99 ; *****
100 ; *****
101 ; *****
102 ; *****
103 ; *****
104 ; *****
105 ; *****

-0030 99 DNIERR EQU 30H ; No drives installed (must have at least one)
-0031 100 DNRERR EQU 31H ; Disks not ready or not bootable
-0032 101 DCRERR EQU 32H ; CRC error
-0033 102 DSKERR EQU 33H ; Seek error
-0034 103 DSNERR EQU 34H ; Sector-not-found error (controller failure)
-0035 104 DUNERR EQU 35H ; 'Disk' (unknown) error (controller failure)
-0036 105 DNSERR EQU 36H ; Not a TI system disk

```

```

-0037 106 DFERR EQU 37H ; Disk format error
-0038 107 DBCERR EQU 38H ; Bad boot sector CRC or bad sector buffer I/F
-0039 108 DDMERR EQU 39H ; DRQ error (controller failure)
109 ,
110 ,
111 , *****
112 , INTERRUPT ERROR CODES - '10xx'
113 , 'xx' in the range 40H --) 4FH
114 , *****
115 XNMERR EQU 40H ; Unexpected non-maskable interrupt (NMI)
116 RMPERR EQU 41H ; RAM parity error detected during operation.
117 WLDERR EQU 42H ; Unexpected (Wild) interrupt
118 ,
119 , *****
120 , SOFTWARE PROBLEM ERROR CODES - '10xx'
121 , *****
122 , 'xx' in the range 50H --) 5FH
123 , *****
124 FATERR EQU 50H ; Fatal software bug encountered
125 ,
126 , *****
127 , RESERVED ERROR CODES
128 , *****
129 , *****
130 , *****
131 UNUSED EQU OFFH ; Do not use OFFH as an error code
132 ,
133 SUBTTL
134 END

```

No errors detected

```

1 *****
2 ; TITLE - ROMTIM (ROM TIMING SERVICES)
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE
4 ; ABSTRACT - The ROMTIM subroutine is called by the system timer interrupt
5 ; processor every 400ms (every 4 clock ticks). This routine scans
6 ; the list of fixed timing events to determine if any events have
7 ; expired. When an event does expire, the subroutine defined in
8 ; the event structure is called.
9 ;
10 ; INPUT - DS = ROM DATA SEGMENT
11 ;
12 ; OUTPUT - NONE
13 ;
14 ; REGISTERS USED - AX,DI,DS,FLAGS
15 ;
16 ; STACK USED - 6 BYTES
17 ;
18 *****
19 NAME ROMTIM (ROM TIMING SERVICES SUBROUTINE)
20 SUBTTL
21 *****
22 PUBLIC DEFINITIONS
23 ;
24 *****
25
26 PUBLIC ROMTIM
27
28 *****
29 EXTERNAL REFERENCES
30 ;
31
32
33
34 SECT ROMDAT
35 EXTERN ROMEVT:WORD , POINTER TO 18T EVENT IN LIST (ROMDAT)
36 LOCAL CONSTANTS
37 ;
38
39 INCLUDE PEG:TIMEVT.EGU
40 *****
41 LOCAL MACROS
42 *****
43
44 INCLUDE PEG:BIT.MAC
45
46 BIT
47 MACRO %BITNUM,%TARGET
48 TEST BYTE PTR %TARGET,1 SHL %BITNUM , TEST BIT, GET Z-FLAG
49 ENDM
50
51 INCLUDE PEG:RES.MAC
52
53 RES
54 MACRO %BITNUM,%TARGET
55 PUSHF
56 ;
57 SAVE FLAGS
58
59
60
61
62
63
64
65
66
67
68
69
70
71
72
73
74
75
76
77
78
79
80
81
82
83
84
85
86
87
88
89
90
91
92
93
94
95
96
97
98
99
100

```

-0000

```
119 AND
120 POPF
121 ENDM
122 BYTE PTR XTARGET, NOT(1 BHL %BITNUM) , REBET THE BIT
, RESTORE FLA08
```

```

124 SUBTTL MAIN
125 ;*****
126 ; MODULE ENTRY POINT
127 ;*****
128
129
130
131
132
133
134
135
136
137
138
139
140+
141
142
143
144
145
146+
147
148
149
150
151
152
153
154
155
156
157
158
159
160
161

-0000

0000' BF 0001*
0003'
0003' 8B 3D
0005' 09 FF
0007' 74 1A

000D' 74 F4
000F' FF 4D 04

0012' 75 EF

001A' 1E
001B' 97
001C' FF 55 06
001F' 5F
0020' 1F
0021' EB E0

0023'
0023' C3

EXIT:
SUBTTL
END
    
```

```

*****
ROMTIM PROC
MOV DI, OFFSET ROMEVT
MOV DI, [DI]
OR DI, DI
JZ EXIT
BIT EVTSTA, IEVFLG[DI]
JZ NXTEVT
DEC WORD PTR IEVCTR[DI]
JNZ NXTEVT
RES PUSH
PUSH DI
CALL WORD PTR IEVSUB[DI]
POP DI
POP DS
JMP NXTEVT
EXIT:
RET
SUBTTL
END
    
```

```

*****
ROMTIM PROC
MOV DI, OFFSET ROMEVT
MOV DI, [DI]
OR DI, DI
JZ EXIT
BIT EVTSTA, IEVFLG[DI]
JZ NXTEVT
DEC WORD PTR IEVCTR[DI]
JNZ NXTEVT
RES PUSH
PUSH DI
CALL WORD PTR IEVSUB[DI]
POP DI
POP DS
JMP NXTEVT
EXIT:
RET
SUBTTL
END
    
```

No errors detected


```
1 *****  
2 ; TITLE - ROMUTL - System ROM utility routines  
3 ; COMPUTER - BOBB Assembly Language  
4 ; ABSTRACT - This module contains several commonly used subroutines  
5 ; *****  
6 ; NAME ROMUTL - System ROM utility routines  
7 ; SUBTTL  
9 ; *****  
10 ; PUBLIC DEFINITIONS  
11 ; *****  
12 ;  
13 PUBLIC DECLED  
14 PUBLIC DELAY  
15 ;  
16 ; *****  
17 ; EXTERNAL REFERENCES  
18 ; *****  
19 ;  
20 SECT ROMDAT  
21 EXTRN PROCM_BYTE ; Parallel control port memory (ROMDAT)  
22 ; *****  
23 ; LOCAL CONSTANTS  
24 ; *****  
25 ; INCLUDE PEG:PORTADDR.EQU  
26 ; INCLUDE PEG:VECTOR.EQU  
27 ; *****  
28 ; *****  
134 ; *****  
135 ; CODE SEGMENT DEFINITION  
136 ; *****  
137 ;  
138 SECT ROMCOD  
139 ASSUME CS:ROMCOD  
140 ; *****  
141 ; *****  
142 ; MODULE ENTRY POINT  
143 ; *****  
144 ;  
145 ; SETPR - Initialize data ports according to table (in the current CB)  
146 ; The format of the table is:  
147 ;  
148 ; DB (port),(data)  
149 ; DB OFFH  
150 ;  
151 ; Where port is an 8-bit port address. End of table denoted by a  
152 ; port value of OFFH. It is recommended this routine be executed  
153 ; with interrupts disabled. This method saves 1 byte over  
154 ; conventional "MOV AL,XX; OUT XX,AL" initialization if the  
155 ; number of ports to be initialized is 4 (9-byte table). There  
156 ; are corresponding savings for larger tables  
157 ;  
158 ; INPUT: CB:SI = Address of table
```



```

159 ; OUTPUT: (none) ports are initialized
160 ; USED:
161 ; STACK: 2
162 ; ASSUME CB:ROMCOD
163 ;
164 ; SETPRT PROC NEAR
165 ; MOV DH,00
166 ;
167 ; SPI:
168 ; BYTE PTR CS:[SI] ; Get the port
169 ; CMP AL,OFFH ; G: End of the list ?
170 ; JZ SP2 ; Y: That's all, then
171 ; MOV DL,AL ; N: Set up DX to point to port
172 ; LODS BYTE PTR CS:[SI] ; Get the data
173 ; OUT DX,AL ; Spit it out
174 ; LOOP SP1 ; Do 'em all
175 ;
176 ; RET ; *** RETURN ***
177 ; *****
178 ; DELAY - delay for (CX) milliseconds
179 ;
180 ; INPUT: CX = length of delay in milliseconds
181 ; OUTPUT: (none)
182 ; USED: CX
183 ; STACK: 4
184 ; ASSUME CB:ROMCOD
185 ;
186 ;
187 ; DELAY PROC NEAR
188 ; PUSH AX
189 ;
190 ; MOV AL,0A0H ; Empirically derived using MDS timer
191 ;
192 ; DEC AL ;
193 ; JNZ DEL2 ; if not 1 msec
194 ; LOOP DEL1 ; if not desired count
195 ; POP AX
196 ; RET ; *** RETURN ***
197 ;
198 ; *****
199 ;
200 ;
201 ; DECLED - This subroutine reads the state of the LED's from RAM,
202 ; decrements it, writes it back to RAM and outputs the
203 ; new state to the LED port.
204 ;
205 ; INPUT - (none)
206 ;
207 ; OUTPUT - AL = New state of LED'S (i.e. PRCD_M-1)
208 ; LED'S decremented and LED ram byte updated
209 ;
210 ; USED - AL

```

0000' 50
0001' B0 A0
0003' FE CB
0005' 75 FC
0007' E2 F8
0009' 98
000A' C3

```

211 ;
212 ; STACK - NONE
213 ;
214 ; *****
215 ;
216 ; ASSUME CB:ROMCOD
217 ;
218 ; DECLD      PROC      NEAR
219 ; MOV        DS,WORD PTR CB:DSADDR+CSMRAP      , Set up DS as ROMDAT
220 ; PUSHF
221 ; CLI
222 ; MOV        AL,PRCD_M
223 ; MOV        AH,AL
224 ; AND        AL,07H
225 ; CMP        AL,07H
226 ; JE         NODEC
227 ; MOV        AL,AH
228 ; CALL      SWPLED
229 ; INC        AL
230 ; CALL      SWPLED
231 ; OUT        PRCD_P,AL
232 ; MOV        PRCD_M,AL
233 ;
234 ; NODEC:
235 ; POPF
236 ; RET
237 ;
238 ; *****
239 ; THIS ROUTINE IS USED TO SWAP BITB 0 & 2 IN SOFTWARE
240 ;
241 ; INPUT - AL = STATE OF THE LED'S
242 ;
243 ; OUTPUT - AL = CORRECT STATE OF THE LED'S
244 ;
245 ; *****
246 ;
247 ; SWPLED PROC      NEAR
248 ; MOV        BL,AL
249 ; XOR        CL,CL
250 ; AND        BL,7H
251 ; TEST       BL,2H
252 ; JNZ        KEEP1
253 ; TEST0:     TEST       BL,1H
254 ; JNZ        SWAPO
255 ; TEST2:     TEST       BL,4H
256 ; JNZ        SWAP2
257 ; UPBITS:   AND         AL,0FBH
258 ; OR         AL,CL
259 ; RET
260 ;
261 ; KEEP1:     OR         CL,00000010B
262 ; JMP

```

```

; Save callers flags (interrupt state)
; Interrupts off
; Get copy of LED's from RAM
; Save LED state in AH
; Mask off last three bits
; Q: Are they all "zero" ?
; Y: Then return (no decrement)
; N: Get copy of LED's back
; ...and decrement LED's (inverted)
; Go SWAP bits 0 & 2
; Output new LED's
; Save new LED state in RAM

; Restore callers flags
; All done !!!

; *****
; THIS ROUTINE IS USED TO SWAP BITB 0 & 2 IN SOFTWARE
;
; INPUT - AL = STATE OF THE LED'S
;
; OUTPUT - AL = CORRECT STATE OF THE LED'S
;
; *****
;
; SAVE LED'S
; CLEAR CL
; MASK OUT LAST THREE BITS
; Q: IS SECOND BIT SET ?
; Y: THEN KEEP IT A ONE.
; Q: IS FIRST BIT SET ?
; Y: THEN SWAP BIT 0 WITH BIT 2
; Q: IS THIRD BIT SET ?
; Y: THEN SWAP BIT 2 WITH BIT 0
; OR IN NEW CORRECT LED'S
; ALL DONE
; KEEP SECOND BIT(1) A ONE
; GO TEST THE OTHER BITS

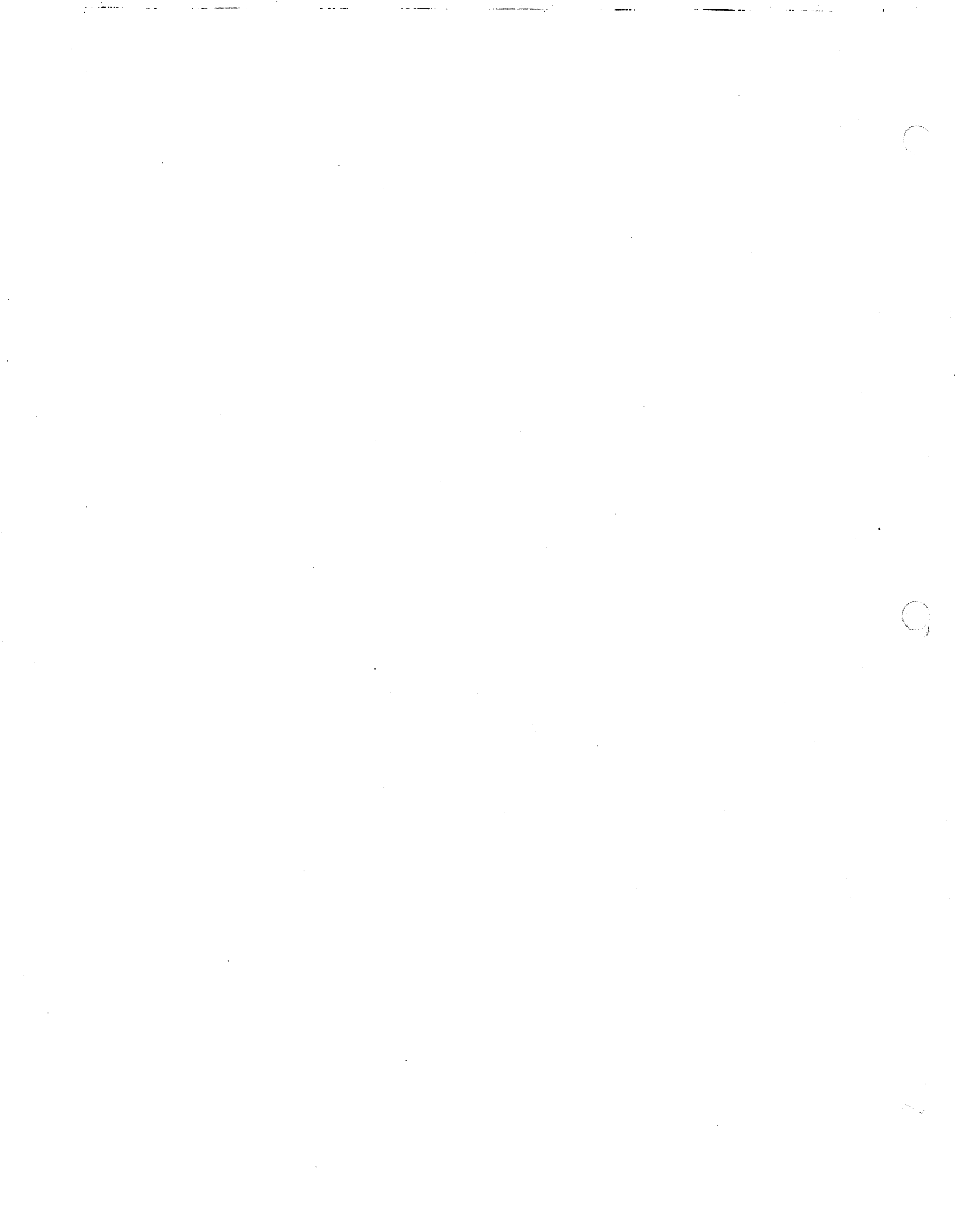
```

```
263 ;  
264 ;SWAP0: OR      CL,00000100B  
265 ;      JMP     TEST2  
266 ;  
267 ;SWAP2: OR      CL,00000001B  
268 ;      JMP     UPBITS  
269 ;  
270      END
```

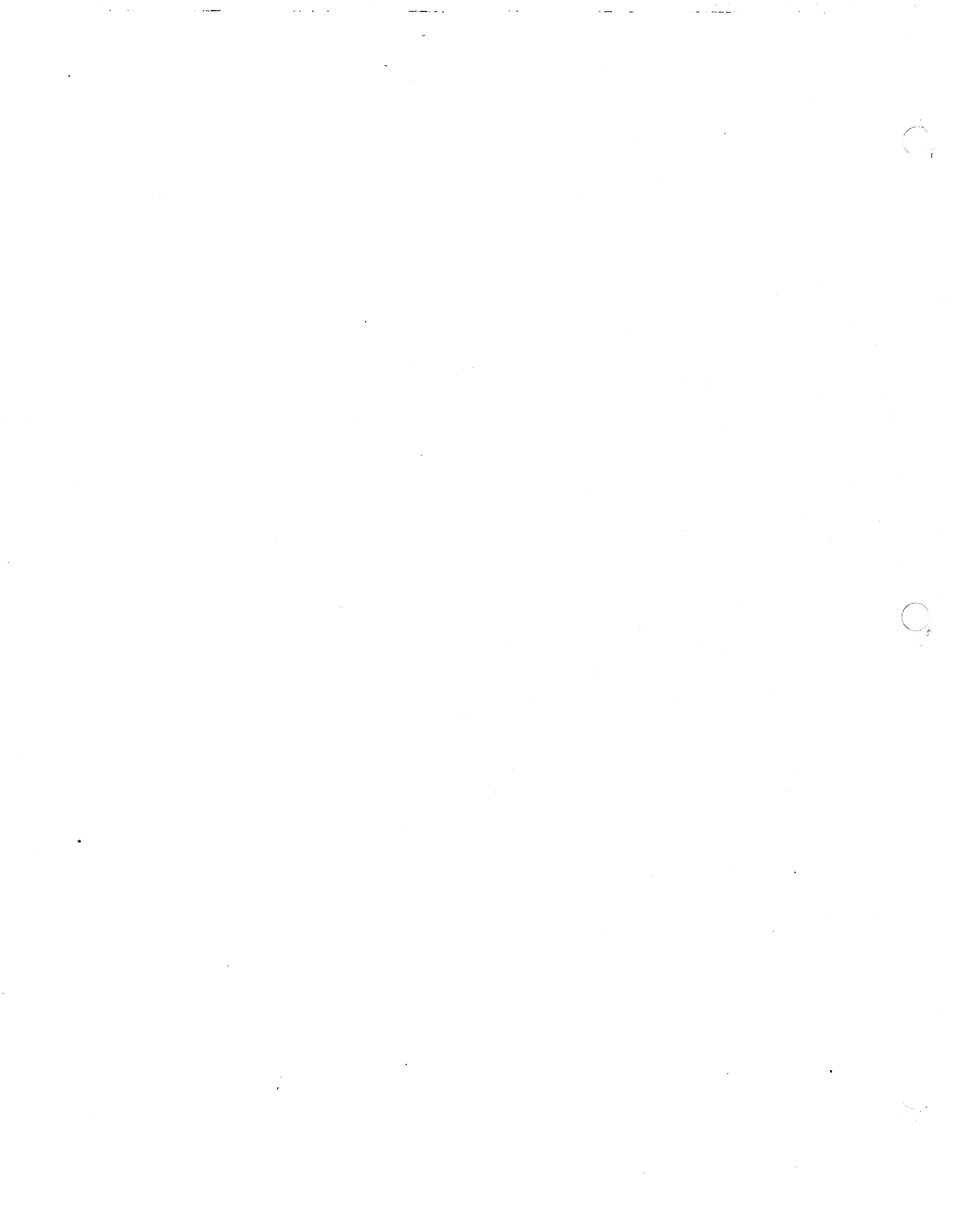
```
; CHANGE THIRD BIT(2) TO A ONE  
; GO TEST BIT THREE
```

```
; CHANGE FIRST BIT(0) TO A ONE  
; GO UPDATE THE NEW BITS
```

No errors detected



```
1 ;*****  
2 ; TITLE - SYSDRG - System organization module  
3 ; COMPUTER - BOBB Assembly Language (BSD Assembler)  
4 ; ABSTRACT - This module defines the 'layout' of the Pegasus system memory.  
5 ;*****  
6 NAME SYSDRG - System organization module  
7 SUBTTL  
9 ;*****  
10 ; PUBLIC DEFINITIONS  
11 ;*****  
12 ;  
13 PUBLIC BOOTLO  
14 PUBLIC BOOTMV  
15 PUBLIC BOOTSZ  
16 PUBLIC EPROM  
17 PUBLIC ROMF4  
18 PUBLIC ROMF6  
19 PUBLIC ROMFB  
20 PUBLIC ROMFA  
21 PUBLIC ROMSIZ  
22 PUBLIC SYSBROM  
23 ;  
24 ;*****  
25 ; LOCAL CONSTANTS  
26 ;*****  
27 ;  
28 BOOTSZ EQU 200H ; Max size of boot sector loaded at BOOTLO  
29 CRTMSZ EQU 1B20H ; Size of memory that Alpha board talks to  
30 ROMSIZ EQU 2000H ; ROM boundaries are BK  
31 ;  
32 ;*****  
33 ; SEGMENT DEFINITION  
34 ;*****  
35 ;  
36 ; ABSO is located at absolute memory address 0. It is used during system  
37 ; initialization and diagnostics to access system constants and interrupt  
38 ; vectors in low RAM (see VECTOR.EGU).  
39 ;  
40 SECT ABSO  
41 ASSUME CS:ABSO, DS:ABSO  
42 ;  
43 ORG OC000H ; BOOT SECTOR LOADED HERE  
44 ;  
45 BOOTMV LABEL BYTE ; This label used as ref to load boot sector  
46 ; (also as base for ROM init stack)  
47 BOOTLO PROC FAR ; DKBOOT jumps to here after boot is loaded  
48 DS BOOTSZ ; space  
49 ;  
50 ;  
51 ;-----  
52 ; ROMDAT is the dedicated RAM area allocated to the BK System ROM on the  
53 ; main board. The optional ROMs are each allocated a separate data area.
```



54 , There is a set of words in the interrupt vector area of memory (ABSO),
 55 , that contains the location and size of the blocks. Each ROM has its own
 56 , 2 words (see VECTOR.EGU). The location DSADDR contains a pointer to
 57 , ROMDAT, and the location DSSIZR contains the size (in bytes) of ROMDAT.
 58 , The other locations (DSADDX and DSSIZX) contain corresponding pointers
 59 , and sizes for the option ROMs.
 60 ,

61 , These values (except for DSADDR/DSSIZR) are initialized to zero during
 62 , System ROM powerup. Each optional ROM is responsible for allocating
 63 , its own data area when it is called from PUPTR1. This is accomplished by
 64 , looking at the DSSIZX locations prior to the current one, scanning
 65 , backwards until a non-zero DSSIZX is encountered. This value is adjusted
 66 , and added to the corresponding DSADDX to form the next contiguous segment
 67 , address and the current DSSIZX is filled in. The size must be a multiple
 68 , of 16, i.e. the low order 4 bits must be zero. This is for ease of
 69 , conversion to segment addresses for calculating total RAM data area size,
 70 , etc. Note that the ROM initialization routines are called in the order
 71 , 0,2,4,6,8 where the numbers correspond to the ROMs at offsets of 0000,
 72 , 2000, 4000, 6000, and 8000 from the beginning of ROMCOD (the System ROM
 73 , is at offset A000).
 74 ,

75 , The memory is allocated sequentially from DSADDR such that the data area
 76 , for the entire set of ROMs in ROMCOD (the aforementioned optional ROMs)
 77 , begins at DSADDR. The total size of the data area is determined by
 78 , adding all the DSSIZX values together, since each size has been rounded
 79 , up to the nearest segment. This implies that the values in the DSSIZX
 80 , locations for non-installed ROMs must be zero.
 81 ,

82 , Although ROMDAT is initially loaded here, the boot routine may change its
 83 , location to anywhere within the 1 Mbyte address range of the processor.
 84 , This is done by changing all the DSADDX locations to point to the new
 85 , locations (by adding/subtracting a constant) and then copying the data
 86 , from DSADDR to the new location. The length for the move is determined
 87 , as shown above, being the total size of allocated RAM area. Interrupts
 88 , should be disabled during this process.
 89 ,

90 , NOTE: Remember that the DSADDX values are segment values, while the
 91 , DSSIZX values are in bytes.

92 , SECT ROMDAT
 93 , ASSUME CS:ROMDAT

0000' INRM DT LABEL BYTE , ROMDAT data initially loaded here.

98 , -----
 99 , CRTDAT is the Alpha-board screen RAM.

100 , SECT CRTDAT
 101 , ASSUME CS:CRTDAT

-0000

102 , DS CRTMSZ

-1820

```

106
107
108
109
110
111
112
113
114
115
116
117
118
119
120
121
122
123
124
125
126
127
128
129
130
131
132
133
134
135
136
137
138
139
140
141
142
143
144
145
146
147
148
149
150

-0000
0000'
2000'
4000'
6000'
8000'
A000'

-0000
-0000
-0000
-0000
-0000
-0000

SECT ROMCOD
ASSUME CB:ROMCOD

ROMF4 LABEL BYTE ; ROM0 (no socket on main board; future option)
DB ROMBIZ

ROMF6 LABEL BYTE ; ROM2 (no socket on main board; future option)
DB ROMBIZ

ROMF8 LABEL BYTE ; ROM4 (no socket on main board; future option)
DB ROMBIZ

ROMFA LABEL BYTE ; ROM6 (no socket on main board; future option)
DB ROMBIZ

EPROM LABEL BYTE ; ROMB Starting loc of main board option ROM
DB ROMBIZ

SYBROM LABEL BYTE ; Starting loc of system ROM
DB ROMBIZ

RESET is located at absolute memory location OFFF0H. This is where the
processor starts after being reset (with CB = FFFFH, other seg-regs = 0)

SECT RESET
ASSUME CS:RESET

END

```

No errors detected

```
1 ;*****  
2 ; TITLE - TIMISR (SYSTEM TIMER INTERRUPT SERVICE ROUTINE)  
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE  
4 ; ABSTRACT - The system timer interrupt service routine logic is executed  
5 ; each time a clock tick occurs (25ms). Register DS is saved on the  
6 ; stack of the interrupted logic. The timer interrupt processor  
7 ; stack is then used to save registers that are commonly used by  
8 ; interrupt processors (AX,BX,and ES) and registers that are used  
9 ; locally.  
10 ;  
11 ; At 25ms intervals (each tick), the ROMTIM subroutine is called  
12 ; to perform timing services for the fired events defined in ROM.  
13 ; A tick counter is incremented and checked to see if 100ms has  
14 ; elapsed. If so, the time of day is updated by calling CLKSrv.  
15 ; If ZEX is present in the system, TIMISR does soft interrupts to  
16 ; ZEX logic to perform timing services for ZEX (dynamic) timing  
17 ; events and to do time slicing operations. If ZEX is not in  
18 ; the system, the soft interrupt logic simply does an IRET.  
19 ;  
20 ; On exit, this logic restores all registers used locally from the  
21 ; interrupt processor stack and jumps to the common interrupt exit  
22 ; logic, passing the location of the interrupted logic's stack in  
23 ; registers ES and BX.  
24 ;  
25 ; REGISTERS USED - none  
26 ;  
27 ; STACK USED - 8 bytes (user stack)  
28 ; 16 bytes (interrupt stack)  
29 ;  
30 ;*****  
31 NAME TIMISR  
32 SUBTTL  
33 ;*****  
34 ; PUBLIC DEFINITIONS  
35 ;  
36 ;*****  
37 PUBLIC TIMISR  
38  
39 ;*****  
40 ; EXTERNAL REFERENCES  
41 ;  
42 ;*****  
43  
44  
45  
46  
47  
48  
49  
50  
51  
52  
53  
=0000  
  
=0000  
SECT ROMCOD ;  
EXTERN CLKSrv:NEAR ; CLOCK SERVICES (CLKDSR)  
EXTERN ROMTIM:NEAR ; TIMING SERVICES (ROMTIM)  
  
SECT ROMDAT ;  
EXTERN DSKC M: BYTE ; MEMORY COPY OF TICK CTR LATCH (ROMDAT)  
EXTERN TIKCTR: BYTE ; TICK COUNTER (ROMDAT)  
EXTERN TIMISP: WORD ; TIMER INTERRUPT STACK SP (ROMDAT)  
EXTERN TMSFSV: WORD ; WORD TO SAVE USER SP (ROMDAT)  
EXTERN TMSSV: WORD ; WORD TO SAVE USER SS (ROMDAT)
```



```
54 ***** LOCAL CONSTANTS *****
55 ;
56 ; INCLUDE PEG:VECTOR.EGU
57 ; INCLUDE PEG:PORTADDR.EGU
58 ; INCLUDE PEG:LATCHES.EGU
59 ;
60 *****
61
62 LIMIT EQU 4 ; NUMBER OF TICKS TO MAKE TIMING
63 ; SERVICES, CLOCK SERVICES RESOLUTION
218
219 SUBTTL MAIN
220 *****
221 ; MODULE ENTRY POINT
222 ; *****
223
224 SECT ROMCOD
225 ASSUME CS:ROMCOD
226 ASSUME DS:ROMDAT
227
228 TIMISR PROC FAR ; ;
229 PUSH DS ; ;
230 INC BYTE PTR CS:INTCTR+CSWRAP ; ;
231 MOV DB,WORD PTR CS:DBADDR+CSWRAP ; ;
232 MOV TMSSV,SS ; ;
233 MOV TMSPSV,SP ; ;
234 MOV SP,DS ; ;
235 MOV SS,SP ; ;
236 MOV SP,OFFSET TIMISP ; ;
237 PUSH AX ; ;
238 PUSH BX ; ;
239 PUSH EB ; ;
240 MOV AL,DSKC_M ; ;
241 AND AL,NOT TMR1EN ; ;
242 OUT DSKC_P,AL ; ;
243 XOR AL,TMR1EN ; ;
244 OUT DSKC_P,AL ; ;
245 STI ; ;
246 PUSH DI ; ;
247 CALL ROMTIM ; ;
248 INC TIKCTR ; ;
249 CMP TIKCTR,LIMIT ; ;
250 NOTYET ; ;
251 JNZ NOTYET ; ;
252 MOV TIKCTR,0 ; ;
253 CALL CLKSrv ; ;
254 INT TIMINT ; ;
255 NOTYET: ; ;
256 INT SLCINT ; ;
257
258 POP DI ; ;
259 MOV DS,WORD PTR CS:DSADDR+CSWRAP ; ;
0000'
0001' 1E
0002' 2EFE 06 C19A
0003' 2E8E 1E C180
0004' 8C 16 0007"
0005' 89 26 0006"
0006' 8C DC
0007' 8E D4
0008' 8C 0003"
0009' 50
0010' 53
0011' 06
0012' A0 0003"
0013' 24 FD
0014' E6 00
0015' 34 02
0016' E6 00
0017' FB
0018' 57
0019' EB 0002"
0020' FE 06 0004"
0021' 80 3E 0004" 04
0022' 75 0A
0023' C6 06 0004" 00
0024' EB 0001"
0025' CD 5A
0026' CD 58
0027' 5F
0028' 5F
0029' 2E8E 1E C180
0030'
0031'
0032'
0033'
0034'
0035'
0036'
0037'
0038'
0039'
0040'
0041'
0042'
0043'
0044'
0045'
```

```
*****
***** LOCAL CONSTANTS *****
*****
***** INCLUDE PEG:VECTOR.EGU *****
***** INCLUDE PEG:PORTADDR.EGU *****
***** INCLUDE PEG:LATCHES.EGU *****
*****
*****
LIMIT EQU 4 ; NUMBER OF TICKS TO MAKE TIMING
; SERVICES, CLOCK SERVICES RESOLUTION
SUBTTL MAIN
*****
; MODULE ENTRY POINT
*****
*****
SECT ROMCOD
ASSUME CS:ROMCOD
ASSUME DS:ROMDAT
TIMISR PROC FAR ; ;
PUSH DS ; ;
INC BYTE PTR CS:INTCTR+CSWRAP ; ;
MOV DB,WORD PTR CS:DBADDR+CSWRAP ; ;
MOV TMSSV,SS ; ;
MOV TMSPSV,SP ; ;
MOV SP,DS ; ;
MOV SS,SP ; ;
MOV SP,OFFSET TIMISP ; ;
PUSH AX ; ;
PUSH BX ; ;
PUSH EB ; ;
MOV AL,DSKC_M ; ;
AND AL,NOT TMR1EN ; ;
OUT DSKC_P,AL ; ;
XOR AL,TMR1EN ; ;
OUT DSKC_P,AL ; ;
STI ; ;
PUSH DI ; ;
CALL ROMTIM ; ;
INC TIKCTR ; ;
CMP TIKCTR,LIMIT ; ;
NOTYET ; ;
JNZ NOTYET ; ;
MOV TIKCTR,0 ; ;
CALL CLKSrv ; ;
INT TIMINT ; ;
NOTYET: ; ;
INT SLCINT ; ;
POP DI ; ;
MOV DS,WORD PTR CS:DSADDR+CSWRAP ; ;
*****
***** SAVE CURRENT DS ON USER STACK *****
***** INCREMENT INTERRUPT COUNTER *****
***** DB = ROM DATA SEGMENT *****
***** SAVE CURRENT SS,SP *****
*****
***** STACK BEQ = ROM DATA SEGMENT *****
*****
***** SS,SP = INTERRUPT STACK PTR *****
***** SAVE COMMONLY USED REGISTERS *****
***** ON INTERRUPT STACK *****
*****
***** AL = COPY OF LATCH FROM MEMORY *****
***** CLEAR THE TIMER INTERRUPT *****
*****
***** ENABLE INTERRUPTS *****
***** SAVE REGISTERS USED LOCALLY *****
*****
***** HANDLE FIXED TIMING SERVICES *****
***** INCREMENT TICK COUNTER *****
***** REACH TIMING INTERVAL LIMIT ? *****
***** NO TIMING OR CLOCK SERVICES YET *****
***** RESET TICK COUNTER *****
***** UPDATE TIME OF DAY *****
***** HANDLE DYNAMIC TIMING SERVICES *****
*****
***** DO TIME-SLICING IF NEEDED *****
*****
***** RESTORE REGISTERS USED LOCALLY *****
***** DS = ROM DATA SEGMENT *****
```

004A' 8E 06 0007"	260	MOV	ES, TMSSV	,	ES - 88 OF INTERRUPTED LOGIC
004E' 8B 1E 0006"	261	MOV	BX, TMSF9V	,	BX - 8P OF INTERRUPTED LOGIC
0052' 2EFF 2E C164	262	JMP	DWORD PTR CS: XITVEC*4+CSWRAP,		TAKE COMMON INTERRUPT EXIT
	263		SUBTTL		
	264		END		

No errors detected

```
1 ;*****  
2 ; TITLE - TIMSUB (TIMER SUBROUTINES)  
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE  
4 ; ABSTRACT - This module contains subroutines that are called via a  
5 ; soft interrupt from the TIMING macro. The following subroutines  
6 ; are provided :  
7 ;  
8 ; TIMCAN - Cancel a timing event  
9 ; TIMRST - Restart a timing event  
10 ;  
11 ;*****  
12 ; NAME TIMSUB (TIMER SUBROUTINES)  
13 ; SUBTTL  
14 ;*****  
15 ; PUBLIC DEFINITIONS  
16 ;  
17 ;*****  
18 ; PUBLIC TIMCAN  
19 ; PUBLIC TIMRST  
20 ;*****  
21 ; LOCAL CONSTANTS  
22 ;  
23 ; INCLUDE PEG:TIMEVT.EGU  
24 ;*****  
25 ;  
45 SUBTTL TIMCAN (CANCEL EVENT SUBROUTINE)  
46 ;  
47 SECT ROMCOD  
48 ASSUME CS:ROMCOD  
49 ;*****  
50 ;  
51 ; TITLE - TIMCAN (TIMER CANCEL EVENT SUBROUTINE)  
52 ; ABSTRACT - This subroutine allows the caller to make an event  
53 ; inactive. The event status bit is reset to indicate that  
54 ; the event is now inactive. Since only interrupt-oriented  
55 ; events are supported at this time, the event acknowledgement  
56 ; flag is not checked.  
57 ;  
58 ; INPUT - DS,DI = EVENT ADDRESS  
59 ;  
60 ; OUTPUT - Event is now inactive  
61 ;  
62 ; REGISTERS USED - none  
63 ;  
64 ; STACK USED - 6 BYTES  
65 ;  
66 ;*****  
67 ;  
68 TIMCAN PROC FAR '','',' DB,DI = EVENT ADDRESS  
69 AND BYTE PTR IEVFLG(DI),NOT(1 SHL EVTSTA);; RESET STATUS BIT  
70 IRET '','',' ** INTERRUPT RETURN **  
71  
72 SUBTTL TIMRST (RESTART EVENT SUBROUTINE)
```

-0000

0000' 80 65 03 7F '
0004' CF

```

73 ; *****
74 ;
75 ; TITLE - TIMRST (TIMER RESTART EVENT SUBROUTINE)
76 ; ABSTRACT - This subroutine allows the caller to restart a timing event.
77 ; The event status bit is reset to indicate that the event is now
78 ; inactive. Since only interrupt-oriented events are supported at
79 ; this time, the event acknowledgement flag is not checked.
80 ;
81 ; INPUT - DS,DI = EVENT ADDRESS
82 ; AX = INITIAL COUNTER VALUE
83 ;
84 ; OUTPUT - Event restarted
85 ;
86 ; REGISTERS USED - none
87 ;
88 ; STACK USED - 6 BYTES
89 ;
90 ; *****
91 ;
92 TIMRST PROC FAR
93 MOV IEVCTR(DI),AX
94 OR BYTE PTR IEVFLG(DI),1 SHL EVTSTA
95 IRET
96 SUBTTL
97 END
98
99

```

0005' 89 45 04
 0008' 80 4D 03 80
 000C' CF

No errors detected


```
1 *****  
2 ; TITLE - TIMTST - timers power up diagnostics.  
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE  
4 ; ABSTRACT - This module contains the power up diagnostics that test the  
5 ; three timers present in the PEGASUS main board. It is assumed that  
6 ; the ability to generate interrupts has been previously tested.  
7 ; This test emphasizes the testing of the timer accuracy in various  
8 ; timer intervals.  
9 *****  
10 NAME TIMTST - PEGASUS power up timer test  
11 SUBTTL  
13 *****  
14 ; EXTERNAL REFERENCES  
15 *****  
16 SECT ROMDAT  
17 EXTERN DSKC_M:BYTE ; DISK SELECT LATCH MEMORY IMAGE. (ROMDAT)  
18 EXTERN PRCO_M:BYTE ; PRINTER OUTPUT LATCH MEMORY IMAGE. (ROMDAT)  
19 SECT ROMCOD  
20 ASSUME CS:ROMCOD  
21 EXTERN DSPDIE:NEAR ; DISPLAY FATAL ERROR and DIE. (OUTPUT)  
22 EXTERN VECINI:NEAR ; ENTRY TO VECTOR INITIALIZATION. (VECINI)  
23 EXTERN DECLD:NEAR ; DECREMENT LED SUBROUTINE. (DECLD)  
24 EXTERN TOERR:ABS ; TIMER 0 ERROR CODE. (TSERR)  
25 EXTERN TIERR:ABS ; TIMER 1 ERROR CODE. (TIERR)  
26 EXTERN TZERR:ABS ; TIMER 2 ERROR CODE. (TZERR)  
27 EXTERN LED100:ABS ; LED ERROR CODE. (ROMERR)  
28 *****  
29 ; PUBLIC DEFINITIONS  
30 ;  
31 PUBLIC TIMTST ; ENTRY TO TIMER POWER UP DIAGNOSTICS.  
32 ;  
33 ; LOCAL CONSTANTS  
34 ;  
35 ; INCLUDE PEG:HWWARE.EQU  
36 ; INCLUDE PEG:VECTOR.EQU  
37 ;  
316 *****  
317 ; BASE PAGE LOCATIONS  
318 *****  
319 ;  
320 SECT ROMCOD  
321 ASSUME CS:ROMCOD, DS:ROMDAT, ES:ABSO  
322 ;  
323 ; Table of test cases.  
324 ;  
325 TMTTAB LABEL WORD ; Table of cases tested.  
326 ;  
327 TCMD EQU 0 ; OFFSET TO MODE BYTE  
328 TCLAT EQU 1 ; OFFSET TO LATCH BYTE  
329 TCERR EQU 2 ; OFFSET TO ERROR CODE  
330 TCPORT EQU 3 ; PORT ADDRESS  
331 TCPROG EQU 5 ; OFFSET TO PROGRAM VALUE  
=0000  
=0000  
=0000  
0000'  
=0000  
=0001  
=0002  
=0003  
=0005
```



```

-0007
-0009
-000B
332 TCCHK EQU 7 ; ; OFFSET TO CHECK VALUE
333 TCLIM EQU 9 ; ; OFFSET TO LIMIT READ.
334 TMTBIZ EQU 11
335 ;
336 ; (TCPRD) PROG = PROGRAMMED TIMER COUNT
337 ; (TCLIM) TIMER READ VALUE = (PROG AND OFFEOH) - 20H
338 ; (TCCHEK) RUN TIME = PROG + (PROG - READ + 10) / 2 * CLOCK RATE
339 ; LOOP COUNT = RUN TIME / LOOP TIME
340 ; LOOP TIME = 21 * 200nsec (LOOP $ time)
341 ;
342 ; TC_SC0+TC_WRD+TC_MD3; Timer 0 mode 3
343 ; TC_SC0+TC_LAT ; Latch timer 0
344 ; TOERR ; Timer 0 fail code
345 ; TIMERO ; Port address for timer 0
346 ; OFFFFH ; Timer count
347 ; 030C6H ; Loop count = 52.428 ms.
348 ; OFFCOH ; Timer read value
349 ;
350 ; TC_SC0+TC_WRD+TC_MD3; Timer 0 mode 3
351 ; TC_SC0+TC_LAT ; Latch timer 0
352 ; TOERR ; Timer 0 fail code
353 ; TIMERO ; Port address for timer 0
354 ; 030D4H ; Timer count
355 ; 094FH ; Loop count = 10 ms.
356 ; 030A0H ; Timer read value
357 ;
358 ; TC_SC2+TC_WRD+TC_MD3; Timer 2 mode 3
359 ; TC_SC2+TC_LAT ; Latch timer 2
360 ; T2ERR ; Timer 2 fail code
361 ; TIMER2 ; Port address for timer 2
362 ; OFFFFH ; Timer count
363 ; 061BDH ; Loop count = 104.8 ms.
364 ; OFFCOH ; Timer read value
365 ;
366 ; TC_SC2+TC_WRD+TC_MD3; Timer 2 mode 3
367 ; TC_SC2+TC_LAT ; Latch timer 2
368 ; T2ERR ; Timer 2 fail code
369 ; TIMER2 ; Port address for timer 2
370 ; 0186AH ; Timer count
371 ; 0951H ; Loop count = 10.0 ms.
372 ; 01B40H ; Timer read value
373 ;
374 ; TC_SC1+TC_WRD+TC_MD3; Timer 1 mode 3
375 ; TC_SC1+TC_LAT ; Latch timer 1
376 ; TIERR ; Timer 1 fail code
377 ; TIMER1 ; Port address for timer 1
378 ; OFFFFH ; Timer count
379 ; 061BDH ; Loop count = 104.8 ms.
380 ; OFFCOH ; Timer read value
381 ;
382 ; TC_SC1+TC_WRD+TC_MD3; Timer 1 mode 3
383 ; TC_SC1+TC_LAT ; Latch timer 1

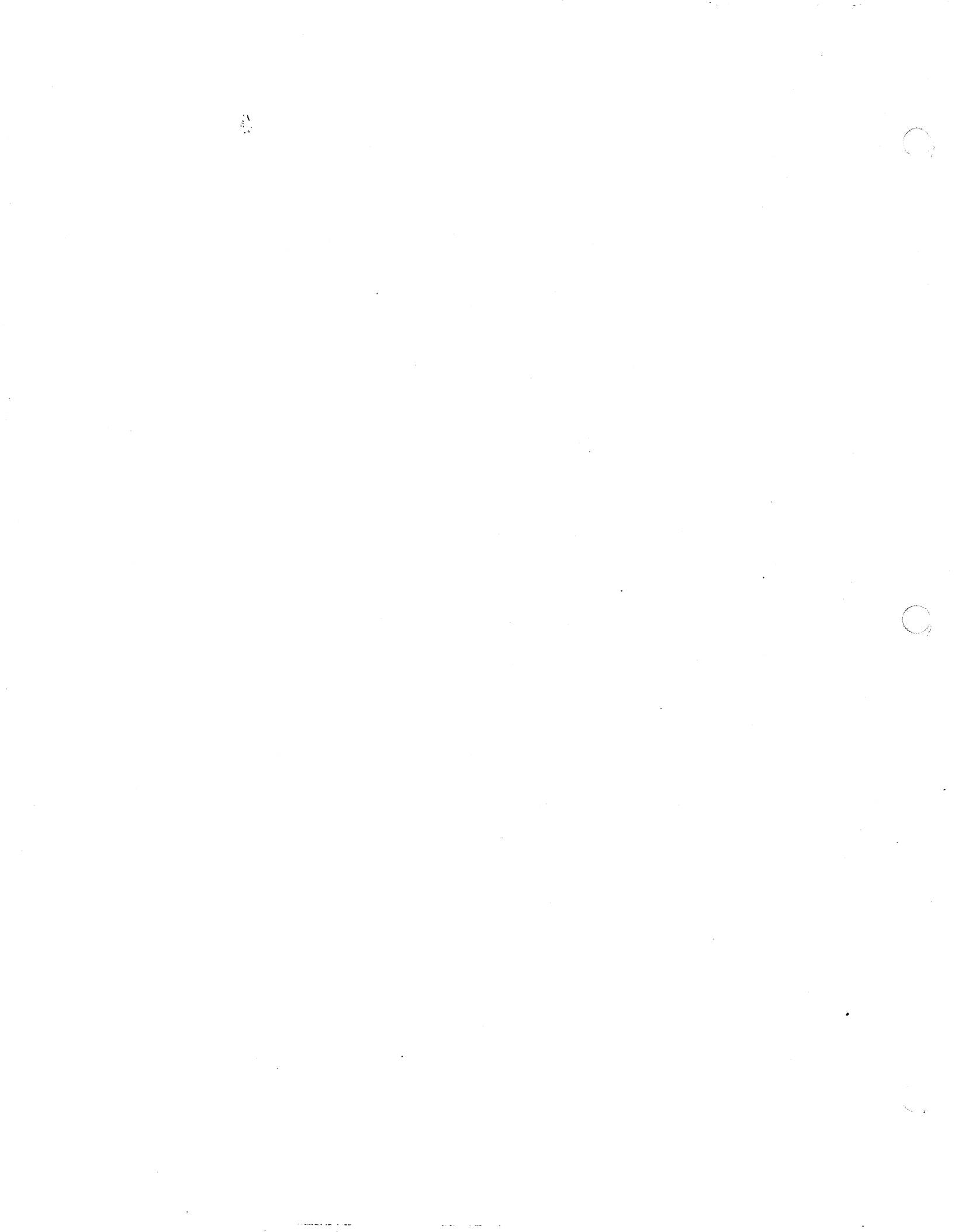
```

```

0039' 07*
003A' 0015
003C' 3D09
003E' 1744
0040' 3CE0
0042'

384 DB TIERR ;
385 DW TIMER1 ; Timer 1 fail code
386 DW 03D09H ; Port address for timer 1
387 DW 01744H ; Timer count
388 DW 03CE0H ; Loop count = 25.0 ms.
389 ; Timer read value
;
390 TMTEND LABEL BYTE
391 ;
392 ;
393 ;
394 ; This is the entry to the timer diagnostics. The speaker time is tested
395 ; first, followed by the test of the interrupt driven timers. SI is used as
396 ; a pointer to the table of test cases.
397 ; DL contains any errors found from the interrupt test.
398 ;
399 ; Disable timer interrupts.
400 ;
401 TIMTST: MOV BL, DL ;;; Get DR'D error bits into BL.
402 MOV AL, DSKC_M ;;; Disable interrupts, enable speaker
403 AND AL, (NOT TMR1EN) AND (NOT TMR2EN) ;;;
404 OR AL, SPKREN ;;;
405 MOV DSKC_M, AL ;;; Get latch memory image.
406 OUT DSKC_P, AL ;;; Write image to latch.
407 CALL TBTMR ;;; Test timers.
408 MOV AL, DSKC_M ;;; Disable the speaker
409 AND AL, (NOT SPKREN) ;;;
410 MOV DSKC_M, AL ;;; Save latch memory image
411 OUT DSKC_P, AL ;;; Write image to latch
412 OR BL, BL ;;;: Any errors?
413 JNZ INFAIL ;;; Y: go report them.
414 CALL DECLED ;;; N: decrement LED count.
415 JMP VECINI ;;; Go execute floppy test.
416 ;
417 ; This is the timer failure logic. It places a failure code on the
418 ; parallel port for factory diagnostics purposes. This code indicates
419 ; that a failure of one of the timers has occurred.
420 ;
421 INFAIL: MOV DL, BL ;;; Put error code into DL.
422 MOV AL, LED100 ;;; AL = LED FAIL CODE.
423 JMP DSPDIE ;;; Fatal error stops the CPU.
424 ;
425 ;
426 ; This PROC is used to test all the timers.
427 ; The method for verifying proper operation of these timers is by
428 ; tracking the elapsed time with a software loop/counter and observing
429 ; the timer internal counter at the proper time to see if it matches its
430 ; expected value.
431 ;
432 TBTMR PROC NEAR
433 MOV SI, OFFSET TMTTAB ;;; Get table of test cases.
434 ;
435 TOLDOP: MOV CX, CS:TCCHK[SI] ;;; CX = software counter value.

```

```
1 *****  
2 ; TITLE - TBTERR (POWERUP TEST ERROR CODES)  
3 ; COMPUTER - BOBB ASSEMBLY LANGUAGE  
4 ; ABSTRACT - THIS MODULE CONSISTS OF THE VARIOUS ERROR CODES THAT THE  
5 ; DIFFERENT POWERUP TESTS MIGHT PUT TO THE PARALLEL PRINTER  
6 ; PORT TO CONVEY ADDITIONAL INFORMATION CONCERNING THE NATURE  
7 ; OF THE LED INDICATED FAILURE.  
8 ;  
9 ; REGISTERS USED - NONE  
10 ;  
11 ; STACK USED - NONE  
12 ;  
13 ; *****  
14 ; NAME TBTERR  
15 ; SUBTTL  
16 ; *****  
17 ; *****  
18 ; PUBLIC DEFINITIONS  
19 ; *****  
20 ;  
21 PUBLIC ATMERR  
22 PUBLIC ATTERR  
23 PUBLIC CINTER  
24 PUBLIC CURERR  
25 PUBLIC CRAMER  
26 PUBLIC E_EVEN  
27 PUBLIC E_ODD  
28 PUBLIC E_REG  
29 PUBLIC E_CMD  
30 PUBLIC VIDERR  
31 PUBLIC VBLERR  
32 PUBLIC TOERR  
33 PUBLIC TIERR  
34 PUBLIC TZERR  
35 PUBLIC ICNERR  
36 PUBLIC IVIERR  
37 PUBLIC TIMERR  
38 PUBLIC NMIERR  
39 PUBLIC KEYERR  
40 PUBLIC FLIERR  
41  
42 SUBTTL MAIN  
43 *****  
44 ; MODULE ENTRY POINT  
45 ; *****  
46 ; INTERRUPT CTRL ERROR CODES FOR PARALLEL PRINTER PORT  
47 ;  
48 ICNERR EQU 00H ; CONTROLLER ERROR (IMR).  
49 IVIERR EQU 01H ; INVALID INTERRUPT ERROR.  
50 NMIERR EQU 02H ; NMI ERROR FAILURE.  
51 TIMERR EQU 04H ; TIMER INTERRUPT FAILURE.  
52 FLIERR EQU 08H ; FLOPPY INTERRUPT FAILURE.  
53 KEYERR EQU 10H ; KEYBOARD INTERRUPT ERROR.  
-0000  
-0001  
-0002  
-0004  
-0008  
-0010
```

```
54 ;  
55 ; *****  
56 ; TIMER ERROR CODES FOR PARALLEL PRINTER PORT *****  
57 ;  
58 TOERR EQU 20H ; TIMER 0 ERROR.  
59 TIERR EQU 40H ; TIMER 1 ERROR.  
60 TZERR EQU 80H ; TIMER 2 ERROR.  
61 ;  
62 ; *****  
63 ; DISK CTRL ERROR CODES FOR PARALLEL PRINTER PORT *****  
64 ;  
65 E_EVEN EQU 01H ; Even-order bit in secbuf bad error  
66 E_ODD EQU 02H ; Odd-order bit in secbuf bad error  
67 E_REQ EQU 04H ; FDC register bad error  
68 E_CMD EQU 08H ; Command execution error  
69 ;  
70 ; *****  
71 ; CRT CTRL ERROR CODES FOR PARALLEL PRINTER PORT *****  
72 ;  
73 ATMERR EQU 01H ; Attribute memory failure.  
74 ATTERR EQU 02H ; Attribute latch failure.  
75 CURERR EQU 04H ; Controller failure (cursor).  
76 CRAMER EQU 08H ; Character memory failure.  
77 VIDERR EQU 10H ; Video output failure.  
78 CINTER EQU 20H ; CRT interrupt failure.  
79 VBLERR EQU 40H ; Vertical blank interrupt failure.  
80 ; *****  
81 ; END
```

No errors detected


```

1 *****
2 ; TITLE - VECINI - ABSO initialization; vectors, system info
3 ; COMPUTER - BOBB Assembly Language
4 ; ABSTRACT - This routine is responsible for initializing the interrupt
5 ; vectors, BIOS interface vectors, and various system information
6 ; contained in the first 1K of RAM. Note that all unused interrupt
7 ; vectors are set to WILD$.
8 ;
9 ; *** BE WARNED: THIS MODULE IS AFFECTED BY ANY CHANGES IN THE
10 ; FILE 'VECTOR.EQU' DUE TO THE FACT THAT THE VECTOR INITIALIZATION
11 ; TABLES MUST MATCH THE VECTOR EQUATES.
12 ;
13 *****
14 NAME VECINI - ABSO initialization; vectors, system info
15 SUBTTL
16 *****
17 PUBLIC DEFINITIONS
18 *****
19 *****
20 ;
21 PUBLIC FILVEC
22 PUBLIC VECINI
23 PUBLIC VECTBO
24 PUBLIC VTOLEN
25 ;
26 *****
27 ; EXTERNAL REFERENCES
28 *****
29 ;
30 EXTRN BEEPID:FAR ; System beeper I/O handler (BELDSR)
31 EXTRN CLK_ID:FAR ; Time-of-day clock I/O handler (CLKDSR)
32 EXTRN CFG_ID:FAR ; System Configuration function (CONFIG)
33 EXTRN CRT_ID:FAR ; Screen I/O handler (CRTDSR)
34 EXTRN DSK_ID:FAR ; Floppy disk I/O handler (DSKDSR)
35 EXTRN DSKISR:FAR ; Disk interrupt service routine (DSKIDSR)
36 EXTRN DSKTST:NEAR ; Entry point for the FDC test routine (DSKTBT)
37 EXTRN FATAL:FAR ; Fatal error handler (OUTPUT)
38 EXTRN INTRET:FAR ; Common IRET instruction (for dummies)(INTXIT)
39 EXTRN INTXIT:FAR ; Common interrupt exit logic (INTXIT)
40 EXTRN KEY_ID:FAR ; Keyboard I/O handler (KEYDSR)
41 EXTRN KEYISR:FAR ; Keyboard interrupt service routine (KEYDSR)
42 EXTRN KPAUSE:FAR ; Keyboard default pause key handler (KEYDSR)
43 EXTRN PUPNMI:FAR ; Powerup NMI interrupt service routine (OUTPUT)
44 EXTRN PRT_ID:FAR ; Printer I/O handler (PRTDSR)
45 EXTRN PWRUP:FAR ; Powerup reset starting location (RESET)
46 EXTRN TIMCAN:FAR ; Timing services - cancel event (TIMSUB)
47 EXTRN TIMISR:FAR ; Timer interrupt service routine (TIMISR)
48 EXTRN TIMRST:FAR ; Timing services - restart event (TIMSUB)
49 EXTRN WILD$:FAR ; Unexpected (Wild) interrupt handler (OUTPUT)
50 ;
51 SECT ROMDAT ; Size of ROMDAT data area (ROMDAT)
52 EXTRN RMDT$Z:ABS
53 ;
    
```



```

54 ***** LOCAL CONSTANTS *****
55 ;
56 ; INCLUDE PEO:VECTOR.EGU
57 ; *****
58 ;
152 ;
153 ;
154 ;
155 TP5VEC EQU (OVFINI*4)+4
156 TP6VEC EQU TP5VEC+4
157 ;
158 *****
159 ; CODE SEGMENT DEFINITION *****
160 ; *****
161 ;
162 SECT ROMCOD
163 ASSUME CS:ROMCOD
164 ;
165 *****
166 ; LOCAL DATA AREA *****
167 ; *****
168 ;
169 ; These sets of vectors are moved into the 8088 interrupt area
170 ; (Table VECTBO is moved by the routine RAMINI)
171 ;
172 VECTBO LABEL WORD ; Table 0 - Processor traps
173 ;
174 DD WILD$$ ; Interrupt 0 - Divide-by-zero trap
175 DD WILD$$ ; Interrupt 1 - Single-step trap
176 DD PUPNMI ; Interrupt 2 - Non-maskable interrupt
177 DD WILD$$ ; Interrupt 3 - Break (single-byte) soft intr
178 DD WILD$$ ; Interrupt 4 - Overflow trap
179 ;
180 VTOEND LABEL WORD
181 VTOLEN EQU (VTOEND-VECTBO)/2
182 ;
183 VECTB1 LABEL WORD ; Table 1 - Pegasus system vectors
184 ;
185 ; Hardware interrupts:
186 ;
187 ;
188 DD WILD$$ ; Interrupt 40H - IRO (unused)
189 DD WILD$$ ; Interrupt 41H - IRI (unused)
190 DD WILD$$ ; Interrupt 42H - IR2 (unused)
191 DD TIMISR ; Interrupt 43H - IR3 (Timer 1)
192 DD WILD$$ ; Interrupt 44H - IR4 (unused)
193 DD WILD$$ ; Interrupt 45H - IR5 (Parallel port ACK)
194 DD DSKISR ; Interrupt 46H - IR6 (Disk controller)
195 DD KEYISR ; Interrupt 47H - IR7 (Keyboard USART)
196 ;
197 ; BIOS interface vectors
198 ;
0000'
0004' 0014" 0000"
0008' 0014" 0000"
000C' 000E" 0000"
0010' 0014" 0000"
0014'
-000A
0014'
0014' 0014" 0000"
0018' 0014" 0000"
001C' 0014" 0000"
0020' 0012" 0000"
0024' 0014" 0000"
0028' 0014" 0000"
002C' 0006" 0000"
0030' 000C" 0000"
    
```

```

0034' 0001" 0000"
003B' 0004" 0000"
003C' 000B" 0000"
0040' 000F" 0000"
0044' 0014" 0000"
004B' 0003" 0000"
004C' 0002" 0000"
0050' 0003" 0000"

0054' 000B" 0000"
005B' 0013" 0000"
005C' 0011" 0000"
0060' 0014" 0000"
0064' 0014" 0000"
006B' 0014" 0000"
006C' 0014" 0000"
0070' 0009" 0000"

0074' 0009" 0000"
007B' 000A" 0000"
007C' 0009" 0000"
0080' 0009" 0000"
0084' 000D" 0000"
008B' 0009" 0000"
008C' 0009" 0000"
0090' 0009" 0000"

0094' 0000'
0096' 0015#
009B' 0000 000C
009C' 0000 0000
00A0' 0000 0000
00A4' 0000 0000
00AB' 0000 0000
G0AC' 0000
00AE' 00
00AF' 00

00B0' 0000
00B2' 0000
00B4'

-0050

DD BEEPIO ; Interrupt 4BH - System beeper I/O
DD CRT_IO ; Interrupt 49H - Screen I/O
DD KEY_IO ; Interrupt 4AH - Keyboard I/O
DD PRT_IO ; Interrupt 4BH - Printer port I/O
DD WILD$$ ; Interrupt 4CH - *** UNUSED ***
DD DSK_IO ; Interrupt 4DH - Floppy disk interface
DD CLK_IO ; Interrupt 4EH - Time-of-day clock I/O
DD CFO_IO ; Interrupt 4FH - System configuration

DD FATAL ; Interrupt 50H - Fatal error trap
DD TIMRST ; Interrupt 51H - Restart timing event
DD TIMCAN ; Interrupt 52H - Cancel timing event
DD WILD$$ ; Interrupt 53H - (unused)
DD WILD$$ ; Interrupt 54H - (unused)
DD WILD$$ ; Interrupt 55H - (unused)
DD WILD$$ ; Interrupt 56H - (unused)
DD INTRET ; Interrupt 57H - CRT character mapping vector

DD INTRET ; Interrupt 5BH - Time slicing (every 25 msec)
DD INTXIT ; Interrupt 59H - Common interrupt exit routine
DD INTRET ; Interrupt 5AH - Dyn. timing services (100 ms)
DD INTRET ; Interrupt 5BH - Keyboard mapping vector
DD KPAUSE ; Interrupt 5CH - Keyboard pause key vector
DD INTRET ; Interrupt 5DH - Keyboard break key vector
DD INTRET ; Interrupt 5EH - Keyboard print screen vector
DD INTRET ; Interrupt 5FH - Keyboard queuing vector

DW ROMDAT ; DSADDR - System ROM DB segment address
DW RMDT5Z ; DSIZR - System ROM DB size in bytes
DD 00000000 ; DSADD0/DSIZ0 - Option ROM 0 DB info
DD 00000000 ; DSADD2/DSIZ2 - Option ROM 2 DB info
DD 00000000 ; DSADD4/DSIZ4 - Option ROM 4 DB info
DD 00000000 ; DSADD6/DSIZ6 - Option ROM 6 DB info
DD 00000000 ; DSADD8/DSIZ8 - Option ROM 8 DB info
DW 0000 ; MEMSIZ
DB 00 ; INTCR
DB 00 ; DRVBYT

DW 0000 ; SYBC01
DW 0000 ; SYBC02

VTIEND LABEL WORD
VTILEN EQU (VTIEND-VECTB1)/2

; *****
; MODULE ENTRY POINT
; *****
; VECINI - Initialize the interrupt vectors and system info
; INPUT: (MEMSIZ) & (DSADDR)
; OUTPUT: (none) Vectors and sys info initialized
; (MEMSIZ) & (DSADDR) unchanged

```

```

251      USED:
252      STACK:
253      ASSUME DS:ROMCOD, ES:AB80
254
255      VECINI PROC NEAR
256          CLI
257          PUSH DS
258          POP AX,AX
259          XOR ES,AX
260          MOV CX,OFFSET WILD$$
261          CALL FILVEC
262
263          ; Protect this
264          ;;; DB = ROMCOD
265          ;;; ES = AB80
266          ;;; Set all vectors to WILD$$
267          ;;; Do it
268          ;;; EB=AB80, DS=ROMCOD ...
269          ;;; Source for move in DS
270          ;;; Destination for move in ES
271          ;;; Length of move (in words)
272
273          MOV EB,OFFSET VECTB1
274          MOV DI,OFFSET IROINT*4
275          MOV CX,OFFSET VTILEN
276          REP
277          MOV WORD PTR [DI],WORD PTR [SI] ;;; Do it
278
279          MOV EB,WORD PTR MEMSIZ,BP ;;; Restore MEMSIZ
280          MOV DS,DX ;;; Set the DS back to normal
281          JMP DSKTBT ;;; Go to next
282
283          ; FILVEC - Fill 808B vector space with 'constant' vector. (Fills entire
284          ; vector space except for first 5 (808B) vectors, DSADDR, & MEMSIZ
285          ; which are init'ed by RAMINI.)
286
287          INPUT: CX = The vector's offset (segment assumed to be ROMCOD)
288          ;;; Interrupts disabled
289          OUTPUT: (none) vector space initialized
290          USED: CX,DI,SI
291          STACK: 2 bytes
292          ASSUME DS:AB80, ES:AB80
293
294      FILVEC PROC NEAR
295          CLD
296          PUSH DS
297          XOR DI,DI
298          MOV DS,DI
299          MOV ES,DI
300          MOV DX,ROMDAT
301          BP,WORD PTR MEMSIZ
302          MOV WORD PTR TP5VEC,CX
303          MOV WORD PTR TP5VEC+2,CS
304          SI,OFFSET TP5VEC
305          DI,OFFSET TP6VEC
306          CX,2*(251-1)
307          REP
308          MOV WORD PTR [DI],WORD PTR [SI]
309
310          ;;; Forward strings
311          ;;; Save caller's segs
312          ;;;
313          ;;; Set up segment registers
314          ;;; Save DSADDR
315          ;;; Save MEMSIZ
316          ;;; Set initial offset
317          ;;; Set initial segment
318          ;;; Copy vector from 1st location
319          ;;; ... to next location
320          ;;; All 251 (DWORD) locations
321          ;;; a word at a time
322
323      END
    
```

00F9'	89 16 0180	303	MOV	WORD PTR DBADDR, DX	;;;	Restore DBADDR
00FD'	89 2E 0198	304	MOV	WORD PTR MEMBIZ, BP	;;;	Restore MEMBIZ
0101'	07	305	POP	EB	;;;	Restore seg regs
0102'	1F	306	POP	DS	;;;	
0103'	C3	307	RET		;;;	*** RETURN ***
		308				
		309	END			

No errors detected